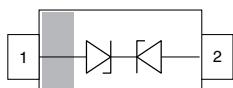


## Low Capacitance, Single-Line ESD-Protection Diode in SOD-323



20503



22756 SOD-323

### MARKING (example only)



XYZ = type code (see table below)

bar = pin 1

### FEATURES

- For LIN-Bus applications
- Small SOD-323 package
- AEC-Q101 qualified
- 1-line ESD-protection
- Working range:  $\pm 26.5$  V
- Low leakage current  $I_R < 0.05$   $\mu$ A
- Low load capacitance  $C_D < 16$  pF
- ESD-protection acc. IEC 61000-4-2  
 $\pm 30$  kV contact discharge  
 $\pm 30$  kV air discharge
- e3 - pins plated with tin (Sn)
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
COMPLIANT

### ORDERING INFORMATION

PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE			PACKAGING CODE		ORDERING CODE (EXAMPLE)	
	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	
		STANDARD	GREEN		15K/BOX = MOQ	10K/BOX = MOQ	
VLIN2626-02G	-	E	-	3	-08	-	VLIN2626-02G-E3-08
VLIN2626-02G	H	E	-	3	-08	-	VLIN2626-02GHE3-08
VLIN2626-02G	-	E	-	3	-	-18	VLIN2626-02G-E3-18
VLIN2626-02G	H	E	-	3	-	-18	VLIN2626-02GHE3-18

### PACKAGE DATA

DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VLIN2626-02G	SOD-323	262	4.30 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	$T_A = 25$ °C; acc. IEC 61000-4-5; $t_p = 8/20$ $\mu$ s; single shot	$I_{PPM}$	4	A
Peak pulse power	$T_A = 25$ °C; acc. IEC 61000-4-5; $t_p = 8/20$ $\mu$ s; single shot	$P_{PP}$	200	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses; $T_A = 25$ °C	$V_{ESD}$	$\pm 30$	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses; $T_A = 25$ °C		$\pm 30$	kV
Operating temperature	Junction temperature	$T_J$	-55 to +150	°C
Storage temperature		$T_{STG}$	-55 to +150	°C

**ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25^\circ C$ , unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	26.5	V
Reverse voltage	At $I_R = 0.05 \mu A$	$V_R$	26.5	-	-	V
Reverse current	At $V_{RWM} = 26.5 V$	$I_R$	-	-	0.05	$\mu A$
Reverse breakdown voltage	At $I_R = 1 mA$	$V_{BR}$	28	30	32	V
Reverse clamping voltage	At $I_{PP} = 1 A$ ; $t_p = 8/20 \mu s$	$V_C$	-	32	40	V
	At $I_{PP} = I_{PPM} = 4 A$ ; $t_p = 8/20 \mu s$	$V_C$	-	39	50	V
Capacitance	At $V_R = 0 V$ , $f = 1 MHz$	$C_D$	-	13.5	16	pF

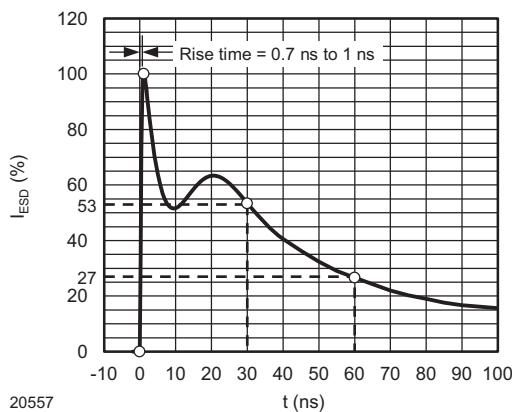
**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25^\circ C$ , unless otherwise specified)


Fig. 1 - ESD Discharge Current Wave Form  
acc. IEC 61000-4-2 (330  $\Omega$  / 150 pF)

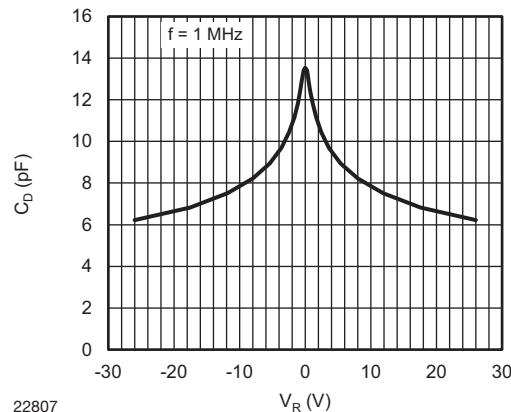


Fig. 3 - Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$

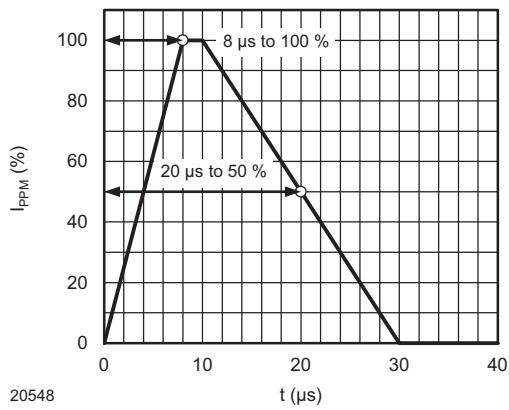


Fig. 2 - 8/20  $\mu s$  Peak Pulse Current Wave Form  
acc. IEC 61000-4-5

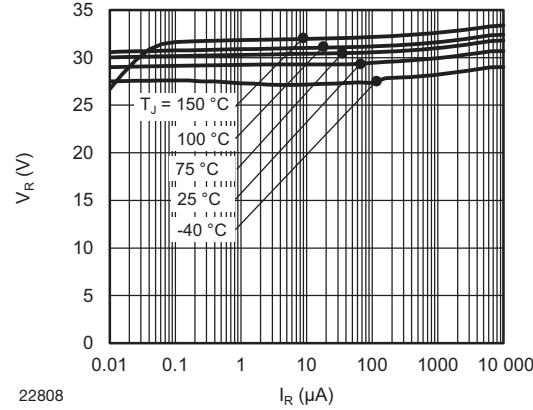


Fig. 4 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$

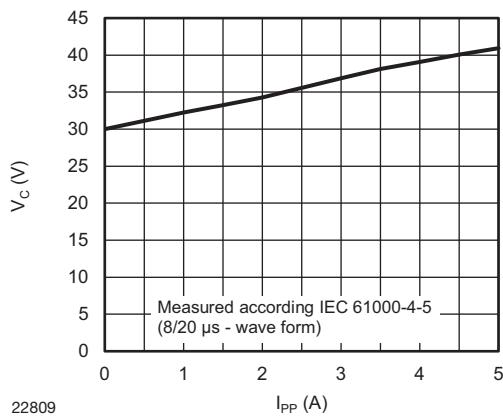


Fig. 5 - Typical Peak Clamping Voltage  $V_C$  vs.  
Peak Pulse Current  $I_{PP}$

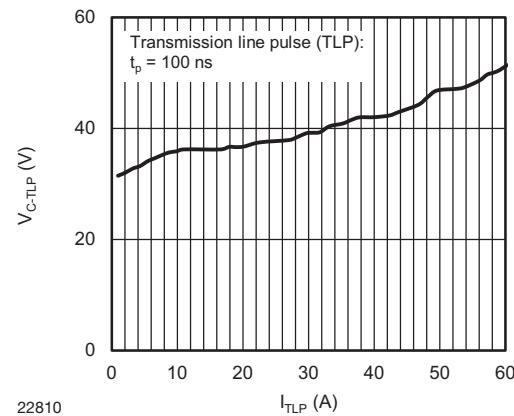
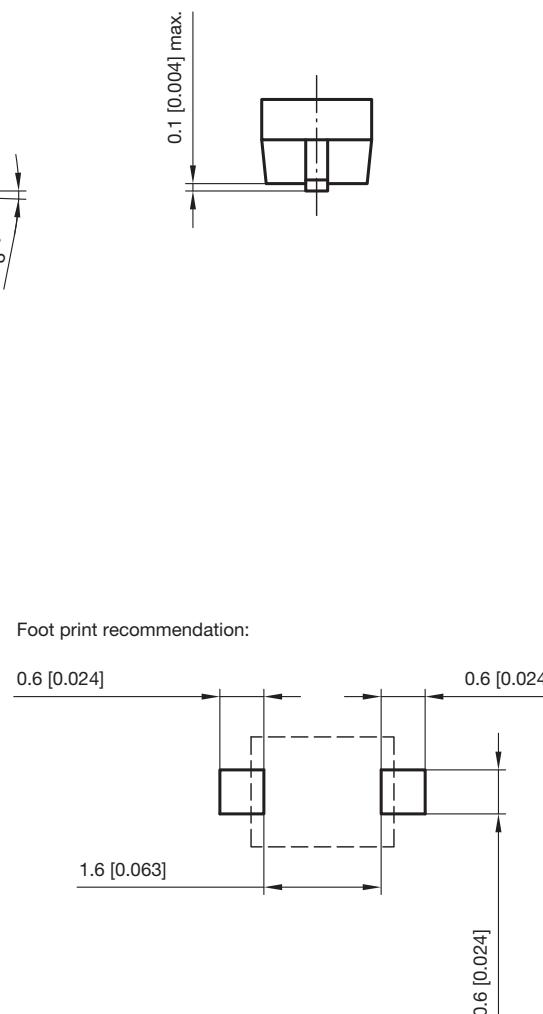
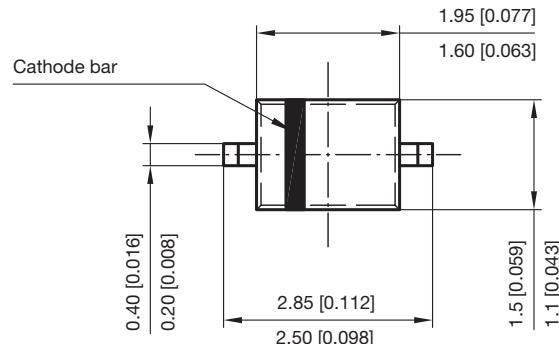
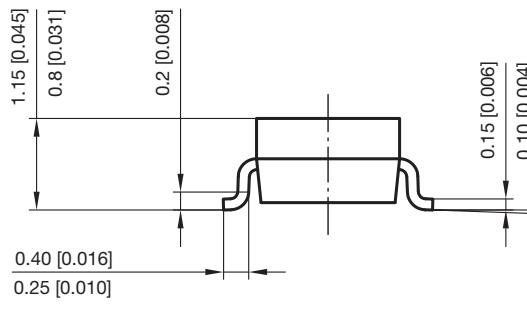
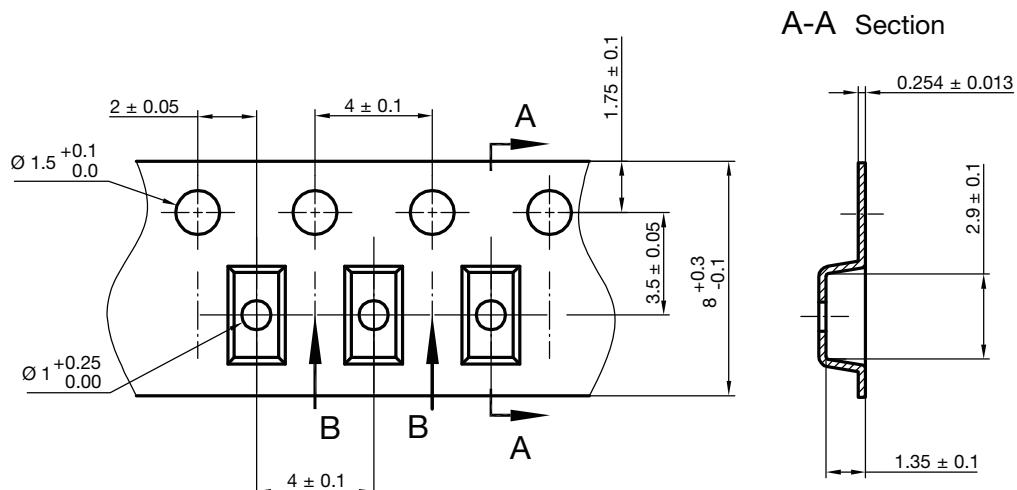
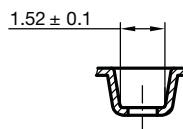


Fig. 6 - Typical Clamping Voltage  $V_{C-TLP}$  vs. Pulse Current  $I_{TLP}$

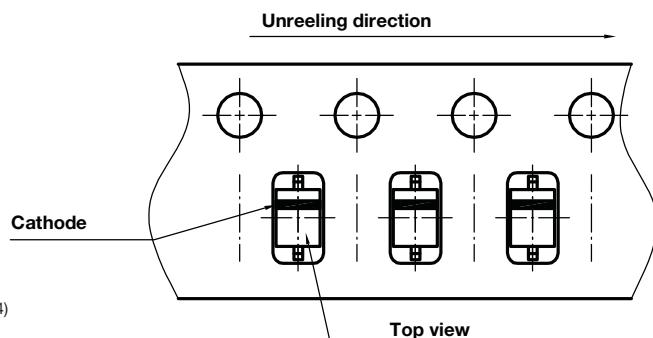
### PACKAGE DIMENSIONS in millimeters (inches) **SOD-323**



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**CARRIER TAPE SOD-323**

**B-B Section**


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22824

**ORIENTATION IN CARRIER TAPE SOD-323**


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22772

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