

New Product Announcement

Achieve Rapid Prototyping of ECP5 FPGA Designs with the Lattice Semiconductor ECP5 Versa Evaluation Board Now at Mouser

June 30, 2015 – [Mouser Electronics](http://www.mouser.com), Inc. is now stocking the [ECP5™ Versa Evaluation Board](#) from [Lattice Semiconductor](#). Part of the ECP5 Versa Development Kit ([LFE5-85E-PB-EVN](#)), the ECP5 Versa Evaluation Board enables developers to experiment with the features of Lattice Semiconductor's ECP5 Field-Programmable Gate Array (FPGA), including PCI Express, Gigabit Ethernet, DDR3, and generic SERDES performance. The evaluation board helps designers to rapidly prototype and test ECP5 designs, and is optimized to deliver the high-performance features found in the ECP5 family of FPGA devices, including the enhanced DSP architecture, high-speed SERDES, and high-speed source synchronous interfaces.

The [Lattice Semiconductor ECP5 Versa Evaluation Board](#), available from Mouser Electronics, is developed using a half-length PCI Express form-factor, which allows demonstration of the PCI Express x1 interconnection. The board features a USB-B connection for the UART and device programming, and comes equipped with 128M Serial SPI Flash, 1GByte DDR3 memory components (64Mbyte x 16), two RJ45 jacks that interface to a 10/100/1000 Ethernet to RGMII and an expansion mezzanine interconnection for prototyping. Other design features of the board include switches, LEDs and displays for demo purposes, easy power measurements, a 14-segment alpha-numeric display, and onboard reference clock sources.

The ECP5 device family covers look-up-table (LUT) capacity to 84K logic elements and supports up to 365 user I/Os. The FPGAs are suitable for a wide range of high-volume, high-speed, and low-cost Digital Signal Processing (DSP) applications. Typical [Programmable Logic](#) functions used in these applications are Finite Impulse Response (FIR) filters, Fast Fourier Transforms (FFT) functions, Correlators, Reed-Solomon/Turbo/Convolution encoders and decoders. The pre-engineered source synchronous logic implemented in the ECP5 device family also supports a broad range of interface standards, including DDR2/3, LPDDR2/3, XGMII and 7:1 LVDS.

Lattice Semiconductor's ECP5 Versa Evaluation Board is now available for purchase through the Mouser Electronics website, and features an ECP5 FPGA with a 1.1 V core supply. The evaluation board can accommodate all pin-compatible ECP5 devices in the 381 ball caBGA package. Also included with the ECP5 Versa Evaluation Board is configuration support for Lattice's Diamond® Programmer. The design software allows large complex designs to be efficiently implemented using the ECP5 FPGA family.

To learn more, visit <http://www.mouser.com/new/Lattice-Semiconductor/lattice-lfe5um-45f-versa-evn/>.

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About Mouser Electronics

Mouser Electronics, a subsidiary of TTI, Inc., is part of Warren Buffett's Berkshire Hathaway family of companies. Mouser is an award-winning, authorized semiconductor and electronic component distributor, focused on the rapid introduction of new products and technologies to electronic design engineers and buyers. Mouser.com features more than 4 million products online from more than 500 manufacturers. Mouser publishes multiple catalogs per year providing designers with up-to-date data on the components now available for the next generation of electronic devices. Mouser ships globally to over 500,000 customers in 170 countries from its 492,000 sq. ft. state-of-the-art facility south of Dallas, Texas. For more information, visit <http://www.mouser.com>.

About Lattice Semiconductor

Lattice Semiconductor is a service-driven developer of innovative low cost, low power programmable design solutions, including FPGAs, PLDs and programmable power and board management devices. Lattice provides customers with a comprehensive support ecosystem that includes powerful, easy to use Design Tools, IP cores, Reference Designs, Development Kits and local technical support.

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Further information, contact:
Kevin Hess, Mouser Electronics
Vice President Technical Marketing
(817) 804-3833
kevin.hess@mouser.com

For press inquiries, contact:
Kelly DeGarmo, Mouser Electronics
Mgr., Corporate Communications
(817) 804-7764
kelly.degarmo@mouser.com