

MLCC – Multilayer Ceramic Chip Capacitors Mid- and High-Voltage

[illegible]

	Capacitance tolerance	Operating Temperature	Capacitance Characteristics within Operating Temperature Range
NPO, C ≥10 pF	±5%	−55 °C to +125 °C	±30 ppm/°C; ±0.54%
X7R	±10%	−55 °C to +125 °C	±15%