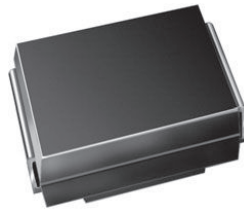


Surface-Mount Glass Passivated Rectifier


SMB (DO-214AA)

Cathode Anode


RoHS
 COMPLIANT
 HALOGEN
FREE

FEATURES

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- Low forward voltage drop
- Low leakage current
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

LINKS TO ADDITIONAL RESOURCES



3D Models

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2.0 A
V_{RRM}	400 V, 600 V, 800 V, 1000 V
I_{FSM}	70 A
I_R	1.0 μ A
V_F at $I_F = 2.0$ A	0.86 V
T_J max.	150 °C
Package	SMB (DO-214AA)
Circuit configuration	Single

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes for consumer, medical and telecommunication.

MECHANICAL DATA

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-E3 - RoHS compliant, commercial grade

Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 and M3 suffix meets JESD 201 class 1A whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	SYMBOL	S2BG	S2BJ	S2BK	S2BM	UNIT
Device marking code		2G	2J	2K	2M	
Maximum repetitive peak reverse voltage	V_{RRM}	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	280	420	560	700	V
Maximum DC forward current (fig. 1)	$I_F^{(1)}$	2.0				A
	$I_F^{(2)}$	1.3				A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I_{FSM}	70				A
Operating and storage temperature range	$T_J^{(3)}, T_{STG}$	-55 to +150				°C

Notes

- (1) Mounted on 8 mm x 8 mm pad areas, 1 oz. FR4 PCB
- (2) Free air mounted on recommended copper pad area
- (3) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$



ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 1.0 A	T _J = 25 °C	V _F ⁽¹⁾	0.90	-	V
	I _F = 2.0 A			0.96	1.1	
	I _F = 1.0 A	T _J = 125 °C		0.78	-	
	I _F = 2.0 A			0.86	1.05	
Reverse current	Rated V _R	T _J = 25 °C	I _R ⁽²⁾	0.15	1.0	μA
		T _J = 125 °C		36	125	
Typical reverse recovery time	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		t _{rr}	2.0		μs
Typical junction capacitance	Rated V _R = 4.0 V, 1 MHz		C _J	16		pF

Notes

- (1) Pulse test: 300 μs pulse width, 1 % duty cycle
- (2) Pulse test: Pulse width, ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	S2BG	S2BJ	S2BK	S2BM	UNIT
Typical thermal resistance	R _{θJA} ⁽¹⁾⁽²⁾	107				°C/W
	R _{θJM} ⁽³⁾	7.2				

Notes

- (1) The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{θJA}
- (2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz. standard footprint
- (3) Thermal resistance junction-to-mount to follow JEDEC® 51-14, transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
S2BJ-M3/I	0.096	I	3200	13" diameter plastic tape and reel
S2BJ-E3/I	0.096	I	3200	13" diameter plastic tape and reel

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

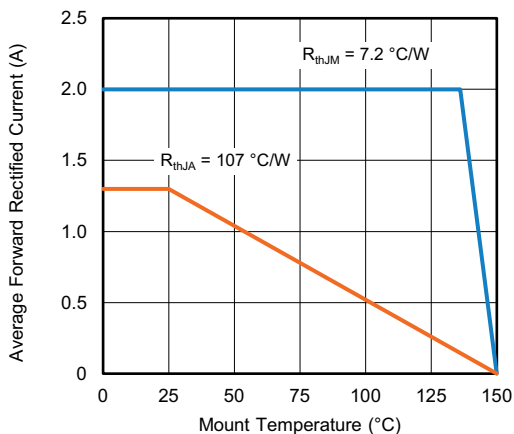


Fig. 1 - Maximum Forward Current Derating Curve

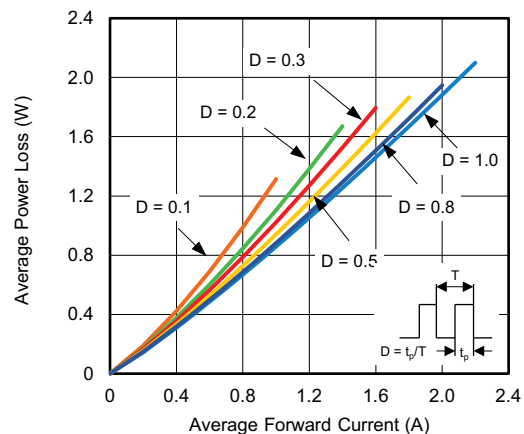


Fig. 2 - Forward Power Loss Characteristics

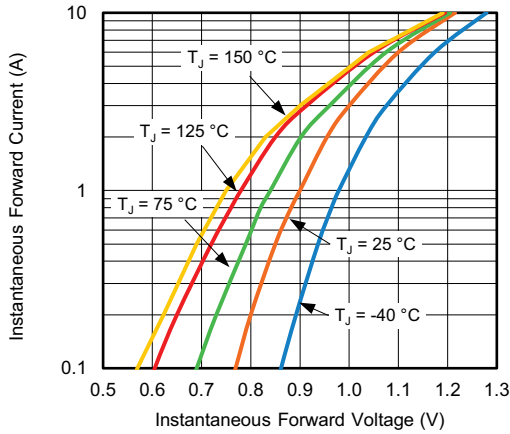


Fig. 3 - Typical Instantaneous Forward Characteristics

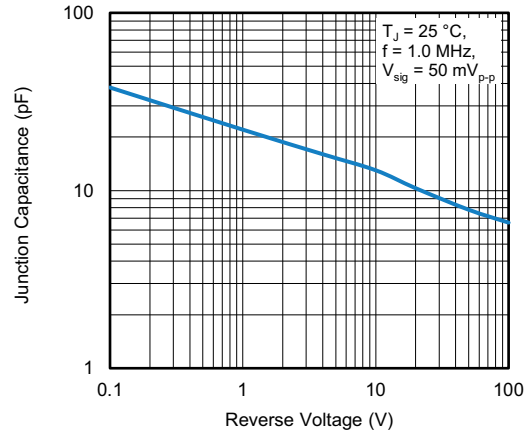


Fig. 5 - Typical Junction Capacitance

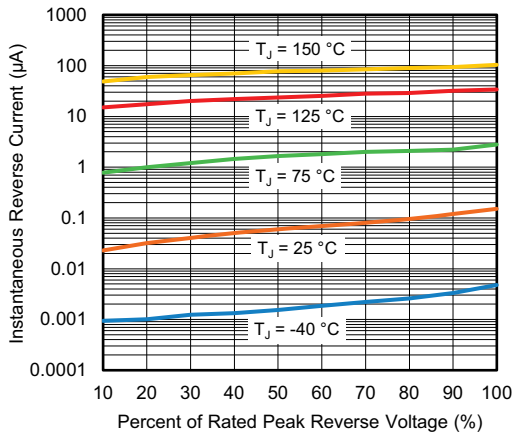


Fig. 4 - Typical Reverse Characteristics

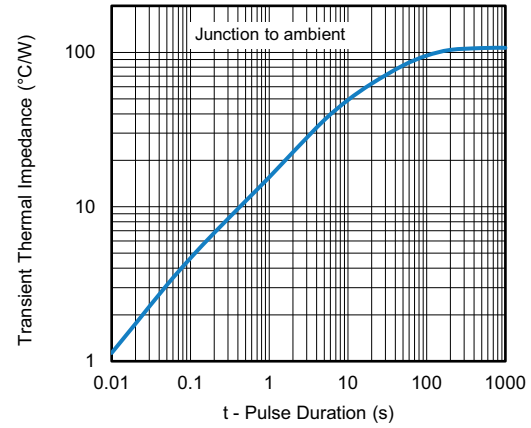
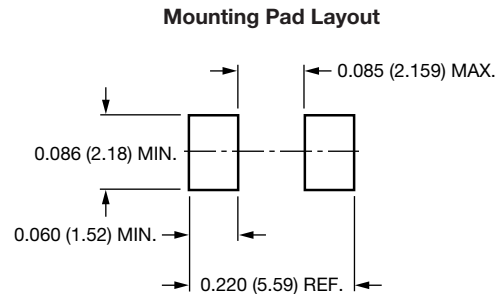
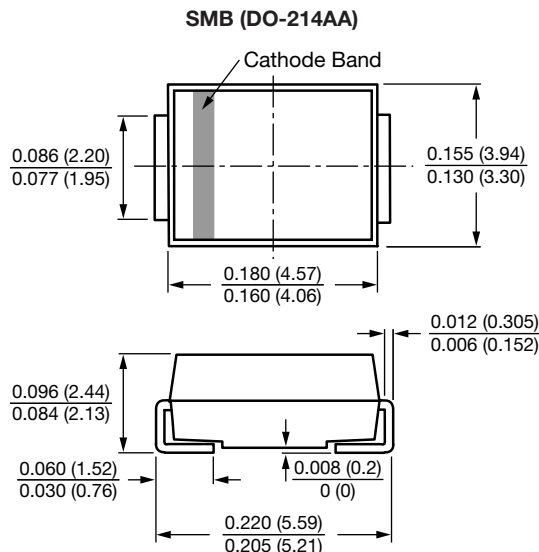


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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