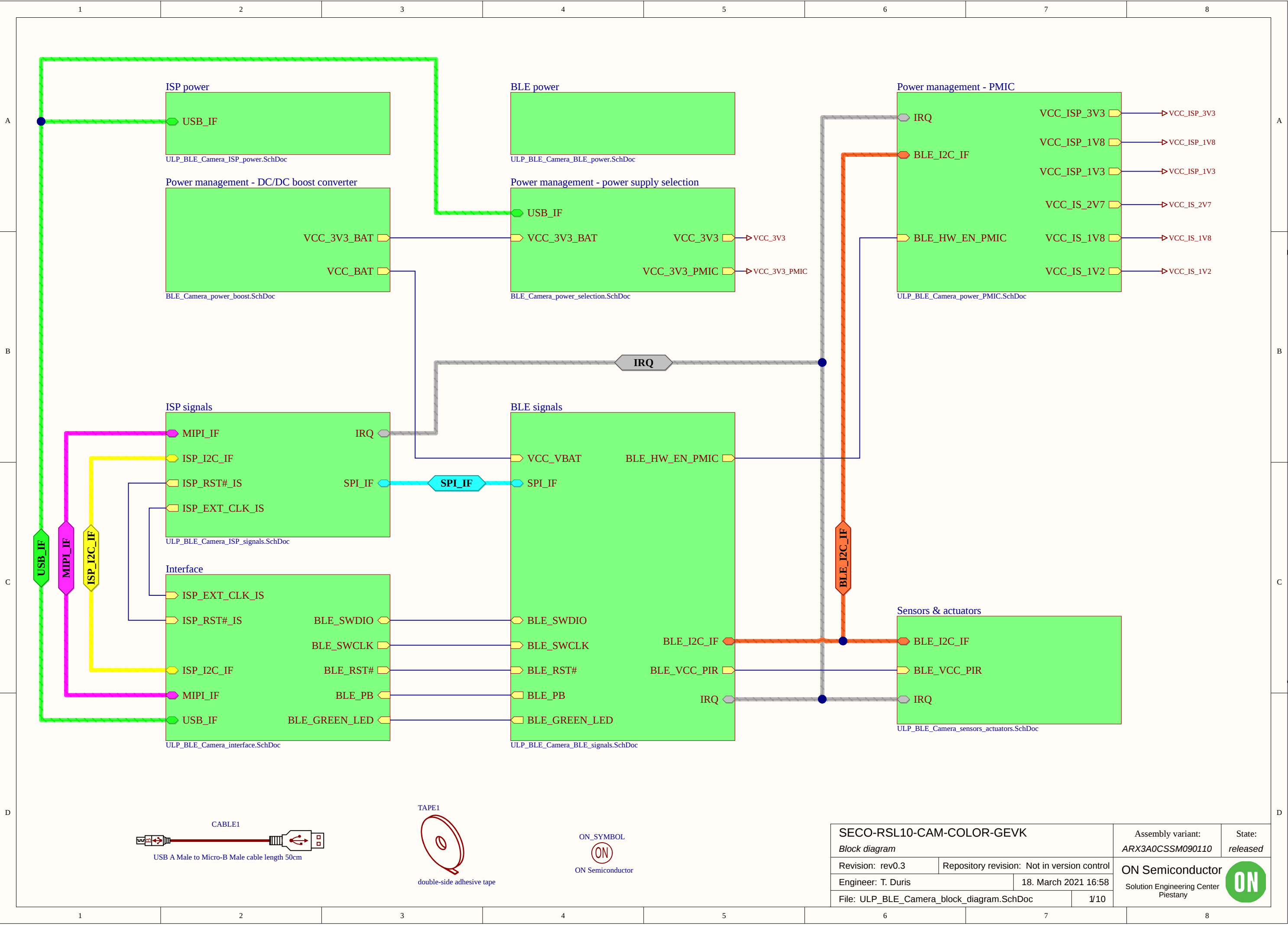
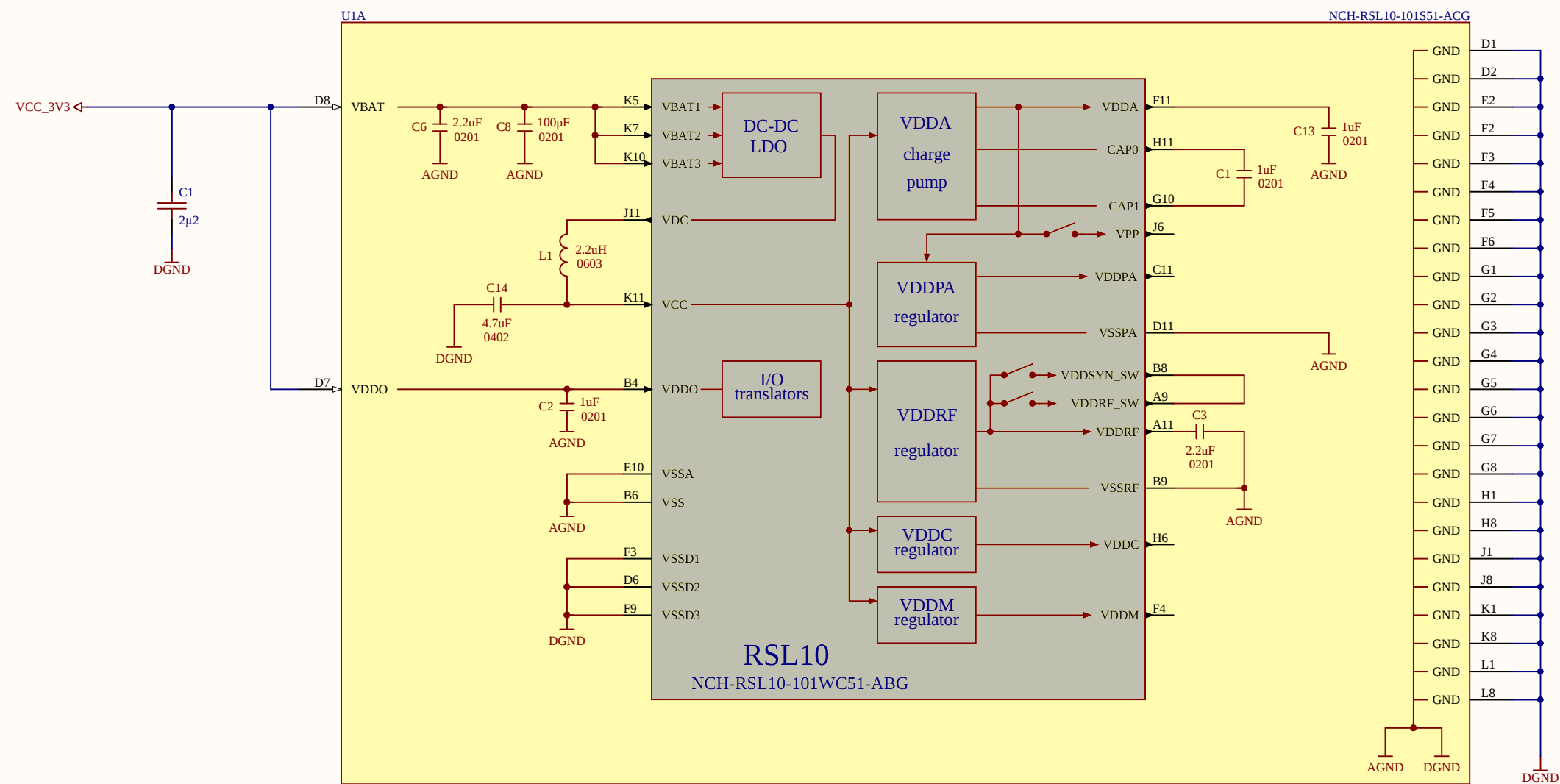
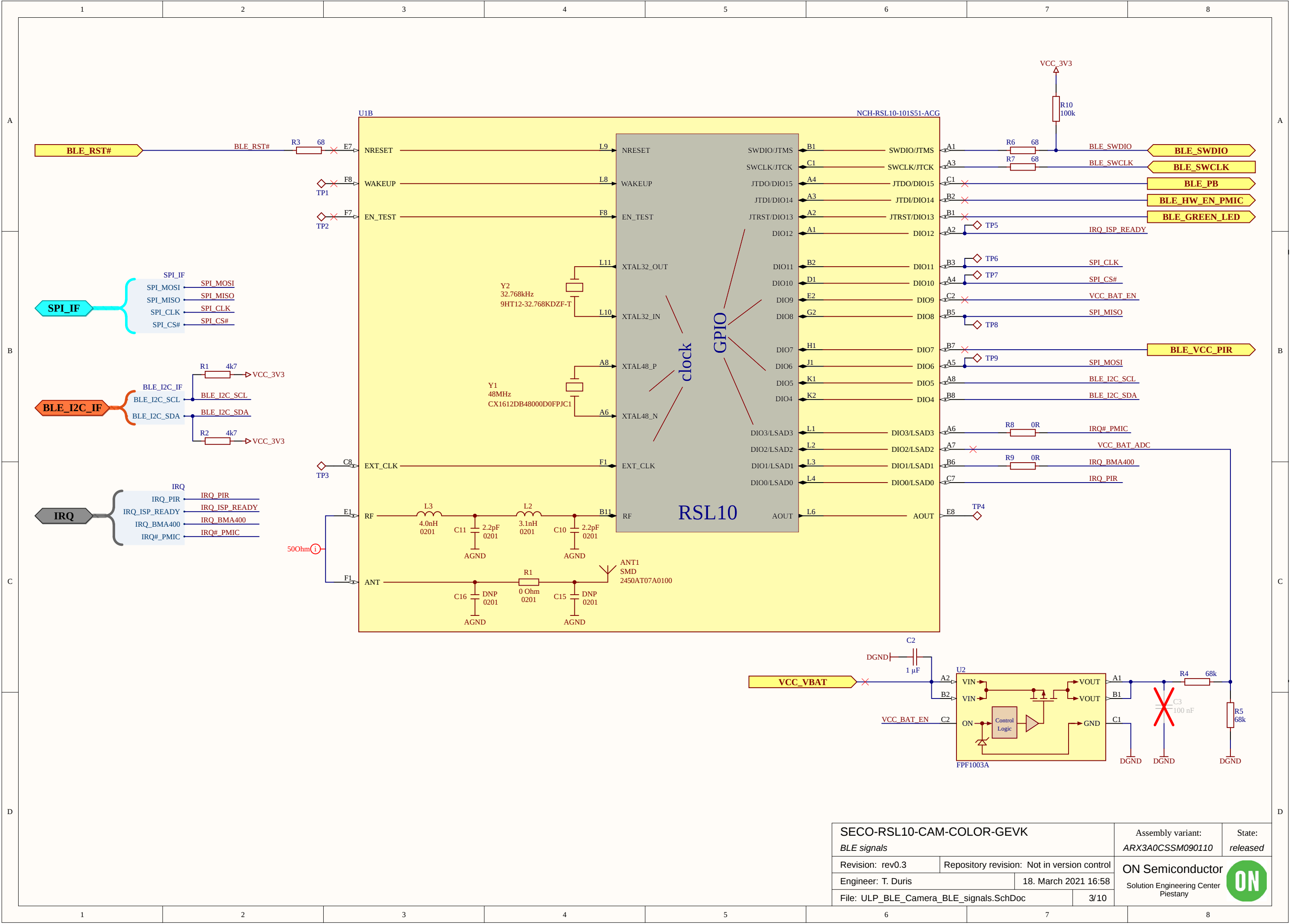


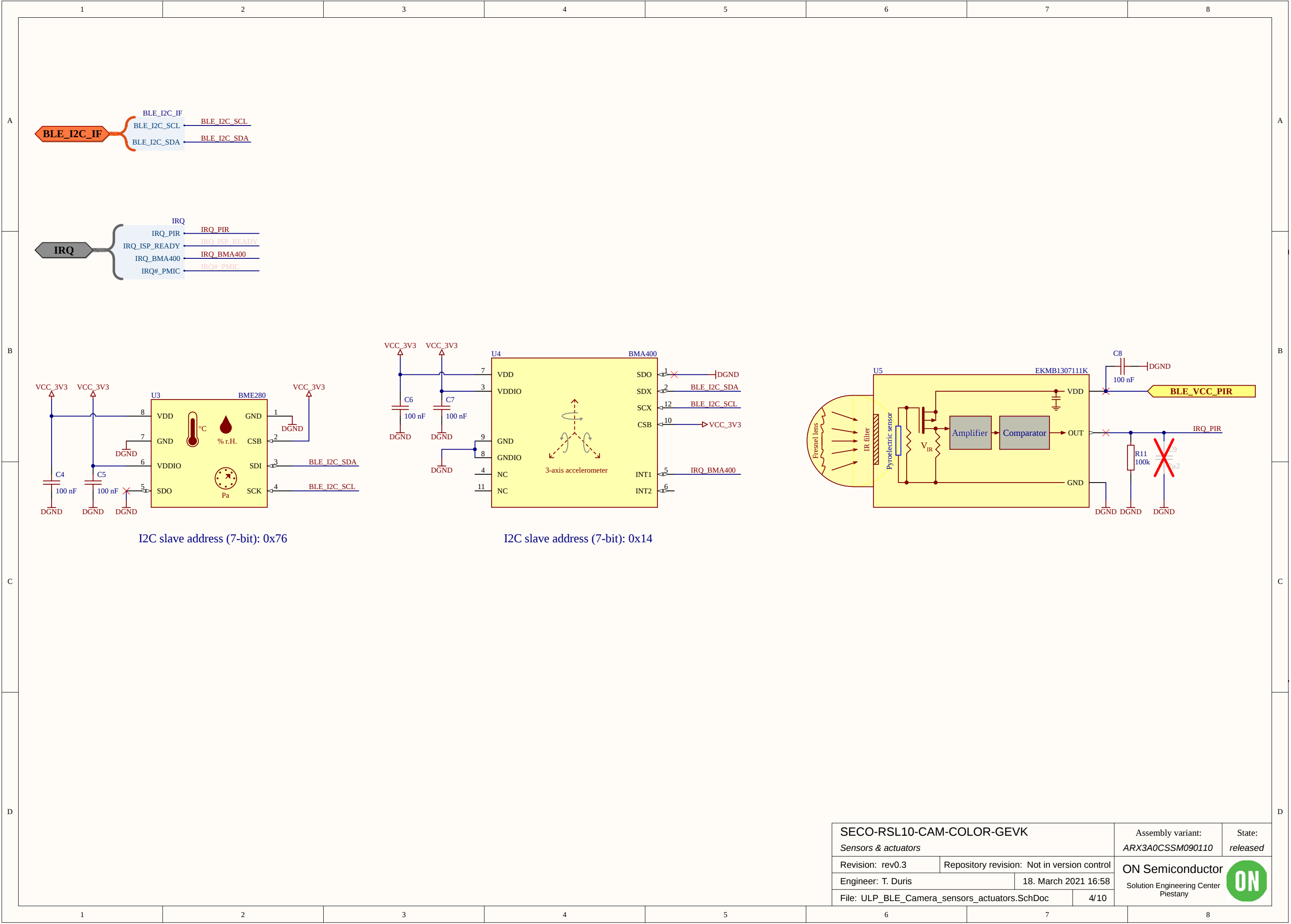


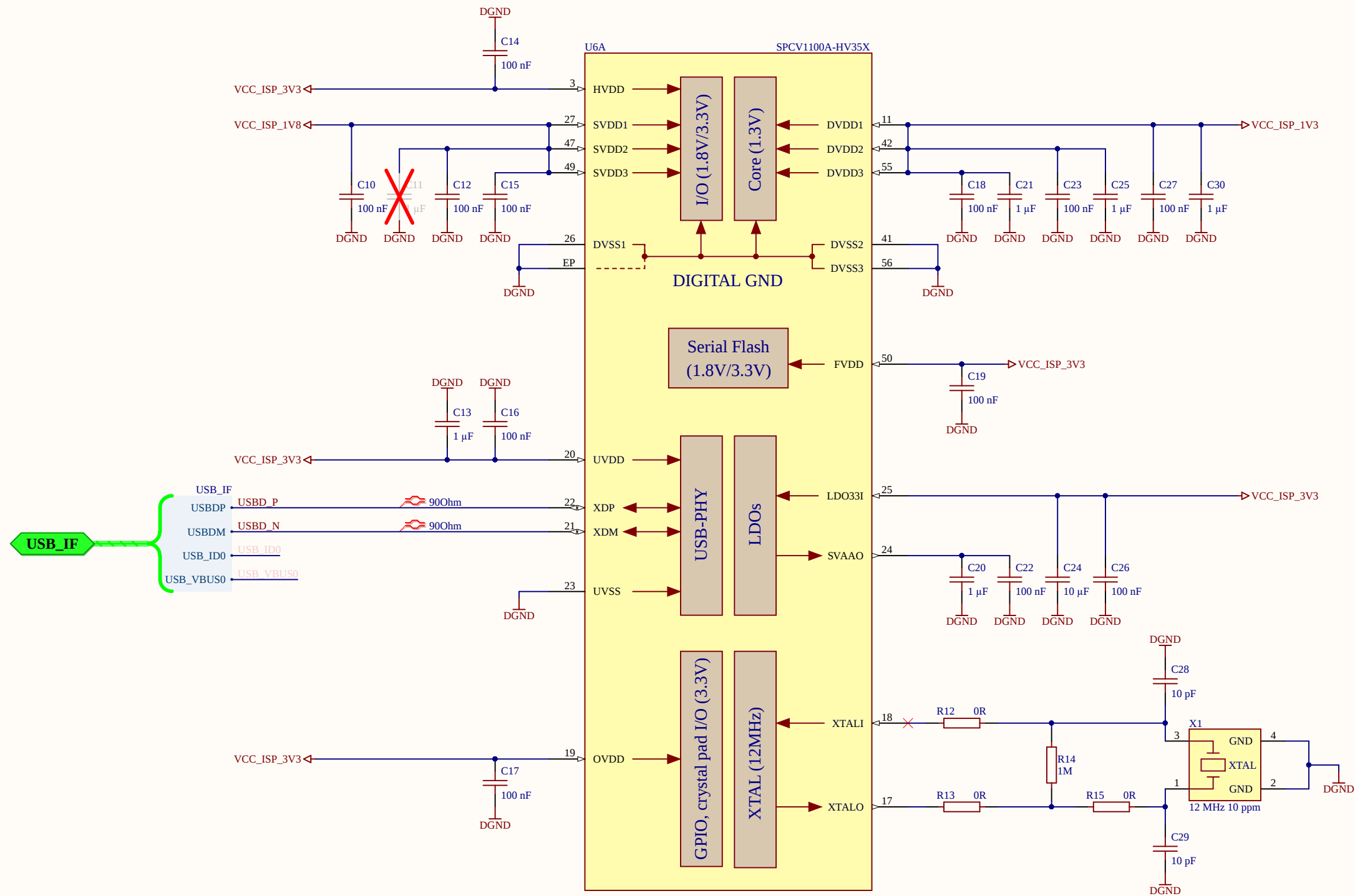


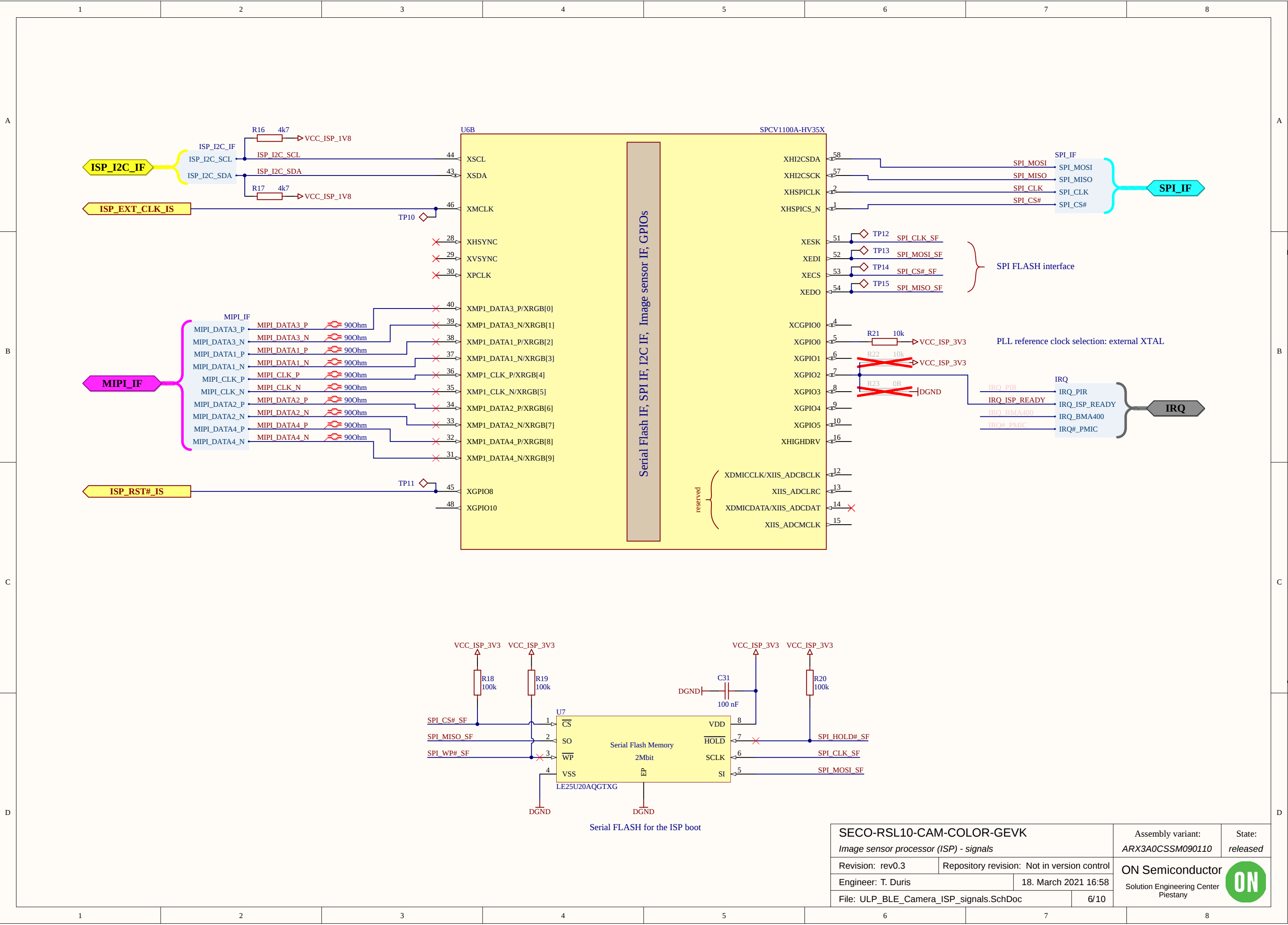
SECO-RSL10-CAM-COLOR-GEVK <i>BLE power</i>			Assembly variant: <i>ARX3A0CSSM090110</i>	State: <i>released</i>
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piestany 	
Engineer: T. Duris		18. March 2021 16:58		
File: ULP_BLE_Camera_BLE_power.SchDoc		2/10		

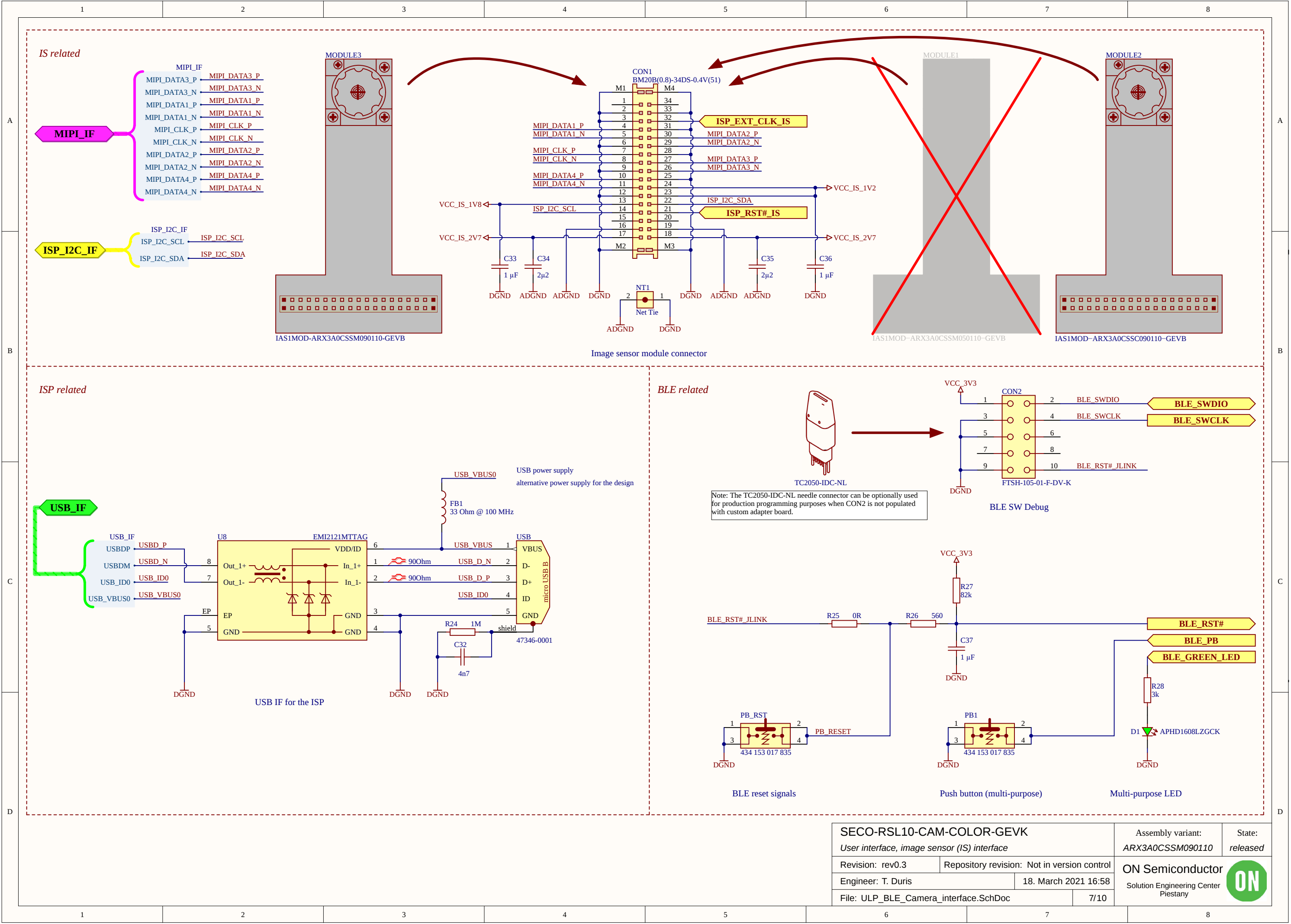


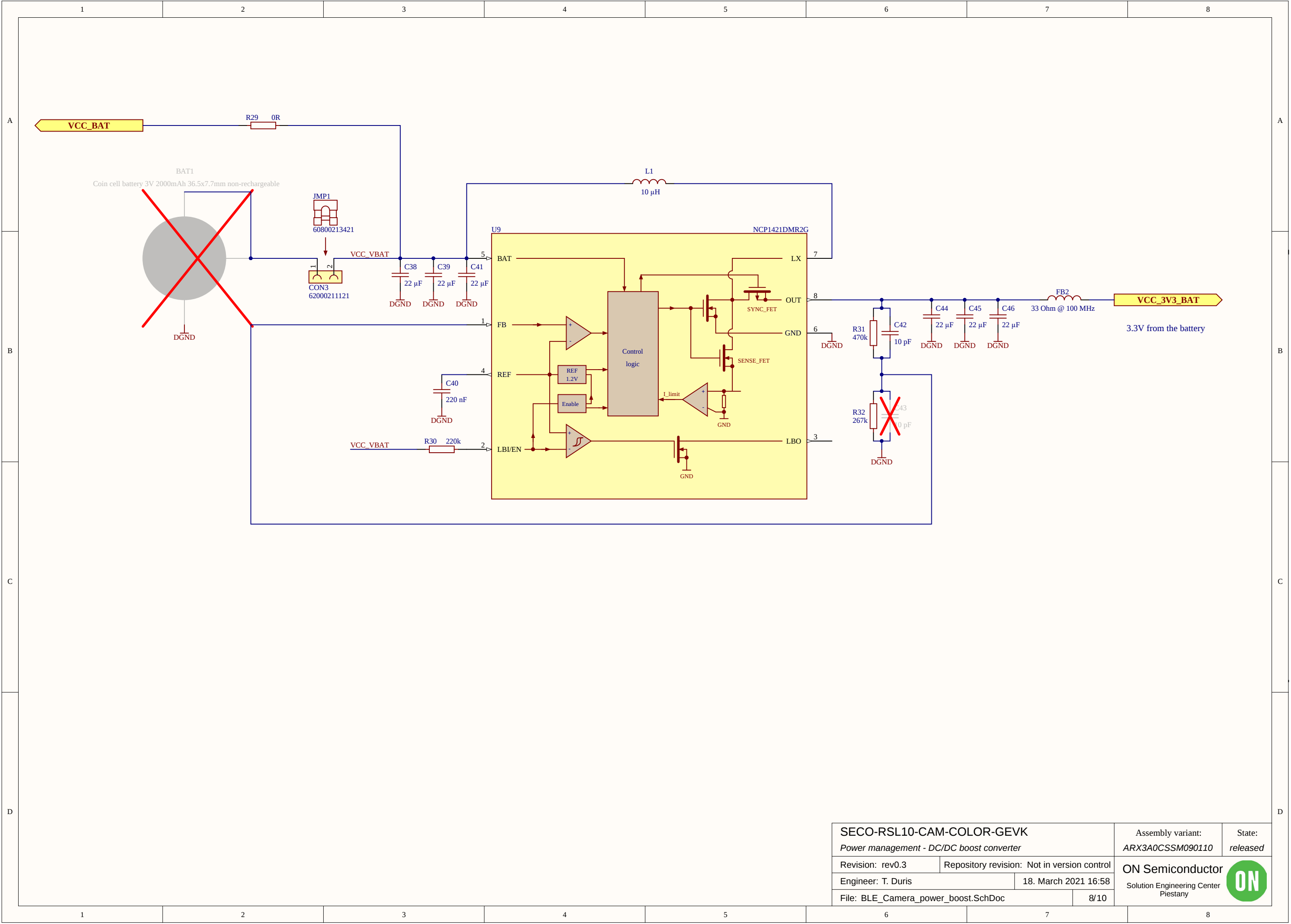


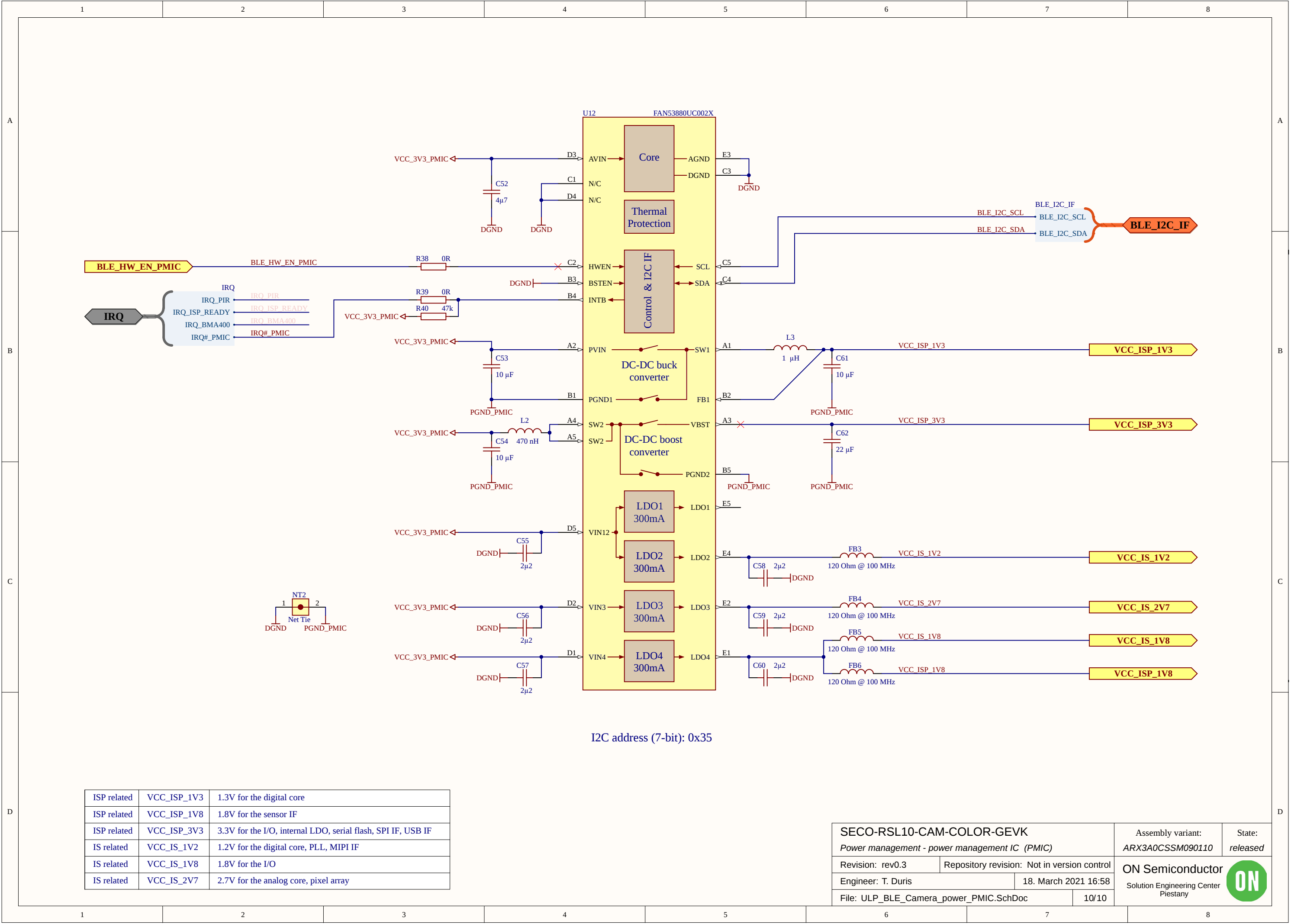












I2C address (7-bit): 0x35

ISP related	VCC_ISP_1V3	1.3V for the digital core
ISP related	VCC_ISP_1V8	1.8V for the sensor IF
ISP related	VCC_ISP_3V3	3.3V for the I/O, internal LDO, serial flash, SPI IF, USB IF
IS related	VCC_IS_1V2	1.2V for the digital core, PLL, MIPI IF
IS related	VCC_IS_1V8	1.8V for the I/O
IS related	VCC_IS_2V7	2.7V for the analog core, pixel array

SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
Power management - power management IC (PMIC)		ARX3A0CSSM090110	released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piestany
Engineer: T. Duris		18. March 2021 16:58	
File: ULP_BLE_Camera_power_PMIC.SchDoc		10/10	

