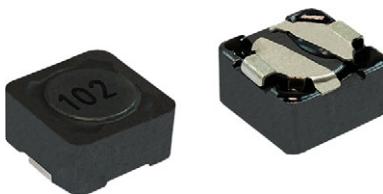


Ferrite Power Inductor, Shielded Drum Core



LINKS TO ADDITIONAL RESOURCES


[Product Page](#)

ELECTRICAL SPECIFICATIONS

Operating temperature:

-40 °C to +105 °C (temperature rise included)

Resistance to solder heat:

255 °C for 10 s (2 times max. through reflow)

FEATURES

- 7.8 mm x 7.8 mm x 5.0 mm max. SMD package
- Shielded drum core ferrite construction to reduce leakage flux
- Inductance range: 1.0 μ H to 1000 μ H
- Material categorization:
for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

APPLICATIONS

- DC/DC power supplies
- Noise suppression and filtering

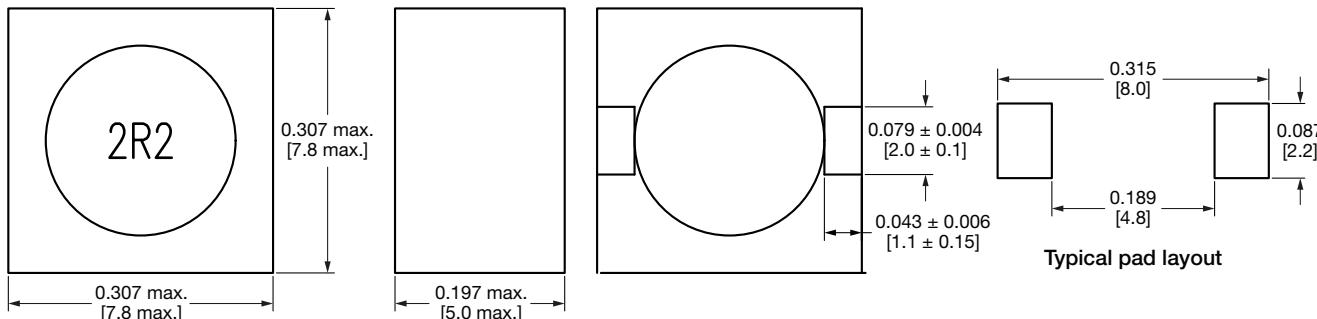
STANDARD ELECTRICAL SPECIFICATIONS

PART NUMBER	L ₀ INDUCTANCE (μ H)	INDUCTANCE TOLERANCE (%)	DCR MAX. (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽¹⁾
IFDC3030EZER1R0N	1.0	30	13	9.00
IFDC3030EZER1R5N	1.5	30	14.5	7.60
IFDC3030EZER2R2M	2.2	20	18	6.50
IFDC3030EZER3R3M	3.3	20	24	5.60
IFDC3030EZER3R6M	3.6	20	26	5.00
IFDC3030EZER4R7M	4.7	20	30	4.40
IFDC3030EZER6R8M	6.8	20	41	3.00
IFDC3030EZER8R2M	8.2	20	48	2.90
IFDC3030EZER100M	10	20	49	1.84
IFDC3030EZER120M	12	20	58	1.71
IFDC3030EZER150M	15	20	81	1.47
IFDC3030EZER180M	18	20	91	1.31
IFDC3030EZER220M	22	20	110	1.23
IFDC3030EZER270M	27	20	150	1.12
IFDC3030EZER330M	33	20	170	0.96
IFDC3030EZER390M	39	20	230	0.91
IFDC3030EZER470M	47	20	260	0.88
IFDC3030EZER560M	56	20	350	0.75
IFDC3030EZER680M	68	20	380	0.69
IFDC3030EZER820M	82	20	430	0.61
IFDC3030EZER101M	100	20	610	0.60
IFDC3030EZER121M	120	20	660	0.52
IFDC3030EZER151M	150	20	880	0.46
IFDC3030EZER181M	180	20	980	0.42
IFDC3030EZER221M	220	20	1170	0.36
IFDC3030EZER271M	270	20	1640	0.34
IFDC3030EZER331M	330	20	1860	0.32
IFDC3030EZER391M	390	20	2850	0.29
IFDC3030EZER471M	470	20	3010	0.36
IFDC3030EZER561M	560	20	3620	0.23
IFDC3030EZER681M	680	20	3820	0.20
IFDC3030EZER821M	820	20	4800	0.30
IFDC3030EZER102M	1000	20	5400	0.27

Notes

- All test data is referenced to 25 °C ambient
- Test condition: 100 kHz, 0.3 V for 8.2 μ H and below, and 1 kHz, 0.3 V for 10 μ H and above
- Storage condition: -40 °C to +105 °C (on board); and -10 °C to +40 °C and < 70 % RH (in component packaging)

⁽¹⁾ DC current (A) that will cause an approximate ΔT of 40 °C or cause L₀ to drop by 35 %, whichever is lower

DIMENSIONS in inches [millimeters]

Typical pad layout
DESCRIPTION

IFDC3030EZ	2.2 μ H	± 20 %	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER

I	F	D	C	3	0	3	0	E	Z	E	R	2	R	2	M
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE		INDUCTANCE TOLERANCE			

ER = tape and reel
2R2 = 2.2 μ H
M = ± 20 %
N = ± 30 %

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