



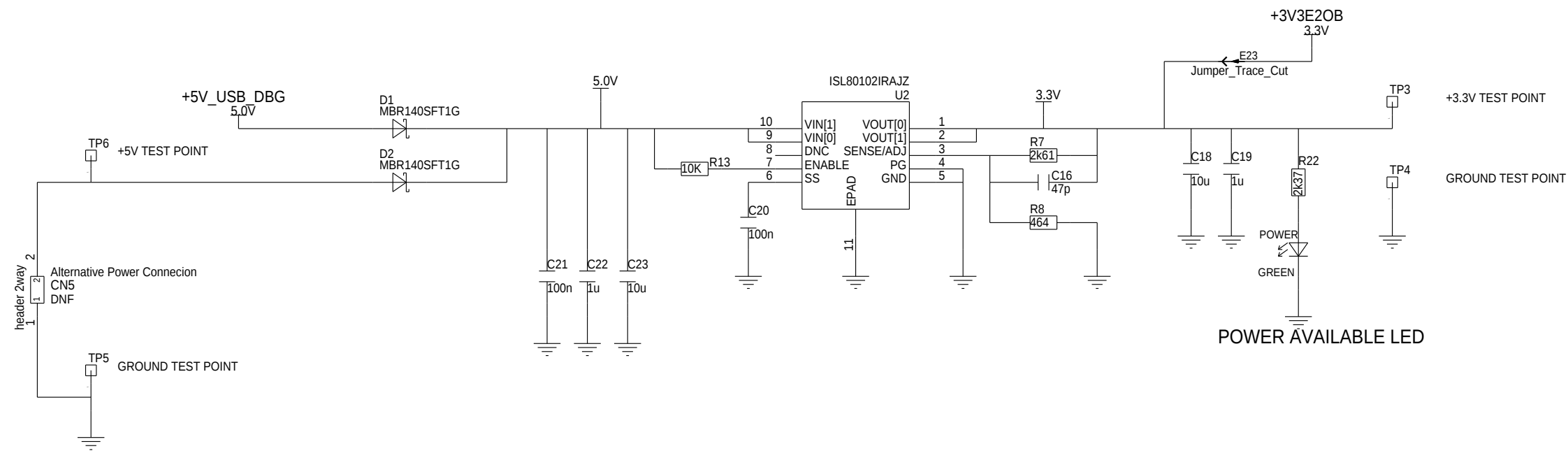
# FPB-RA2E2

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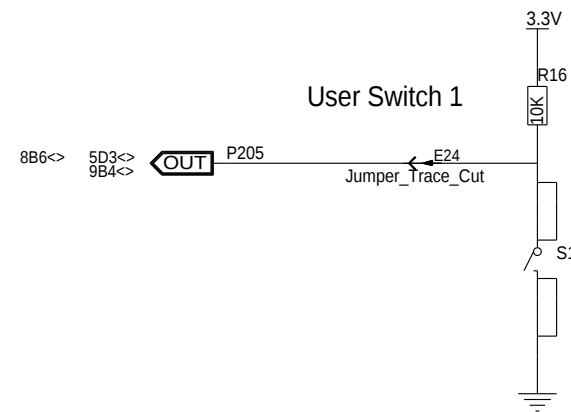
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| REV | COMMENT                           | DATE       | DRAWN BY |
|-----|-----------------------------------|------------|----------|
| 1.0 | WS Release                        | 01/02/2021 | SDD/KSS  |
| 1.1 | ES Board (WS + MOD)               | 26/03/2021 | SDD/KSS  |
| 2.0 | MP Release                        | 28/04/2021 | SDD/KSS  |
| 2.1 | Fitted CN1 Header and Jumper      | 11/06/2021 | SDD/KSS  |
| 2.2 | Changed VCL capacitor value       | 17/06/2021 | SDD/KSS  |
| 2.3 | Changed Y2 to be DNF              | 12/01/2022 | SDD/KSS  |
| 2.4 | Marked unconnected ports as (NC)  | 14/04/2022 | SDD/KSS  |
| 2.5 | Removed SPI references from PMOD2 | 05/05/2022 | SDD/KSS  |

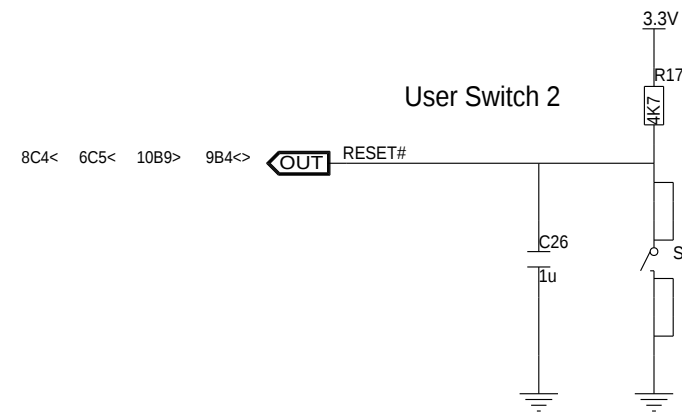
# 3.3V LINEAR REGULATOR



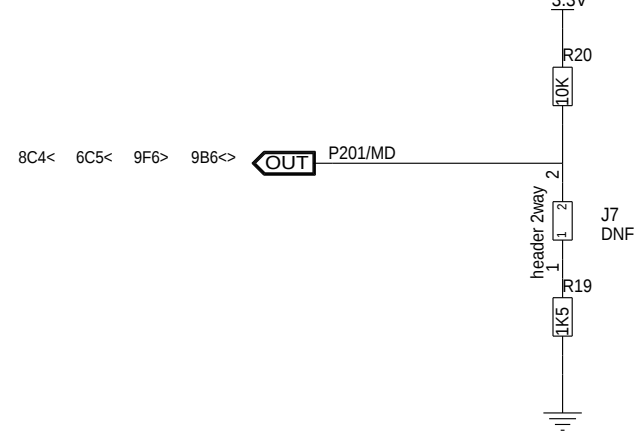
## USER PUSH-BUTTONS



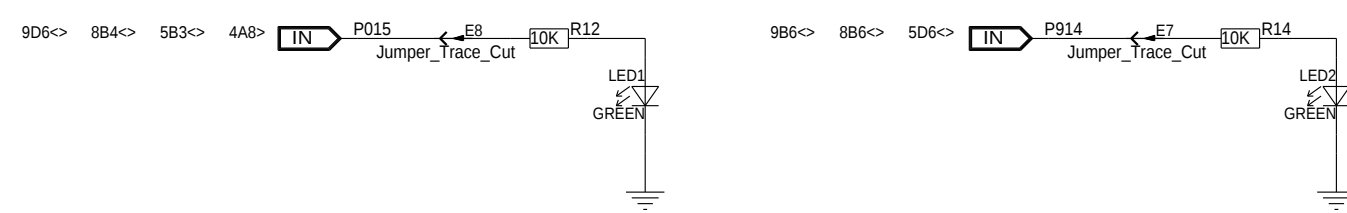
## RESET PUSH-BUTTON

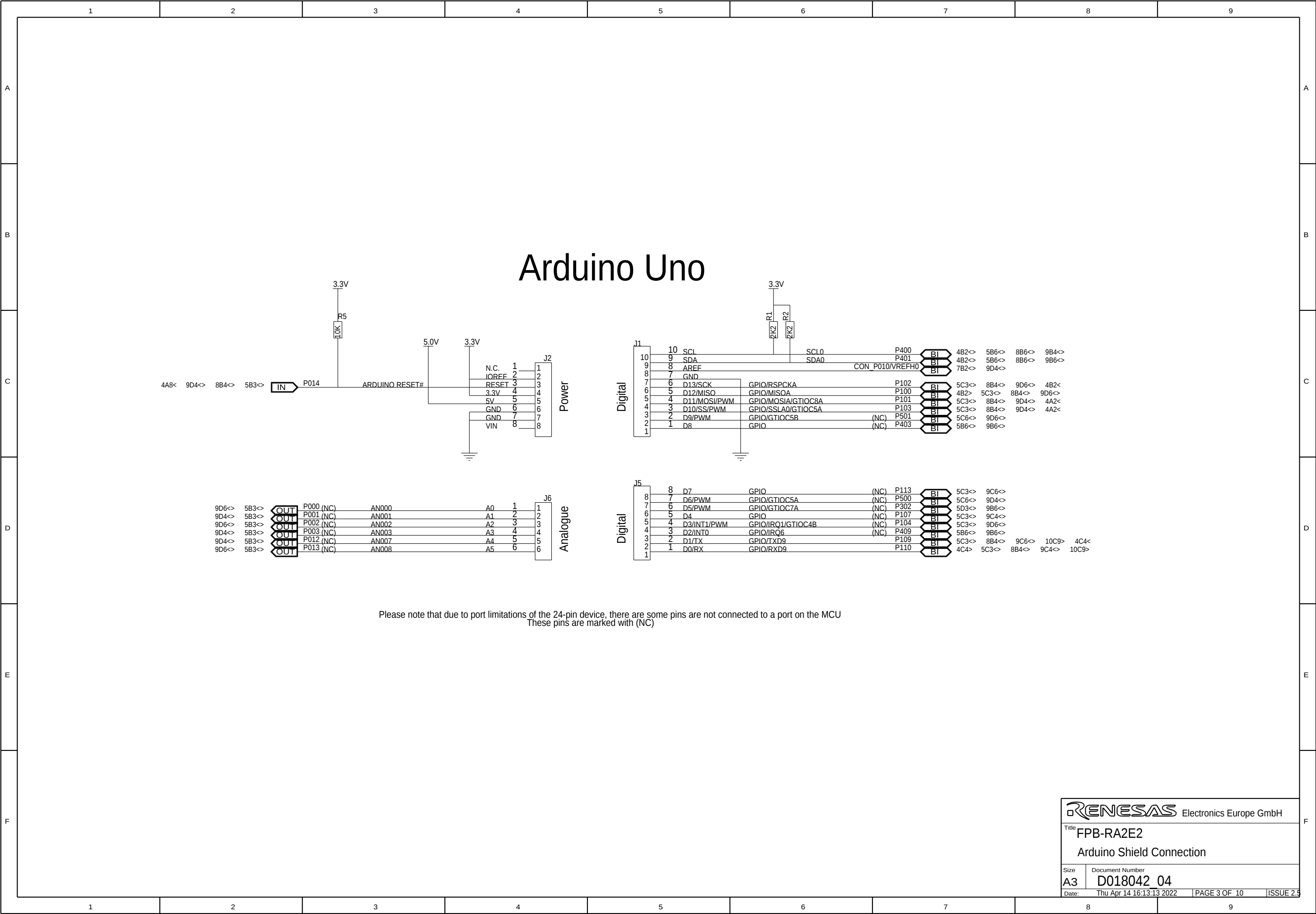


## MCU BOOT MODE

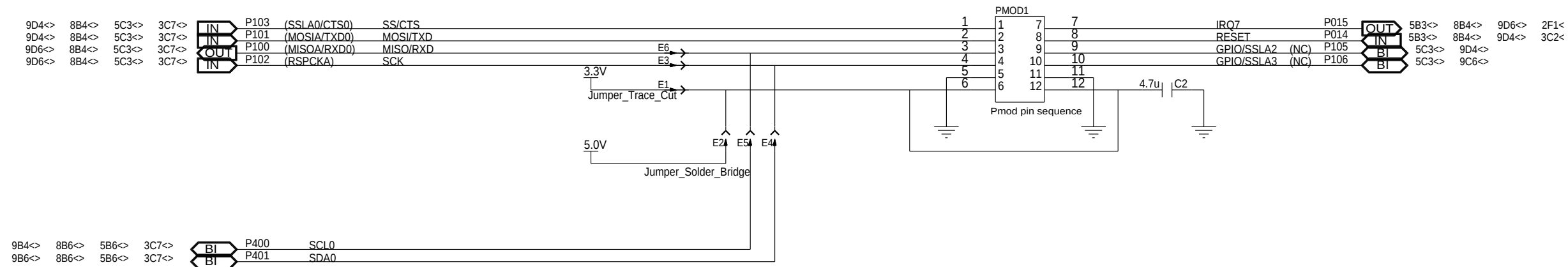


## USER LEDS

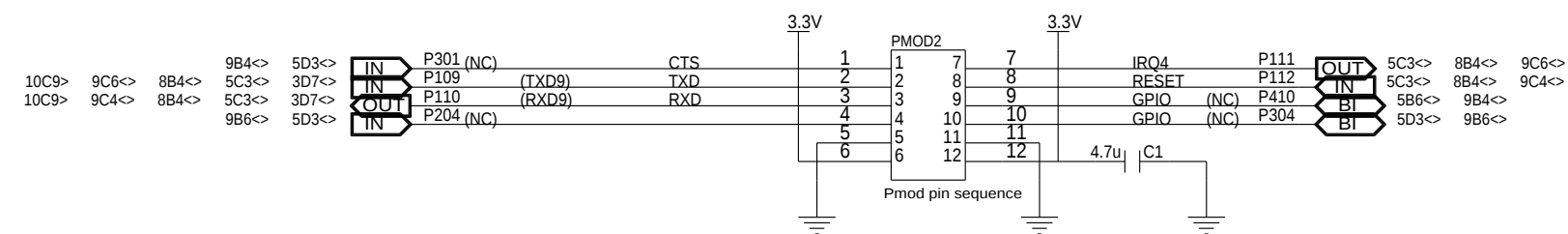




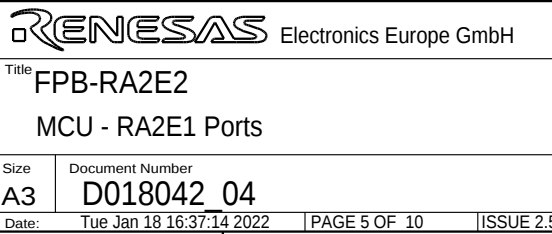
## SPI/UART/I2C

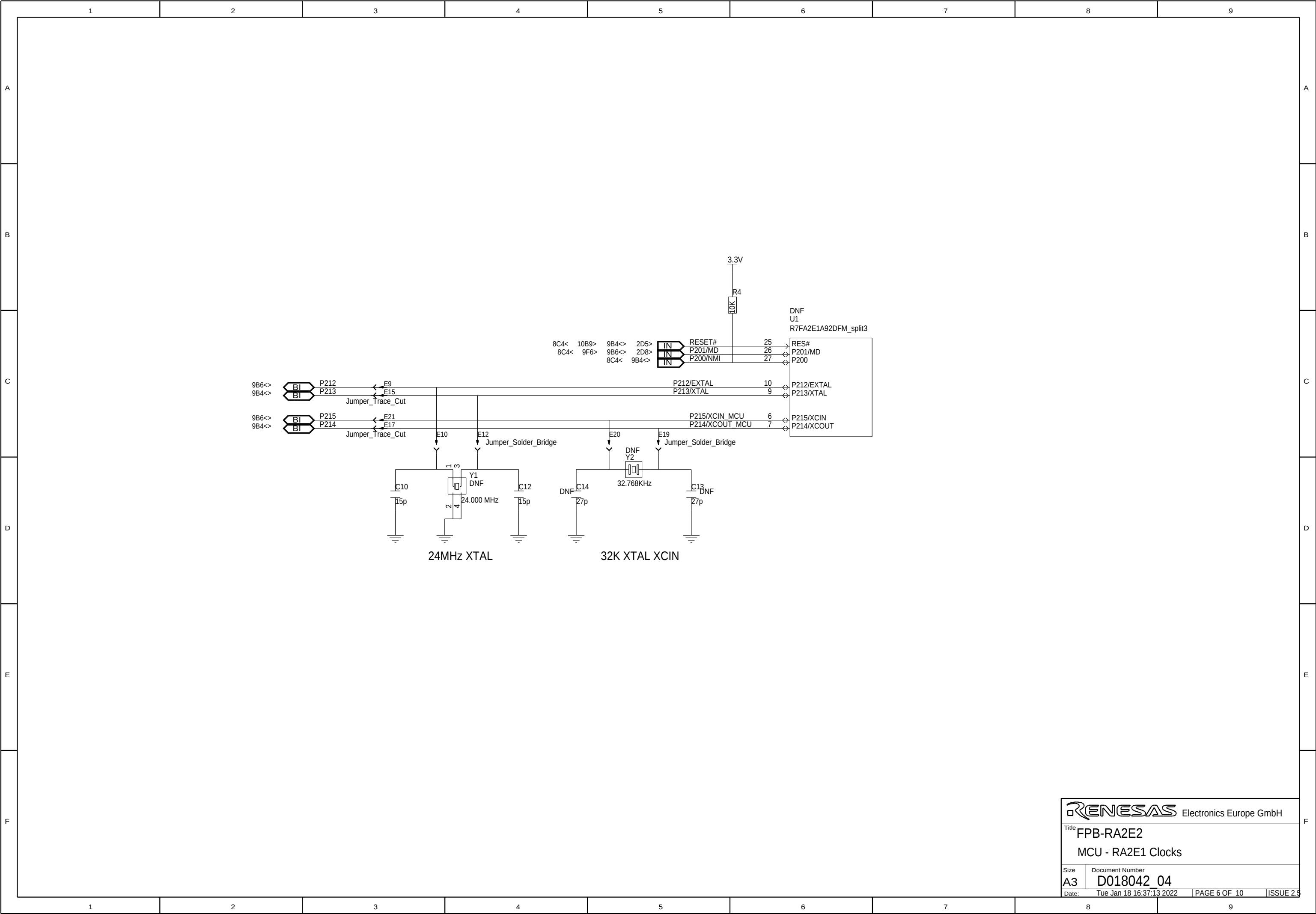


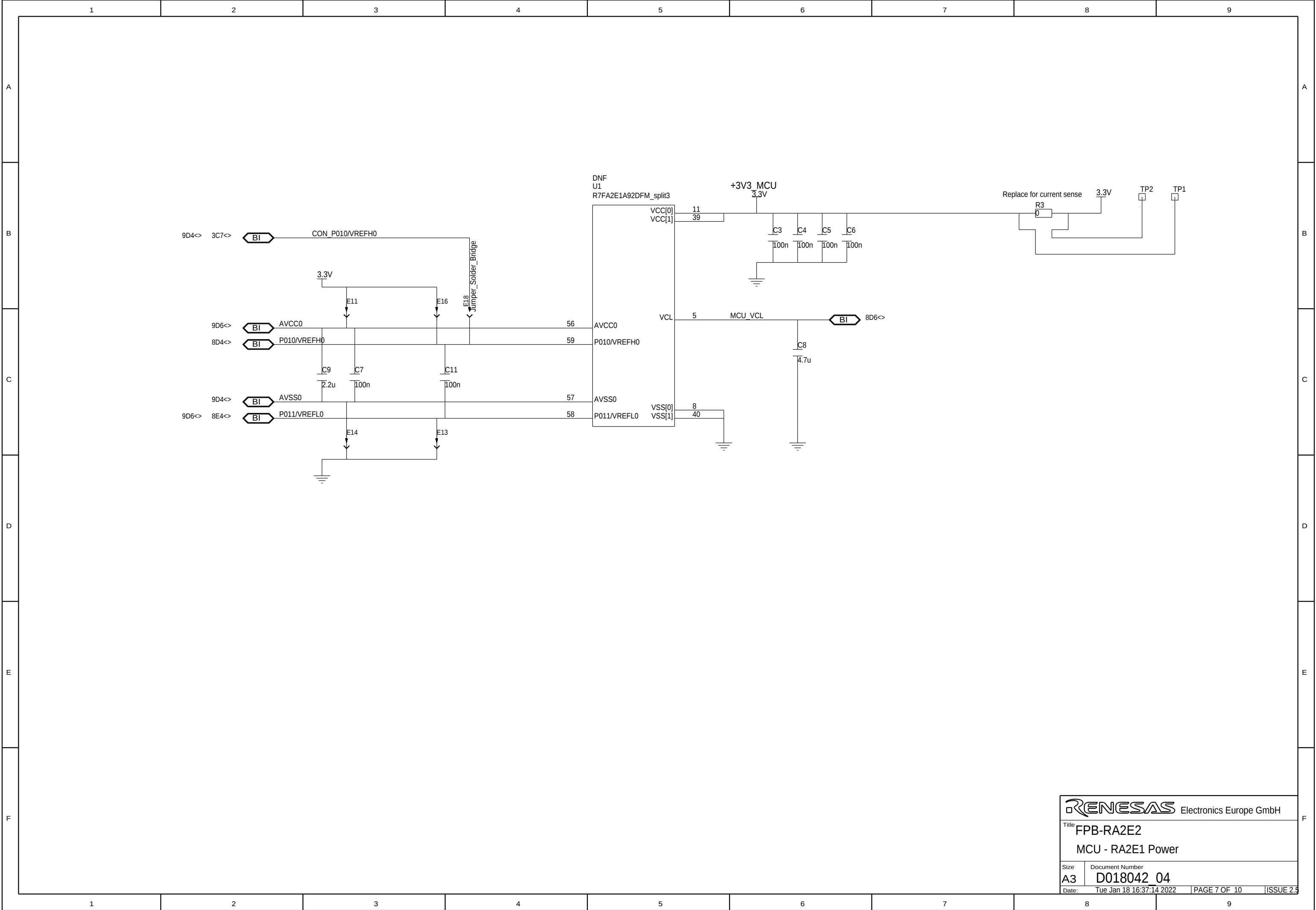
## UART

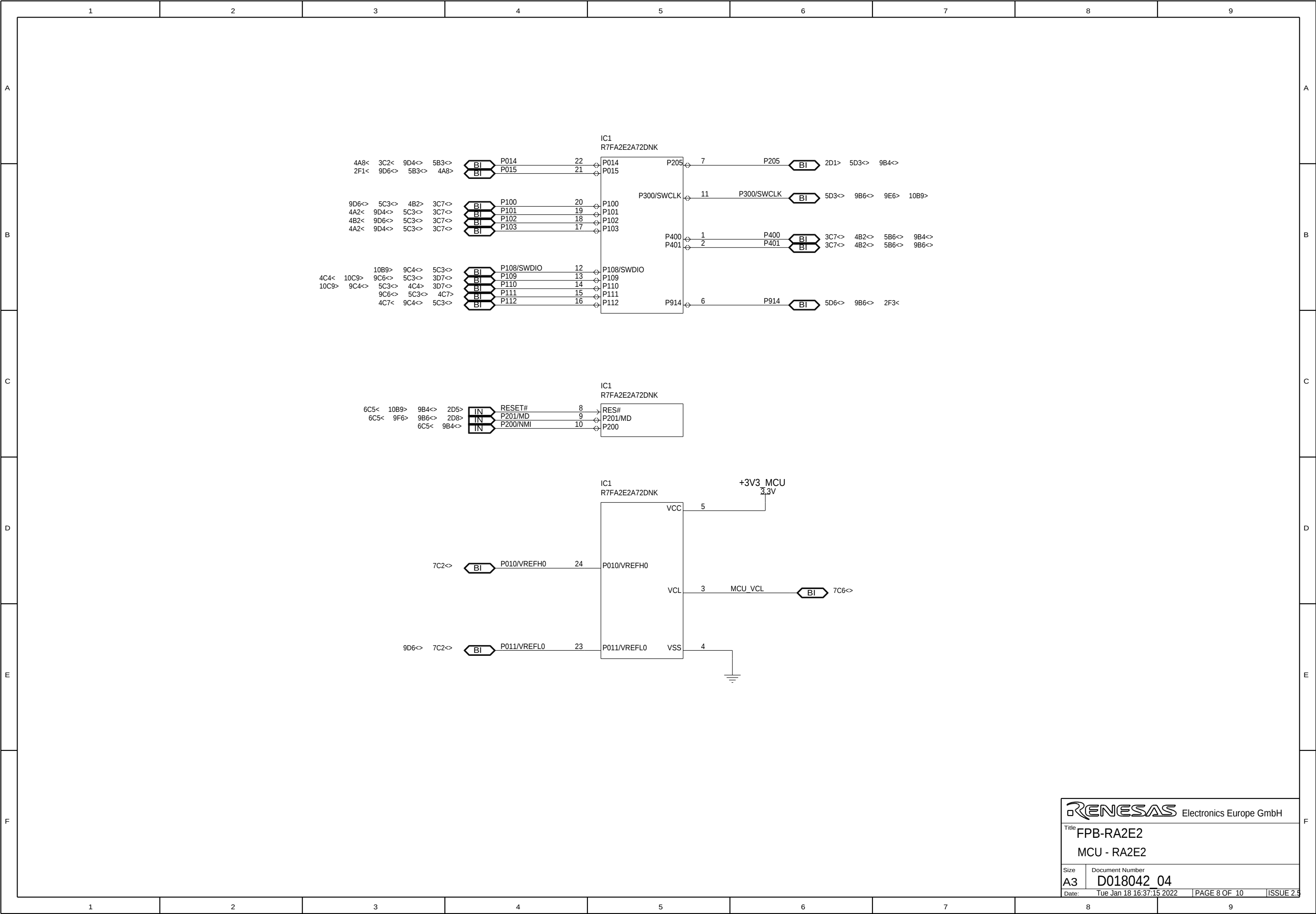


Please note that due to port limitations of the 24-pin device, there are some pins are not connected to a port on the MCU  
These pins are marked with (NC)

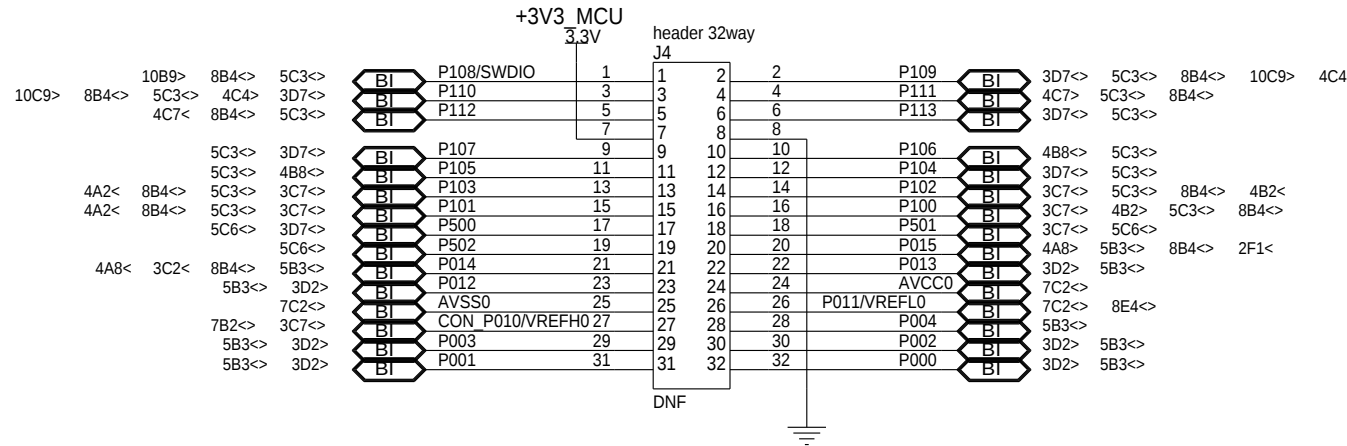
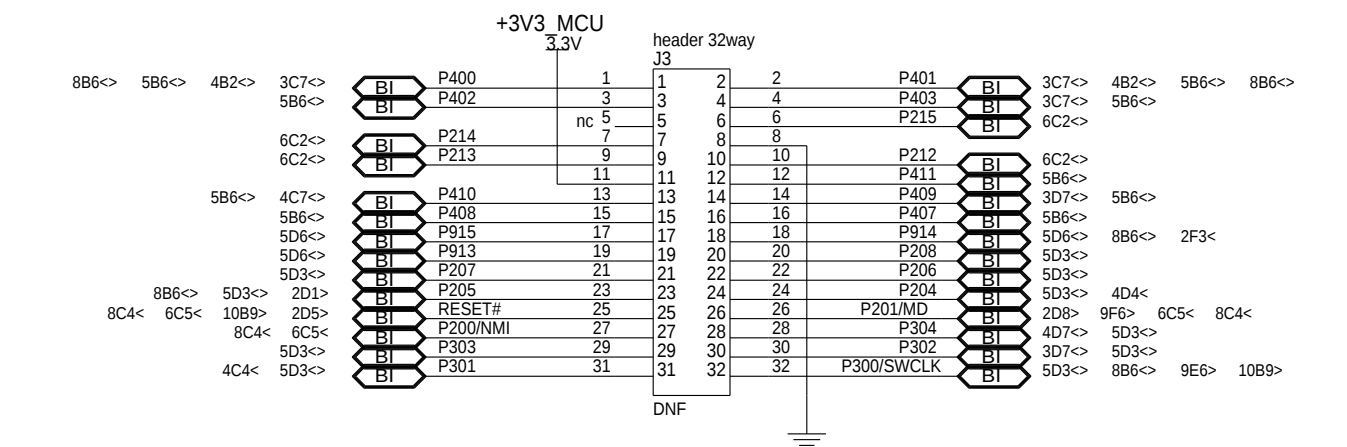




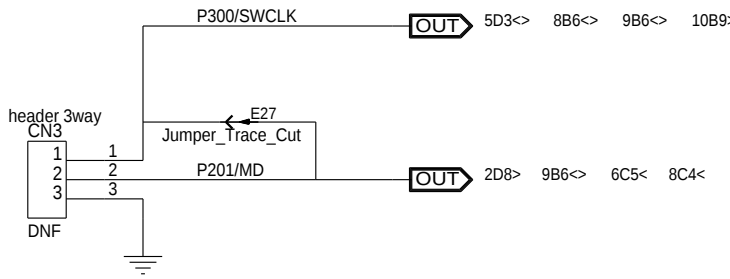




# PIN HEADERS



# DEBUG CONFIGURATION



Debug Circuit is company confidential

161-10007394  
SMD  
ZX62D-B-5PA8\_30  
CN6

