
RedyKits™ for the ADPL42005 Low Dropout (LDO) Linear Regulator

FEATURES

- Several voltage options for evaluation
- Surface-mount ADPL42005 8-lead, exposed pad SOIC and LFCSP parts that can be easily soldered to the evaluation boards
- Additional parts for prototyping

ADPL42005CPZ-REDYKIT CONTENTS

- 1 EVAL-ADPL42005CP-AZ LFCSP evaluation board with 3.3V output
- 6 ADPL42005 LFCSP voltage options in labeled bags

ADPL42005RDZ-REDYKIT CONTENTS

- 1 EVAL-ADPL42005RD-AZ SOIC evaluation board with 3.3V output
- 6 ADPL42005 SOIC voltage options in labeled bags

GENERAL DESCRIPTION

The ADPL42005CPZ-REDYKIT and ADPL42005RDZ-REDYKIT allow simplified prototyping and evaluation and are available for several fixed output voltage options in the ADPL42005 product family. The RedyKits allow the user to evaluate the voltage options with the two easy-to-order kits.

All surface-mount ADPL42005 parts are sorted and stored in the kits with the Analog Devices part number and fixed output voltage clearly printed on each zip-top bag.

The kits can be used in the engineering lab to evaluate different voltage options. If other voltage options must be evaluated, a different part from the kits can be easily soldered onto the evaluation board supplied with each kit.

The ADPL42005CPZ-REDYKIT and the ADPL42005RDZ-REDYKIT are available with an adjustable 3.3V output voltage evaluation board. The kits also contain additional parts for prototyping.

The kit enclosure, which contains individually packaged parts and the evaluation board, is packaged in an ESD foam pad inside a treated cardboard box.

EVALUATION BOARD LAYOUT AND CIRCUIT DIAGRAMS

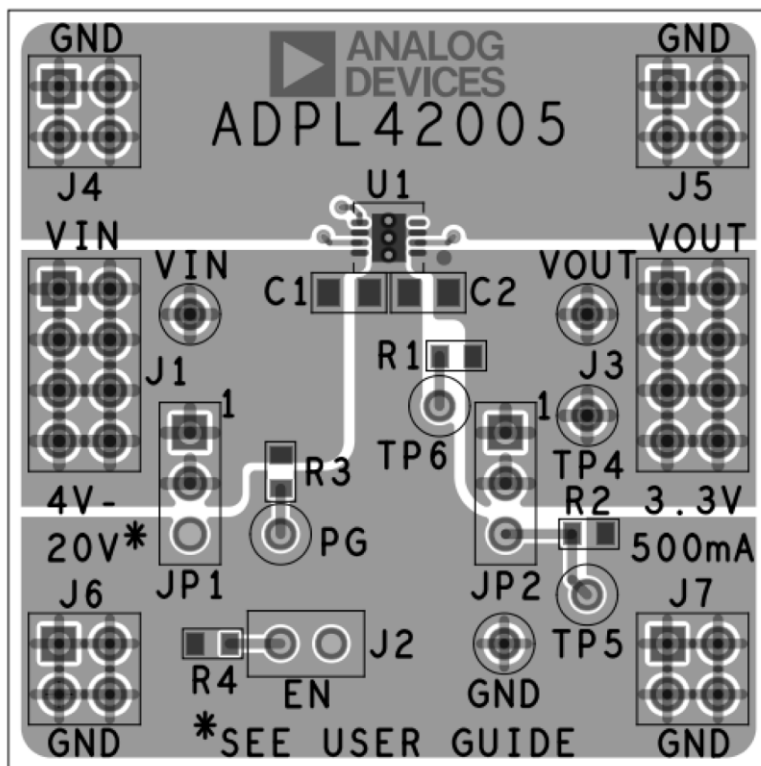


Figure 1. ADPL42005 Evaluation Board Layout, LFCSP

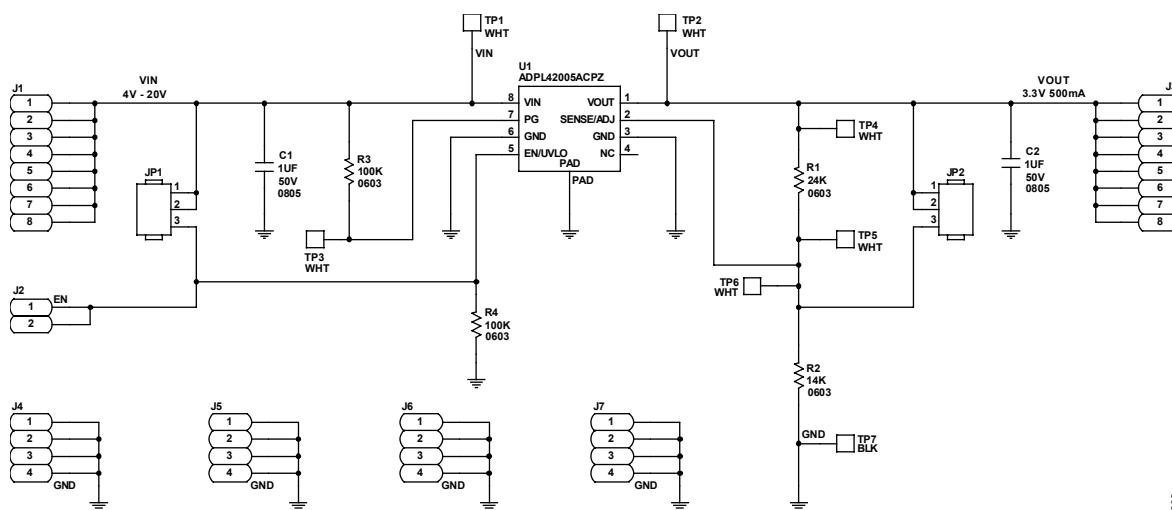


Figure 2. ADPL42005 Evaluation Board Circuit, LFCSP

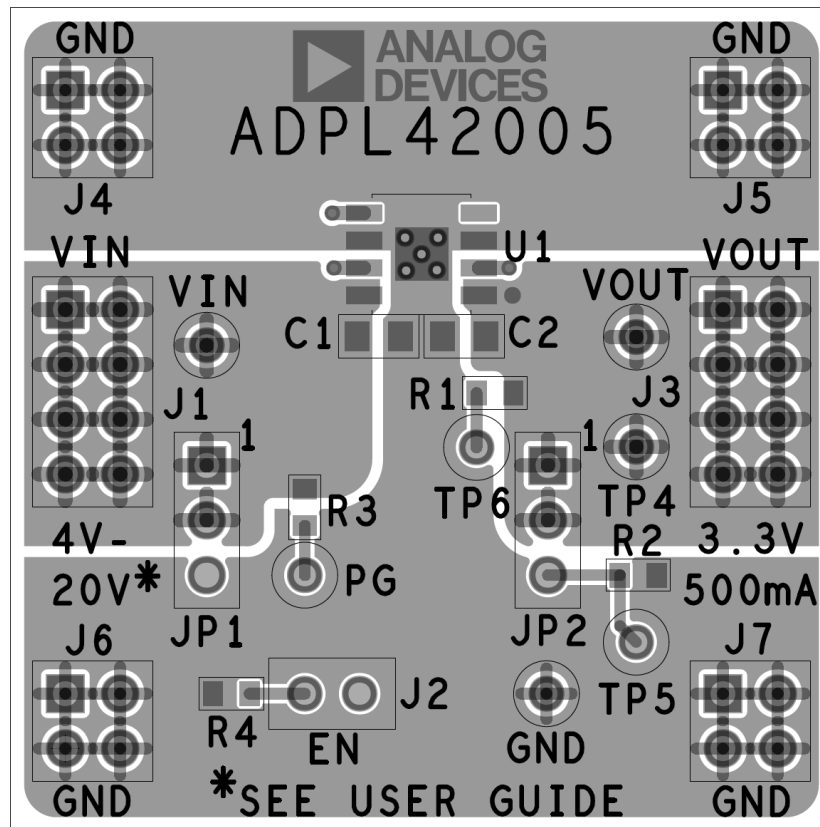


Figure 3. ADPL42005 Evaluation Board Layout, SOIC

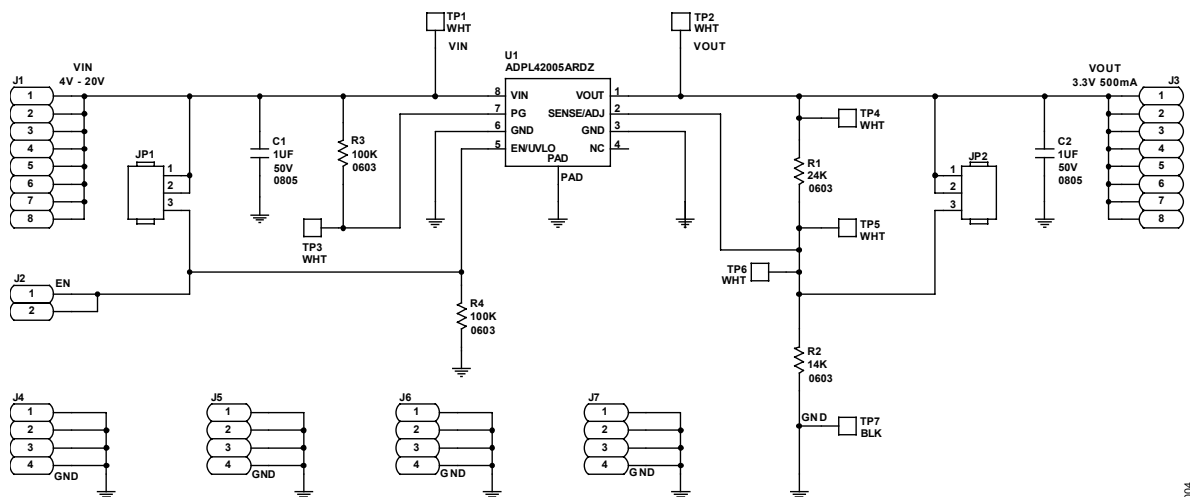


Figure 4. ADPL42005 Evaluation Board Circuit, SOIC

ORDERING INFORMATION**Bill of Materials****Table 1. ADPL42005CPZ-REDYKIT**

QUANTITY	MODEL	OUTPUT VOLTAGE (V)	DESCRIPTION
3	ADPL42005ACPZ-1.5-R7	1.5	8-Lead LFCSP_WD
3	ADPL42005ACPZ-1.8-R7	1.8	8-Lead LFCSP_WD
3	ADPL42005ACPZ-2.5-R7	2.5	8-Lead LFCSP_WD
3	ADPL42005ACPZ-3.0-R7	3.0	8-Lead LFCSP_WD
3	ADPL42005ACPZ-3.3-R7	3.3	8-Lead LFCSP_WD
3	ADPL42005ACPZ-5.0-R7	5.0	8-Lead LFCSP_WD
1	EVAL-ADPL42005CP-AZ	Adjustable (3.3)	LFCSP Evaluation Board

Table 2. ADPL42005RDZ-REDYKIT

QUANTITY	MODEL	OUTPUT VOLTAGE (V)	DESCRIPTION
3	ADPL42005ARDZ-1.5-R7	1.5	8-Lead SOIC_N_EP
3	ADPL42005ARDZ-1.8-R7	1.8	8-Lead SOIC_N_EP
3	ADPL42005ARDZ-2.5-R7	2.5	8-Lead SOIC_N_EP
3	ADPL42005ARDZ-3.0-R7	3.0	8-Lead SOIC_N_EP
3	ADPL42005ARDZ-3.3-R7	3.3	8-Lead SOIC_N_EP
3	ADPL42005ARDZ-5.0-R7	5.0	8-Lead SOIC_N_EP
1	EVAL-ADPL42005RD-AZ	Adjustable (3.3)	SOIC Evaluation Board

REVISION HISTORY

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	02/25	Initial release	—

Notes

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