



QUICK START GUIDE

VIA SOM-5000

Debian 12 EVK



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Revision History

Version	Date	Remarks
1.01	09/04/2025	Added instructions in Section 3.5 for using the VTool application to configure the supported system memory size. Added instructions in Section 3.6 for using the VTool application to configure the display settings. Updated the camid values in Section 3.9 for optional cameras connected to the MIPI CSI connectors on the VIA SOMDB7 reference carrier board.
1.00	11/11/2024	Initial release

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1. Introduction

This Quick Start Guide provides an overview of how to boot the Debian image for the VIA SOM-5000 Evaluation Kit and configure the supported hardware functions in the build.

**Note:**

The VIA SOM-5000 Evaluation Kit includes the VIA SOM-5000 module and VIA SOMDB7 reference carrier board.

The VIA SOM-5000 Debian 12 EVK is developed based on the MediaTek Yocto BSP, and it enables the hardware features of the VIA SOM-5000 Evaluation Kit.

1.1 EVK Package Contents

There are two folders in the package listed as below.

Firmware folder	Description
VIA_SOM-5000_Debian_12_EVK.zip	Debian evaluation image
Document folder	Description
VIA_SOM-5000_Debian_12_EVK_Quick_Start_Guide.pdf	Quick Start Guide

1.1.1 Firmware Folder Contents

VIA_SOM-5000_Debian_12_EVK.zip: Contains the precompiled Debian image for evaluating the VIA SOM-5000 Evaluation Kit.

1.1.2 Document Folder Contents

VIA_SOM-5000_Debian_12_EVK_Quick_Start_Guide.pdf: This Quick Start Guide provides an overview on how to boot the Debian image for the VIA SOM-5000 Evaluation Kit and configure the supported hardware functions in the build.

1.2 Version Information and Supported Features

- Kernel version: 5.15.47
- Evaluation image: Debian 12
- Development based on MediaTek Yocto BSP
- Supports eMMC boot
- Supports HDMI 4K display and audio outputs
- Supports MIPI DSI capacitive touch panels:
 - NuWhole 7" NT070R11-12ACR5 (1200 x 1920)
 - Goodix I2C touch
- Supports debugging using the debug console
- Supports RGMII and USB 3.0 Gigabit Ethernet
- Supports MediaTek MT6365 Headphone-out and Mic-in
- Supports the optional AzureWave AW-XB468NF (MT7921) dual-band Wi-Fi 6 and Bluetooth 5.2 module
- Supports the optional 4G LTE mobile broadband M.2 modules:
 - Telit LN920A6
 - Quectel EM05-G
- Supports the optional MIPI CSI IMX258 and OV5648 camera modules
- Supports MediaTek NeuroPilot AI APU hardware acceleration

2. Image Installation

This section explains the setup requirements for installing the Debian evaluation image on the VIA SOM-5000 Evaluation Kit.

The precompiled images are provided in the "Firmware" folder.

2.1 Installing with the Genio Tool

Follow the steps below to install the Debian evaluation image:

Step 1

Refer to the instructions described in the following sections of the [MediaTek IoT Environment Setup](#) and set up the Genio tool environment on a Windows host machine:

- Setup Git for Windows
- Install Fastboot and ADB Device USB Driver
- Install Fastboot Tool on Windows
- Setup Python 3 on Windows
- Install Genio Tools

Step 2

Connect the Windows host machine and the VIA SOM-5000 Evaluation Kit through the Micro USB 2.0 port using a Micro USB cable. The Micro USB 2.0 port is located on the back panel of the VIA SOM-5000 Evaluation Kit.



Step 3

Extract the Debian 12 evaluation image package "VIA_SOM-5000_Debian_12_EVK.zip" on the Windows host machine.

Step 4

The VIA SOM-5000 Evaluation Kit is now ready to be flashed with the provided Debian evaluation image. Set the path to the "platform tools" folder, and run the following commands.

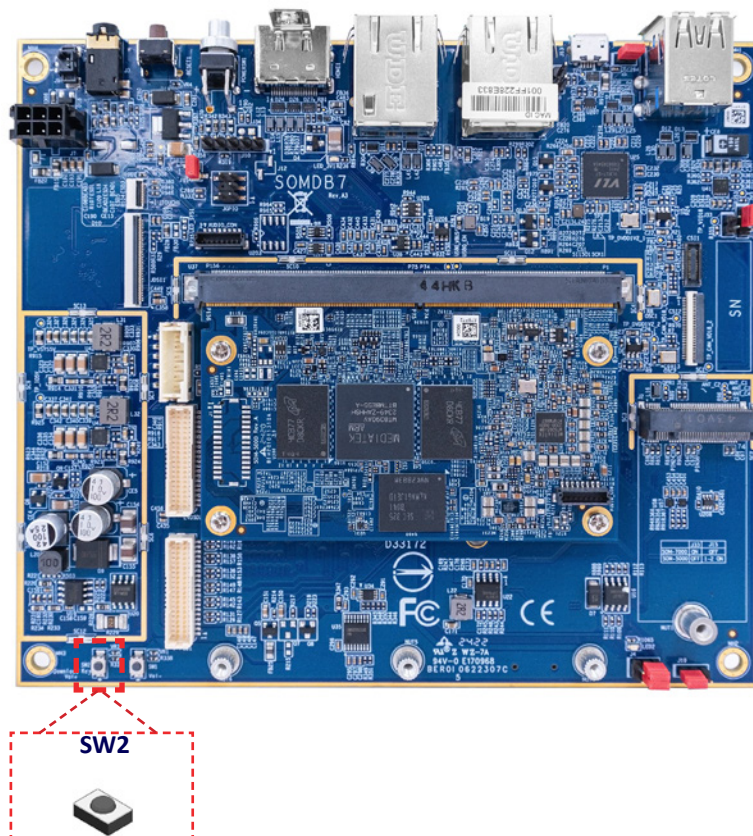
```
D:\VIA_SOM-5000_Debian_12_EVK>set PATH=D:\platform-tools;%PATH%
D:\VIA_SOM-5000_Debian_12_EVK>genio-flash
Genio Tools: v1.4.1
Yocto Image:
  name:      Debian GNU/Linux 12 (bookworm) (debian-12.7)
  distro:    debian 12.7 (debian)
  codename:  None
  machine:   som-5000
  overlays:  ['video.dtbo', 'apusys.dtbo', 'gpu-mali.dtbo']
```

```
WARNING:aiot:Cannot find any FTDI device
WARNING:aiot:Unable to find and reset the board. Possible causes are:
1. This is not a Genio 350/700 EVK, nor a Pumpkin board.
2. The board port UART0 is not connected.
3. The UART0 port is being opened by another tool, such as TeraTerm on Windows.
You can now manually reset the board into DOWNLOAD mode.
```

```
INFO:aiot:Continue flashing...
Looking for MediaTek SoC matching USB device 0e8d:0003
```

Step 5

Next, press and hold the SW2 download key located on the top layer of the VIA SOM-5000 Evaluation Kit, and plug in the AC-to-DC power adapter. The VIA SOM-5000 Evaluation Kit will automatically power on.



Location of the SW2 key



Note:

After powering ON the VIA SOM-5000 Evaluation Kit, make sure the Windows host machine detects it as an ADB device.

Step 6

Messages will start printing to show that the flashing process is loading, and will then pause to find the VIA SOM-5000 Evaluation Kit in bootloader mode. When the VIA SOM-5000 Evaluation Kit is found, the flashing process will begin and messages will start printing to show its progress. When the first message is printed, release the SW2 download key.

```
D:\VIA_SOM-5000_Debian_12_EVK>set PATH=D:\platform-tools;%PATH%
D:\VIA_SOM-5000_Debian_12_EVK>genio-flash
Genio Tools: v1.4.1
Yocto Image:
  name:      Debian GNU/Linux 12 (bookworm) (debian-12.7)
  distro:    debian 12.7 (debian)
  codename:  None
  machine:   som-5000
  overlays:  ['video.dtbo', 'apusys.dtbo', 'gpu-mali.dtbo']

WARNING:aiot:Cannot find any FTDI device
WARNING:aiot:Unable to find and reset the board. Possible causes are:
1. This is not a Genio 350/700 EVK, nor a Pumpkin board.
2. The board port UART0 is not connected.
3. The UART0 port is being opened by another tool, such as TeraTerm on Windows.
You can now manually reset the board into DOWNLOAD mode.

INFO:aiot:Continue flashing...
Looking for MediaTek SoC matching USB device 0e8d:0003
Opening COM5 using baudrate=115200
Connected to MediaTek SoC: hw_code[0x8188]
Sending bootstrap to address: 0x201000
Jumping to bootstrap at address 0x201000 in AArch64 mode
erasing mmc0
< waiting for any device >
Erasing 'mmc0' (bootloader) request sz: 0x3a3e0000,
real erase len: 0x0
OKAY [ 0.749s]
Finished. Total time: 0.754s
```

Step 7

When the flashing process is complete, confirmation messages will be printed as shown below.

```
D:\VIA_SOM-5000_Debian_12_EVK>set PATH=D:\platform-tools;%PATH%
D:\VIA_SOM-5000_Debian_12_EVK>genio-flash
Genio Tools: v1.4.1
Yocto Image:
  name:      Debian GNU/Linux 12 (bookworm) (debian-12.7)
  distro:    debian 12.7 (debian)
  codename:  None
  machine:   som-5000
  overlays:  ['video.dtbo', 'apusys.dtbo', 'gpu-mali.dtbo']

WARNING:aiot:Cannot find any FTDI device
WARNING:aiot:Unable to find and reset the board. Possible causes are:
1. This is not a Genio 350/700 EVK, nor a Pumpkin board.
2. The board port UART0 is not connected.
3. The UART0 port is being opened by another tool, such as TeraTerm on Windows.
You can now manually reset the board into DOWNLOAD mode.

INFO:aiot:Continue flashing...
Looking for MediaTek SoC matching USB device 0e8d:0003
Opening COM5 using baudrate=115200
Connected to MediaTek SoC: hw_code[0x8188]
Sending bootstrap to address: 0x201000
Jumping to bootstrap at address 0x201000 in AArch64 mode
erasing mmc0
< waiting for any device >
Erasing 'mmc0' (bootloader) request sz: 0x3a3e0000,
real erase len: 0x0
OKAY [ 0.749s]
Finished. Total time: 0.754s
```

```

erasing mmc0boot0
Erasing 'mmc0boot0' (bootloader) request sz: 0x400000, real
erase len: 0x400000
OKAY [ 0.062s]
Finished. Total time: 0.083s
erasing mmc0boot1
Erasing 'mmc0boot1' (bootloader) request sz: 0x400000, real
erase len: 0x400000
OKAY [ 0.016s]
Finished. Total time: 0.016s
flashing mmc0=debian-12.7-som-5000.wic.img
Sending sparse 'mmc0' 1/25 (243525 KB) OKAY [ 8.790s]
Writing 'mmc0' OKAY [ 10.513s]
Sending sparse 'mmc0' 2/25 (257407 KB) OKAY [ 9.916s]
Writing 'mmc0' OKAY [ 10.476s]
Sending sparse 'mmc0' 3/25 (262051 KB) OKAY [ 9.935s]
Writing 'mmc0' OKAY [ 11.427s]
Sending sparse 'mmc0' 4/25 (242275 KB) OKAY [ 9.514s]
Writing 'mmc0' OKAY [ 11.454s]
Sending sparse 'mmc0' 5/25 (249091 KB) OKAY [ 10.026s]
Writing 'mmc0' OKAY [ 11.211s]
Sending sparse 'mmc0' 6/25 (259939 KB) OKAY [ 10.115s]
Writing 'mmc0' OKAY [ 10.822s]
Sending sparse 'mmc0' 7/25 (261327 KB) OKAY [ 10.113s]
Writing 'mmc0' OKAY [ 10.622s]
Sending sparse 'mmc0' 8/25 (262013 KB) OKAY [ 10.427s]
Writing 'mmc0' OKAY [ 11.540s]
Sending sparse 'mmc0' 9/25 (262083 KB) OKAY [ 10.494s]
Writing 'mmc0' OKAY [ 11.463s]
Sending sparse 'mmc0' 10/25 (254126 KB) OKAY [ 10.342s]
Writing 'mmc0' OKAY [ 11.986s]
Sending sparse 'mmc0' 11/25 (255986 KB) OKAY [ 10.456s]
Writing 'mmc0' OKAY [ 11.571s]
Sending sparse 'mmc0' 12/25 (262126 KB) OKAY [ 38.875s]
Writing 'mmc0' OKAY [ 11.486s]
Sending sparse 'mmc0' 13/25 (261085 KB) OKAY [ 40.381s]
Writing 'mmc0' OKAY [ 11.498s]
Sending sparse 'mmc0' 14/25 (260512 KB) OKAY [ 32.293s]
Writing 'mmc0' OKAY [ 10.393s]
Sending sparse 'mmc0' 15/25 (261369 KB) OKAY [ 35.518s]
Writing 'mmc0' OKAY [ 11.681s]
Sending sparse 'mmc0' 16/25 (260397 KB) OKAY [ 17.850s]
Writing 'mmc0' OKAY [ 11.509s]
Sending sparse 'mmc0' 17/25 (258322 KB) OKAY [ 16.791s]
Writing 'mmc0' OKAY [ 11.843s]
Sending sparse 'mmc0' 18/25 (261879 KB) OKAY [ 13.487s]
Writing 'mmc0' OKAY [ 11.467s]
Sending sparse 'mmc0' 19/25 (260856 KB) OKAY [ 10.150s]
Writing 'mmc0' OKAY [ 11.242s]
Sending sparse 'mmc0' 20/25 (241259 KB) OKAY [ 9.685s]
Writing 'mmc0' OKAY [ 9.863s]
Sending sparse 'mmc0' 21/25 (259515 KB) OKAY [ 10.410s]
Writing 'mmc0' OKAY [ 10.375s]
Sending sparse 'mmc0' 22/25 (261968 KB) OKAY [ 10.250s]
Writing 'mmc0' OKAY [ 10.688s]
Sending sparse 'mmc0' 23/25 (261440 KB) OKAY [ 11.076s]
Writing 'mmc0' OKAY [ 13.116s]
Sending sparse 'mmc0' 24/25 (260646 KB) OKAY [ 10.535s]
Writing 'mmc0' OKAY [ 11.731s]
Sending sparse 'mmc0' 25/25 (116627 KB) OKAY [ 4.717s]
Writing 'mmc0' OKAY [ 4.993s]
Finished. Total time: 671.793s
flashing mmc0boot0=b12.img
Warning: skip copying mmc0boot0 image avb footer (mmc0boot0 partition size: 0, mmc0boot0
image size: 279896).
Sending 'mmc0boot0' (273 KB) OKAY [ 0.016s]
Writing 'mmc0boot0' OKAY [ 0.016s]
Finished. Total time: 0.094s
flashing mmc0boot1=u-boot-env.bin
Warning: skip copying mmc0boot1 image avb footer (mmc0boot1 partition size: 0, mmc0boot1
image size: 4096).

```

```
Sending 'mmc0boot1' (4 KB)          OKAY [ 0.000s]
Writing 'mmc0boot1'                OKAY [ 0.000s]
Finished. Total time: 0.031s
Rebooting                          OKAY [ 0.000s]
Finished. Total time: 0.000s

D:\VIA_SOM-5000_Debian_12_EVK>
```

Step 8

Unplug the AC-to-DC power adapter to power off the VIA SOM-5000 Evaluation Kit.

Step 9

Unplug the Micro USB cable, press the Power Button for 2 seconds and release it to power on the VIA SOM-5000 Evaluation Kit.

When the boot process has completed, you will see the Debian 12 desktop.

3. Hardware Functions

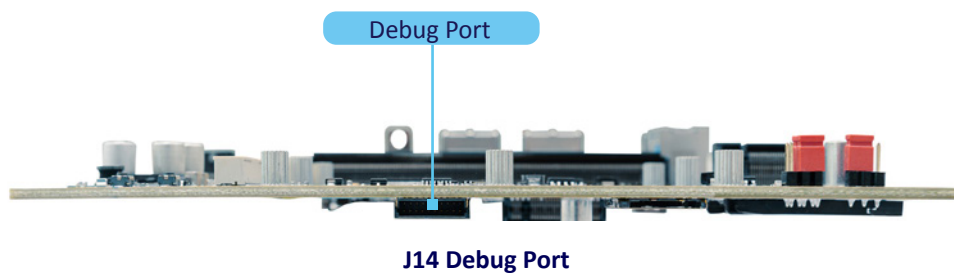
This section explains how to enable and test the hardware functions precompiled in the VIA SOM-5000 Debian 12 EVK.

3.1 Using the Debug Console

Follow the steps below to use the debug console:

Step 1

Connect the Windows host machine and the VIA SOM-5000 Evaluation Kit through the "J14" debug port using the provided debug cable. The "J14" debug port is located on the front panel of the VIA SOM-5000 Evaluation Kit.

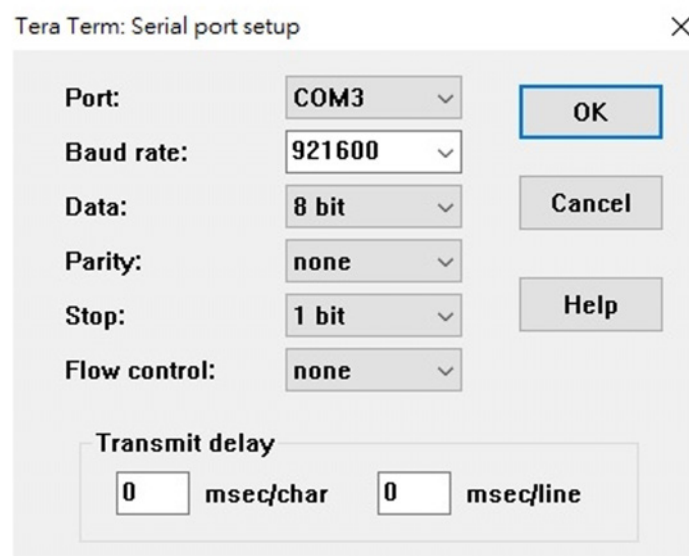


Step 2

Use a serial port communication program such as PuTTY or Tera Term to connect the debug console.

Step 3

Select the correct serial port and set the Console Baud Rate to "921600".



Step 4

Power on the VIA SOM-5000 Evaluation Kit to initiate the boot process.

Step 5

When the VIA SOM-5000 Evaluation Kit has completed booting, log in to the debug console. The default username is "debian" and no password is required.

3.2 Changing the Kernel Debug Level

To disable the kernel messages, modify the debug level using the following commands:

```
debian@som-5000:~$ sudo su
root@som-5000:/home/debian# echo 3 > /proc/sys/kernel/printk
```

3.3 Checking the BSP Version

To check the BSP version, use the following command:

```
root@som-5000:/home/debian# cat /etc/via-release
```

3.4 DVFS

To verify the DVFS (Dynamic Voltage Frequency Scaling) function and list all the supported features, use the following commands:

```
root@som-5000:/home/debian# ls -l /sys/devices/system/cpu/cpu0/cpufreq/
total 0
-r--r--r-- 1 root root 4096 Aug 26 11:15 affected_cpus
-r----- 1 root root 4096 Aug 26 11:15 cpuinfo_cur_freq
-r--r--r-- 1 root root 4096 Aug 26 11:15 cpuinfo_max_freq
-r--r--r-- 1 root root 4096 Aug 26 11:15 cpuinfo_min_freq
-r--r--r-- 1 root root 4096 Aug 26 11:15 cpuinfo_transition_latency
-r--r--r-- 1 root root 4096 Aug 26 11:15 related_cpus
-r--r--r-- 1 root root 4096 Aug 26 11:15 scaling_available_frequencies
-r--r--r-- 1 root root 4096 Aug 26 11:15 scaling_available_governors
-r--r--r-- 1 root root 4096 Aug 26 11:15 scaling_cur_freq
-r--r--r-- 1 root root 4096 Aug 26 11:15 scaling_driver
-rw-r--r-- 1 root root 4096 Aug 26 11:15 scaling_governor
-rw-r--r-- 1 root root 4096 Aug 26 11:15 scaling_max_freq
-rw-r--r-- 1 root root 4096 Aug 26 11:15 scaling_min_freq
-rw-r--r-- 1 root root 4096 Aug 26 11:15 scaling_setspeed
drwxr-xr-x 2 root root 0 Aug 26 11:15 schedutil
drwxr-xr-x 2 root root 0 Aug 26 11:15 stats
root@som-5000:/home/debian#
```

To check the supported and current CPU frequency, use the following commands:

```
root@som-5000:/home/debian# cat /sys/devices/system/cpu/cpu0/cpufreq/scaling_available_frequencies
2000000 1903000 1807000 1710000 1614000 1517000 1421000 1325000 1228000 1132000 1035000
939000 842000 746000 650000 500000
root@som-5000:/home/debian# cat /sys/devices/system/cpu/cpu0/cpufreq/cpuinfo_cur_freq
1325000
```

3.5 System Memory

Follow instructions in steps 1 - 4 of [section 3.1 Using the Debug Console](#) to initiate the boot process. When prompted, press any key to stop the boot process and enter the U-Boot console as shown below:

```
U-Boot 2022.10-g1908346e2f (Jul 04 2024 - 08:09:40 +0000)

CPU:   MediaTek MT8390
DRAM:  4 GiB
Enabling SCP SRAM
Core: 75 devices, 24 uclasses, devicetree: separate
MMC:   mmc@11230000: 0
Loading Environment from MMC... OK
Warning: device tree node '/config/environment' not found
In:     serial
Out:    serial
Err:    serial
Net:    eth0: usb_ether
Hit any key to stop autoboot: 0
=>
```

U-Boot Console View

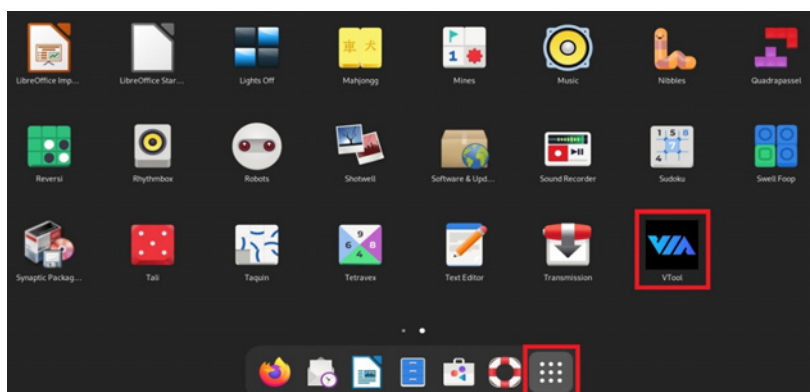
To set 4GB as the supported system memory size, use the following commands:

```
=> setenv boot_conf "#conf-mediatek_som-5000.dtb#conf-video.dtb#conf-gpu-mali.dtb#conf-
apusys.dtb#conf-dram-4gb.dtb"
=> setenv list_dtbo video.dtb gpu-mali.dtb apusys.dtb dram-4gb.dtb
=> saveenv
=> reset
```

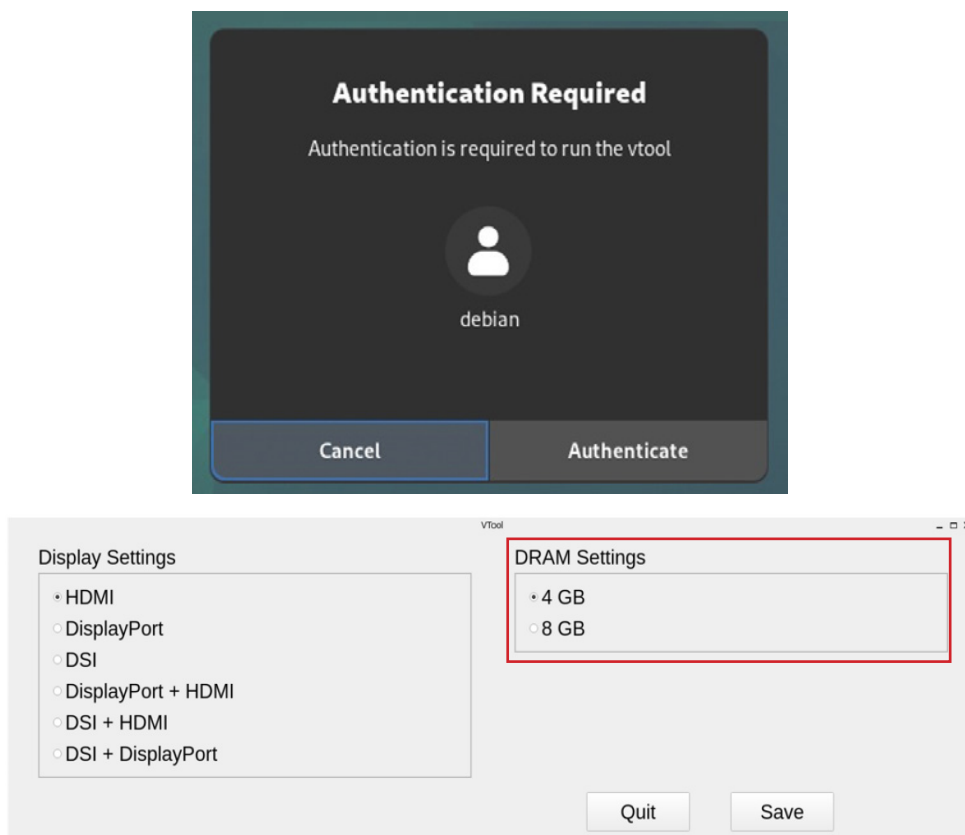
To set 8GB as the supported system memory size, use the following commands:

```
=> setenv boot_conf "#conf-mediatek_som-5000.dtb#conf-video.dtb#conf-gpu-mali.dtb#conf-
apusys.dtb#conf-dram-8gb.dtb"
=> setenv list_dtbo video.dtb gpu-mali.dtb apusys.dtb dram-8gb.dtb
=> saveenv
=> reset
```

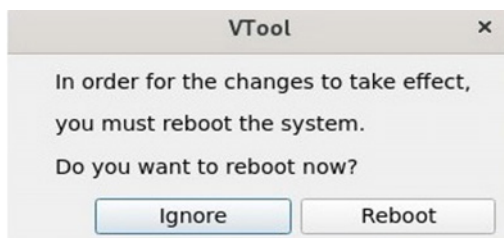
Supported system memory size can also be configured through the VTool application, which can be found as shown below. Launch the application.



When the VTool application is launched, a dialog box will be displayed requesting for user authentication to run the application. Upon clicking "Authenticate", the VTool application will open. Select the desired supported system memory size on the right side of the window and save the configuration.



When a window pops up, click "Reboot" to make the change effective immediately, or "Ignore" to make the change effective on the next boot.


Note:

If the VIA SOM-5000 Evaluation Kit has 4GB of system memory onboard but 8GB is set as the supported system memory size, the EVK firmware will fail to boot.

3.6 Display

Follow instructions in steps 1 - 4 of [section 3.1 Using the Debug Console](#) to initiate the boot process. When prompted, press any key to stop the boot process and enter the U-Boot console as shown below:

```
U-Boot 2022.10-g1908346e2f (Jul 04 2024 - 08:09:40 +0000)

CPU:   MediaTek MT8390
DRAM:  4 GiB
Enabling SCP SRAM
Core:  75 devices, 24 uclasses, devicetree: separate
MMC:   mmc@11230000: 0
Loading Environment from MMC... OK
Warning: device tree node '/config/environment' not found
In:     serial
Out:    serial
Err:    serial
Net:    eth0: usb_ether
Hit any key to stop autoboot:  0
=>
```

U-Boot Console View

To set an HDMI monitor as the display output, use the following commands:

```
=> setenv boot_conf "#conf-mediatek_som-5000.dtb#conf-apusys.dtb#conf-gpu-mali.dtb#conf-
video.dtb"
=> setenv list_dtb apusys.dtb gpu-mali.dtb video.dtb
=> saveenv
=> reset
```

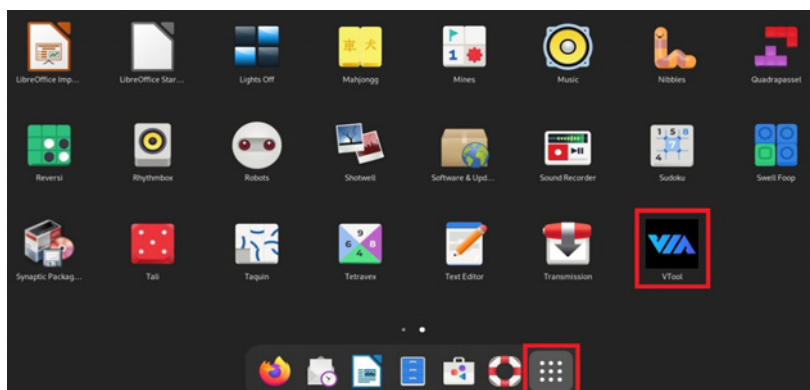
To set the provided LCD panel as the display output, use the following commands:

```
=> setenv boot_conf "#conf-mediatek_som-5000.dtb#conf-apusys.dtb#conf-gpu-mali.dtb#conf-
video.dtb#conf-display-dsi.dtb"
=> setenv list_dtb apusys.dtb gpu-mali.dtb video.dtb display-dsi.dtb
=> saveenv
=> reset
```

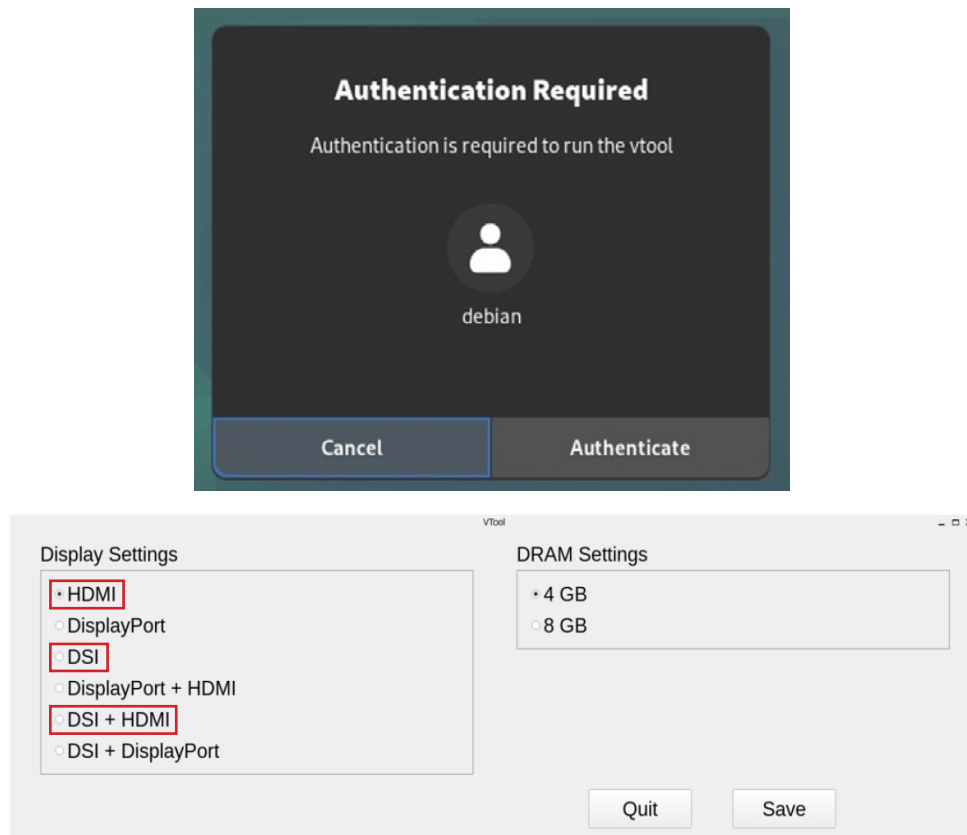
To set an HDMI monitor and the provided LCD panel as dual display outputs, use the following commands:

```
=> setenv boot_conf "#conf-mediatek_som-5000.dtb#conf-apusys.dtb#conf-gpu-mali.dtb#conf-
video.dtb#conf-display-dsi-hdmi.dtb"
=> setenv list_dtb apusys.dtb gpu-mali.dtb video.dtb display-dsi-hdmi.dtb
=> saveenv
=> reset
```

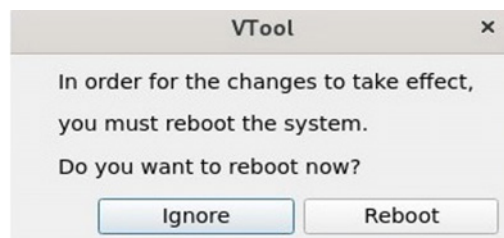
Display settings can also be configured through the VTool application, which can be found as shown below. Launch the application.



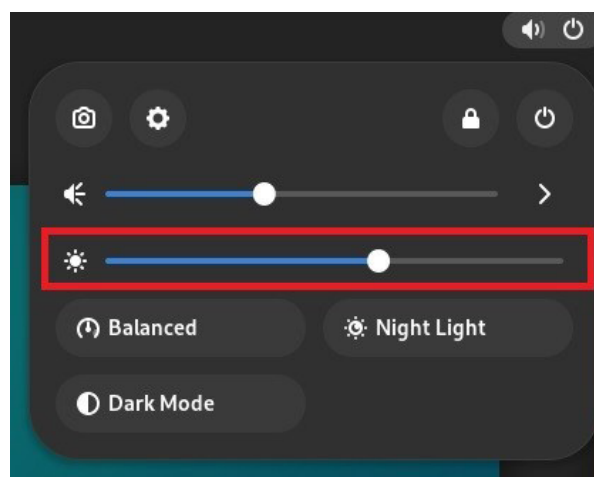
When the VTool application is launched, a dialog box will be displayed requesting for user authentication to run the application. Upon clicking "Authenticate", the VTool application will open. Select the desired display setting on the left side of the window and save the configuration.



When a window pops up, click "Reboot" to make the change effective immediately, or "Ignore" to make the change effective on the next boot.



To control the LCD backlight, use the standard system settings and adjust the scroll bar.



3.7 Video Playback

The VIA SOM-5000 Evaluation Kit supports H.265 and H.264 video decoding up to 4K@75fps/160Mbps.

Copy the "test.mp4" video file to a USB drive and connect the USB drive to the VIA SOM-5000 Evaluation Kit.

To playback H.265 or H.264 video, use the following command:

```
debian@som-5000:~$ vplay.sh -f /media/debian/USB/test.mp4
```



Note:

The path "/media/debian/USB/" in the above example is the location of the kernel module package "module.tgz" on the USB flash drive.

3.8 Audio Output and Recording

Copy the "test.wav" audio file to a USB drive and connect the USB drive to the VIA SOM-5000 Evaluation Kit.

To set up the HDMI audio output, navigate to "Settings > Sound > Output Device" and select "HDMI output - Built-in Audio".



To set up the headphone audio output, navigate to "Settings > Sound > Output Device" and select "Earphone speaker - Built-in Audio".



To playback the HDMI or headphone audio output, use the following command:

```
debian@som-5000:~$ aplay /media/debian/USB/test.wav
```



Note:

The "/media/debian/USB/" path in this example is of the USB drive.

To change the audio volume, navigate to "Settings > Sound > System Volume" and adjust the scroll bar.



To set up the Mic-in audio recording, navigate to "Settings > Sound > Input Device" and select "Earphone microphone - Built-in Audio".

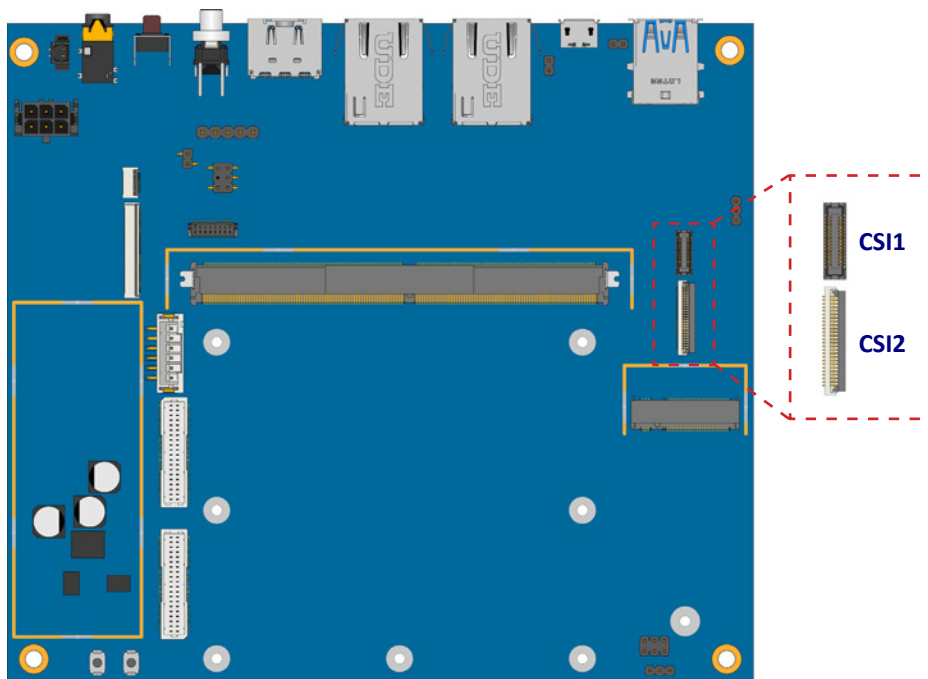


To record the Mic-in audio input, use the following command:

```
debian@som-5000:~$ arecord -c 2 -r 48000 -f S16_LE /tmp/test.wav
Recording WAVE '/tmp/test.wav' : Signed 16 bit Little Endian, Rate 48000 Hz, Stereo
```

3.9 Camera

The VIA SOM-5000 Evaluation Kit comes with two MIPI CSI connectors "CSI1" and "CSI2" located on the VIA SOMDB7 carrier board.



Location of the MIPI CSI Connectors

To preview an image from an optional CSI camera, use the following command:

```
debian@som-5000:~$ vcamera.sh -t csi -c <camid> -m preview -r 1920x1080
```

The <camid> value depends on which MIPI CSI connectors are the optional cameras connected to. Check the table below for reference.

MIPI CSI Camera Connection Status		<camid> Value	
CSI2	CSI1	CSI2	CSI1
Connected	Connected	0	1
Connected	Not Connected	0	None
Not Connected	Connected	None	1

To capture an image from an optional CSI camera, use the following command:

```
debian@som-5000:~$ vcamera.sh -t csi -c <camid> -m capture
```



Note:

If the optional CSI IMX258 camera is connected, the captured image will be saved as "csi-camera-<camid>-capture-1440x1088.jpg" in the "/home/debian/" directory.

If the optional CSI OV5648 camera is connected, the captured image will be saved as "csi-camera-<camid>-capture-1920x1080.jpg" in the "/home/debian/" directory.

To record a video file from an optional CSI camera, use the following command:

```
debian@som-5000:~$ vcamera.sh -t csi -c <camid> -m encode
```



Note:

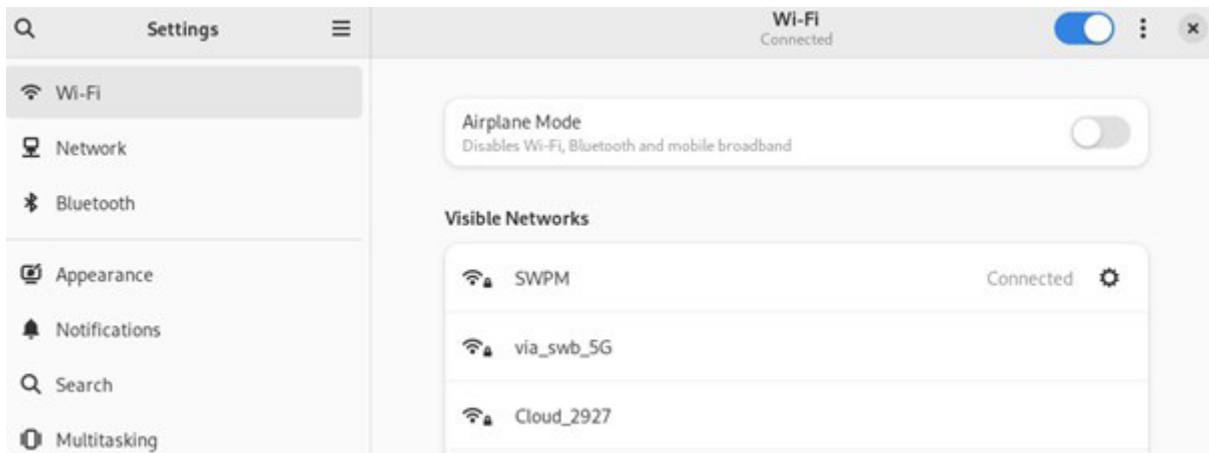
The recorded video will be saved as "csi-camera-<camid>-encode-1280x720.mp4" in the "/home/debian/" directory

To playback a recorded video file from an optional CSI camera, use the following command:

```
debian@som-5000:~$ vplay.sh -f csi-camera-<camid>-encode-1280x720.mp4 -a 0
```

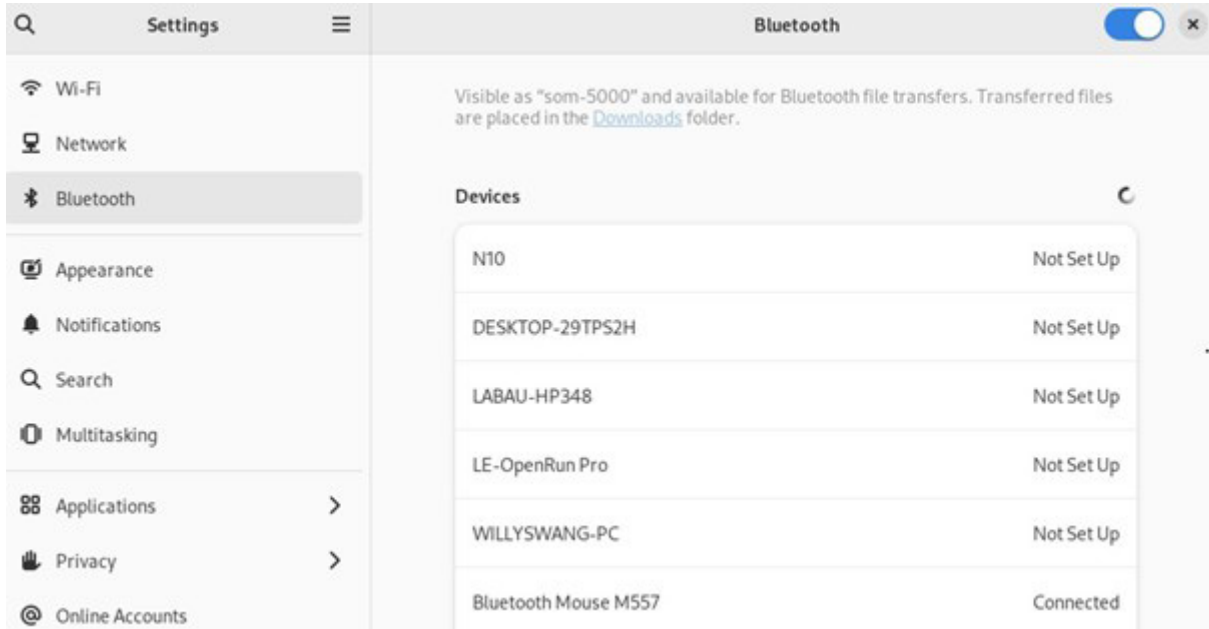
3.10 Wi-Fi

To verify the Wi-Fi function, navigate to "Settings > Wi-Fi" and select a desired "Visible Networks" for connection.



3.11 Bluetooth

To verify the Bluetooth function, navigate to "Settings > Bluetooth" and select a desired "Devices" for connection.



3.12 MTK NeuroPilot AI APU Hardware Acceleration

To verify MTK NeuroPilot AI APU hardware acceleration, use the following commands

```
root@som-5000:/home/debian# python3 /usr/share/benchmark_dla/benchmark.py --auto
2024-11-07 10:27:19,272 [INFO] /usr/share/benchmark_dla/ResNet50V2_224_1.0_quant.tflite,
mdla3.0, avg inference time: 6.05
/usr/share/benchmark_dla/ResNet50V2_224_1.0_quant.tflite, mdla3.0, avg inference time: 6.05
2024-11-07 10:27:26,955 [INFO] /usr/share/benchmark_dla/ResNet50V2_224_1.0_quant.tflite,
vpu, avg inference time: 59.31
/usr/share/benchmark_dla/ResNet50V2_224_1.0_quant.tflite, vpu, avg inference time: 59.31
2024-11-07 10:27:27,593 [INFO] /usr/share/benchmark_dla/mobilenet_v2_1.0_224_quant.tflite,
mdla3.0, avg inference time: 1.04
/usr/share/benchmark_dla/mobilenet_v2_1.0_224_quant.tflite, mdla3.0, avg inference time:
1.04
2024-11-07 10:27:29,801 [INFO] /usr/share/benchmark_dla/mobilenet_v2_1.0_224_quant.tflite,
vpu, avg inference time: 17.16
/usr/share/benchmark_dla/mobilenet_v2_1.0_224_quant.tflite, vpu, avg inference time: 17.16
2024-11-07 10:27:30,701 [INFO] /usr/share/benchmark_dla/ssd_mobilenet_v1_coco_quantized.
tflite, mdla3.0, avg inference time: 2.62
/usr/share/benchmark_dla/ssd_mobilenet_v1_coco_quantized.tflite, mdla3.0, avg inference
time: 2.62
2024-11-07 10:27:33,915 [INFO] /usr/share/benchmark_dla/ssd_mobilenet_v1_coco_quantized.
tflite, vpu, avg inference time: 24.04
/usr/share/benchmark_dla/ssd_mobilenet_v1_coco_quantized.tflite, vpu, avg inference time:
24.04
2024-11-07 10:27:35,428 [INFO] /usr/share/benchmark_dla/inception_v3_quant.tflite, mdla3.0,
avg inference time: 6.86
/usr/share/benchmark_dla/inception_v3_quant.tflite, mdla3.0, avg inference time: 6.86
2024-11-07 10:27:44,451 [INFO] /usr/share/benchmark_dla/inception_v3_quant.tflite, vpu, avg
inference time: 74.31
/usr/share/benchmark_dla/inception_v3_quant.tflite, vpu, avg inference time: 74.31
```

If the program runs and outputs results with "mdla3.0, avg inference time" or "vpu, avg inference time", MTK NeuroPilot AI APU hardware acceleration is enabled and running correctly.



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