

TABLE OF CONTENTS

Table of Contents	
01	TITLE PAGE
02	NAFE33352 IC
03	POWER SUPPLY
04	ARDUINO HEADERS

JUMPER SETTINGS

REF DES	JUMPER(DEFAULT)	PAGE NAME
J15	OPEN	02-NAFE33352 IC
J7	CLOSE	03-POWER SUPPLY

REF DES	ASSY OPT	PAGE NAME
R21	DNP	02-NAFE33352 IC

NAFE33352-UIOM

SCHEMATIC NOTES:

Text is this COLOR are notes in the schematic.

SILKSCREEN:

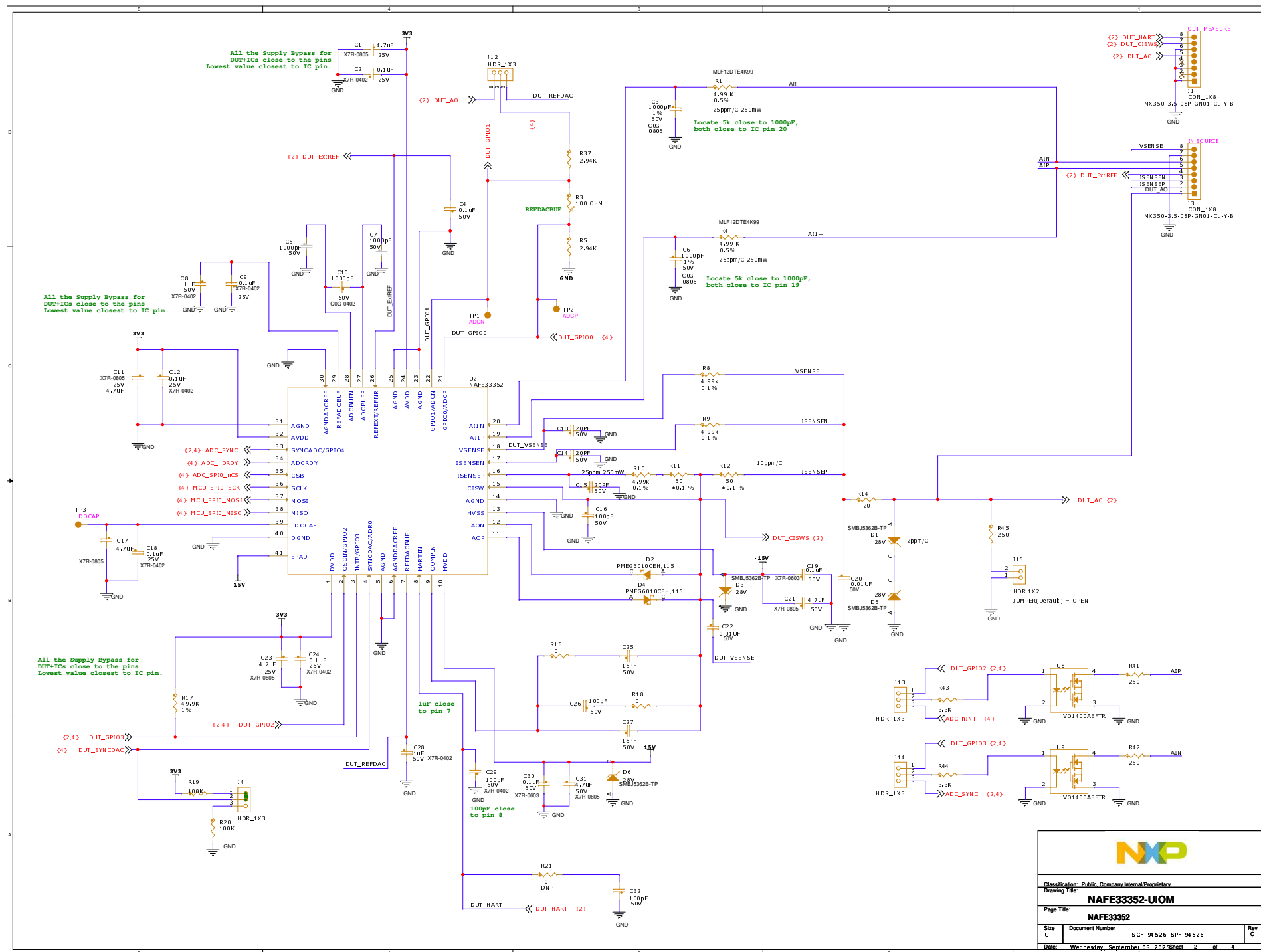
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CAD NOTES:

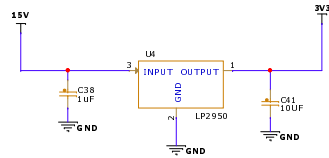
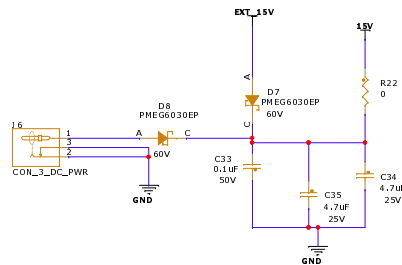
Text in this COLOR are CAD Notes

Revision History			
Rev	Description	Date	APPROVED
X1	Initial Release	7-17-2024	Madhura Tapse
X2	Removed several expensive parts for BOM cost reduction	10-16-2024	Madhura Tapse
X3	Made several part substitutions for BOM cost reduction	11-8-2024	Madhura Tapse
X4	Made several more part substitutions for BOM cost reduction	11-15-2024	Madhura Tapse
A	Final release of schematic	12-3-2024	Madhura Tapse
B	R14 to ISENIP, Changed R21 to 0 ohm, Moved DUT_ExtREF to J3.4.	7-30-2025	Madhura Tapse
C	Added R45 and, J14, Changed R41, R42, R45 to 250ohm/0.5W, updated J6	9-3-2025	Madhura Tapse

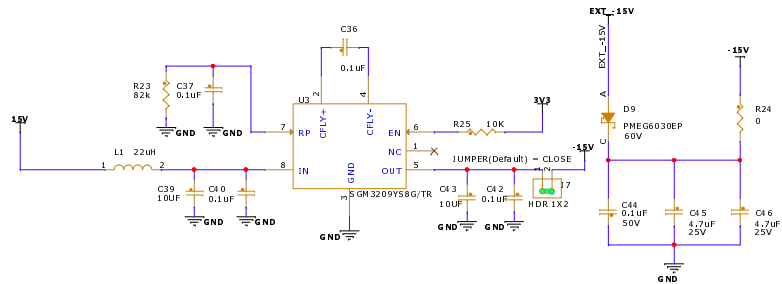
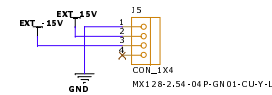
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Drawn by: Michael Steffen	Page Title: Title, ToC and Rev History		
Approved: Madhura	Size C	Document Number SCH-94526, SPF-94526	Rev C
Date: Wednesday, September 03, 2025 Sheet 1 of 4			



MCU POWER SUPPLIES

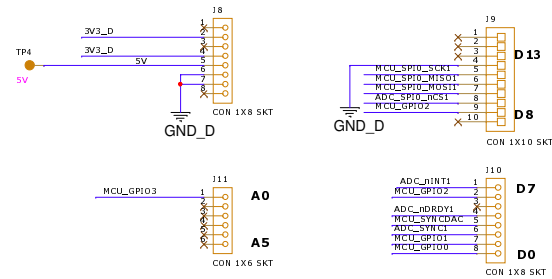


EXTERNAL POWER SUPPLIES

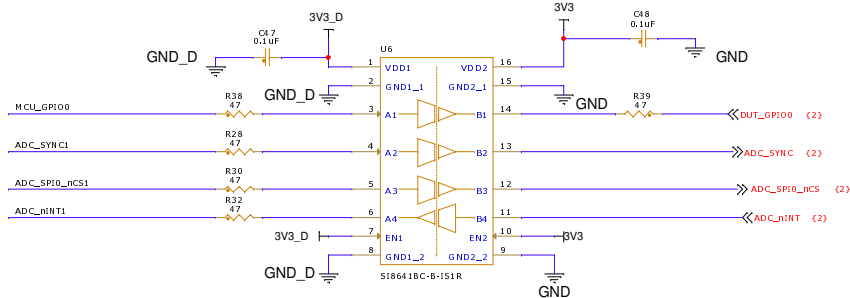


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Page Title: POWER SUPPLY		
Size C	Document Number SCH-94526, SPF-94526	Rev C
Date: Wednesday, September 03, 2014 Sheet 3 of 4		

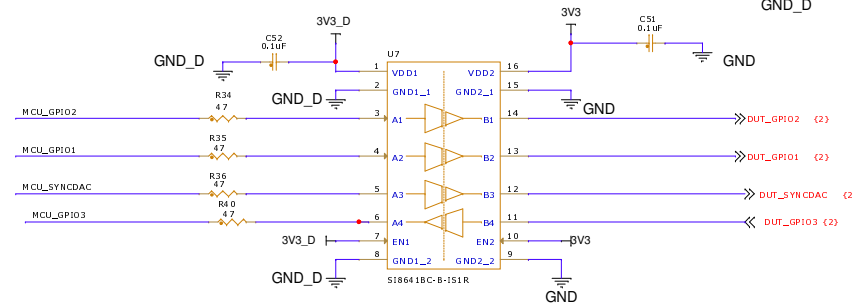
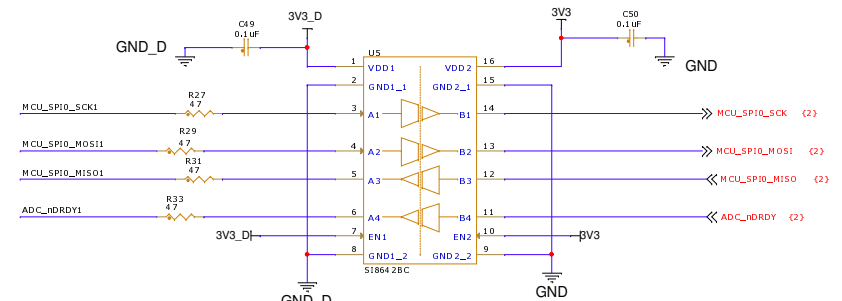
ARDUINO INTERFACE HEADERS



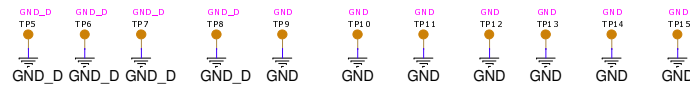
Outputs are default to 'L'



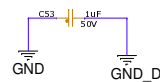
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Spread GND test points uniformly across the board



Stand-offs spread uniformly across the board



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Page Title: ARDUINO HEADERS			
Size C	Document Number	SCH-94526, SPF-94526	Rev C
Date: Wednesday, September 03, 208 Sheet 4 of 4			