



This document regroups all links necessary for the user to develop an application

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Mouseover "L" in "Link"

RZ/G3S Easy Download Guide

Step	Information for engineers	Contents Type	Contents Name	RZ/G3S Link	Note
	RZ/G Series 32/64-bit MPU (Wiki)	Link summary	Introduction,Boards,Software and Documentation	Link	-
	Contact Support	QA Ticket	Technical Q&A	Link	-

Step	Introduction	Contents Type	Contents Name	RZ/G3S Link	Note
1	RZ Family Brochure	Document	RZ Family Microprocessors	Link	-
2	Flyer	Document	Renesas RZ/G3S Group	Link	-
3	User's Manual Overview	Document	User's Manual Overview	TBD	-
4	60's Video	Video	60's Video	Link	-

Step	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/G3S Link	Note
1	User's Manual	Document	User's Manual: Hardware	Link	-
2	How to start up the board	Document	SMARC Module Board User's Manual	Link	-
		Document	SMARC Carrier Board User's Manual	Link	-
		Document	Evaluation Board Kit(EVK) Start Up Guide	TBD	-
3	Board information / how to get	Info/purchase	Evaluation Kit WEB Page	Link	-
4-1	Evaluation environment start-up method Board Support Package (BSP)	Document	Release Note for RZ/G3S Board Support Package(v1.0.0)	Link	-
		Document	Linux Start-up Guide	TBD	-
		Document	RZ/G3S Group BSP Manual Set	Link	EULA*2
		Document	RZ/G Proprietary SW Package Handling	Please contact Distl/Sales	-
4-2	Evaluation environment start-up method Multi-OS Package	Document	Release Note for Multi-OS Package v2.00	Link	-
		Document	Getting Started with Flexible Software Package	Link	-
		Document	Setting GPIO with Flexible Software Package	Link	-
		Document	FSP Documentation	Link	-
		Document	Release Note for Flexible Software Package	Link	-
5-1	How to get various software				
	Pre-built Images	Software	RZ/G3S Pre-built Images	Link	-
	Board Support Package (BSP)	Web Link	RZ/G3S Board Support Package V1.0.0 [5.10-CIP]	Link	-
	Feature Package (Multi-OS Package)	Web Link	RZ/G3S Group Multi-OS Package	Link	EULA*2
	Feature Package (Security Package)	Web Link	Security Package	Link	SLA*3
6	How to get various tools	Tool	Smart Configurator	Link	-
7	How to launch various tools	Document	Smart Configurator for RZ User's Guide	Link	-

Step	Custom board development	Contents Type	Contents Name	RZ/G3S Link	Note
	PCB Design and Verification Flow (for DDR-IF)	Document	PCB Design and Verification Flow (for DDR-IF)	Please contact Distl/Sales	
	PCB design checklist when designing a custom board	Document	Board circuit design checklist	Link	-
	PCB Design Guide for High Speed Serial Interfaces	Document	PCB Design Guidelines for MIPI-CSI, MIPI-DSI, USB2.0 and PCI Express Gen	Link	-
	PCB Design Guide for DDR memory,DDR Config Generation Tool	Document,Tool	PCB Design Guideline for DDR4/DDR3L, DDR Config Generation Tool	Link	TCU*5
	PCB Verification Guide for Core	Document	PCB verification guide for Core VDD	Link	-
	PCB Verification Guide for DDR memory	Document,Model	PCB verification guide for DDR4/DDR3L	Link	TCU*5
	Reference Desing Data(Schamtics,Layout,BOM)	Design Data	SMARC Module Board Design Data(Schematics,Layout,BOM)	Link	Disclaimer*4
	Reference Desing Data(Schamtics,Layout,BOM)	Design Data	Carrier Board Design Data(Schematics,Layout,BOM)	Link	Disclaimer*4
	IBIS(For LSI)	Model	IBIS	Link	TCU*5
	Boundary Scan Description Language file	File	BSDL	Link	Disclaimer*4
	Power Delivery Network model	Model	PDN	Link	Disclaimer*4
	Guide to Using RGMII in Making an Ethernet-IF Connection	Document	Guide to Using RGMII in Making an Ethernet-IF Connection	Link	-
	Power Consumption Measurement	Document	Power Consumption Measurement	Link	-
	Thermal Management Guide	Document	Thermal Management Guideline	Link	-
	Mecanical Handling Guideline	Document	Mechanical Handling Guideline	Link	-
	Reflow Soldering conditions	Document	Mount Conditions Reflow Soldering Conditions	Link	-
	Baking conditions	Document	Mount Conditions Baking Conditions	TBD	-
	Semiconductor Package Mount Manual	Document	Semiconductor Package Mount Manual	Link	-
	Lifetime estimation guide	Document	Lifetime Guideline	Link	-
	Power Management IC installed in EVK	Device Info	Power Management IC	Link	-
	Clock Generator IC installed in EVK	Device Info	Clock Generator IC	Link	-
	Reliability Report	Document	R9A08G045XXGBG Reliability Report	TBD	
	Power calculation sheet and guide	Document	Power calcuration sheet and guide	Please contact Distl/Sales	-

*1: Letter of Acknowledgement

*2: End User Lisence Agreement

*3: Software Lisence Agreement

*4: Disclaimer Letter

*5: Terms and Conditions of Use