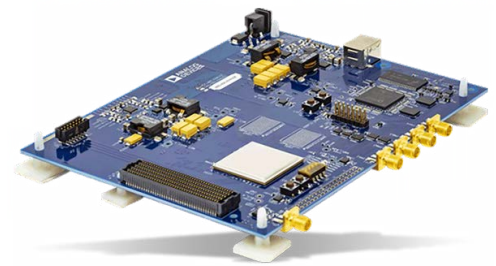# HSC-ADC-EVALEZ
FPGA Based Data Capture Kit

Product Overview
07-08-2021
For the most up-to-date information, visit www.mouser.com or the supplier's website.

Description

Analog Devices HSC-ADC-EVALEZ FMC-Compatible high speed converter evaluation platform uses an FPGA based buffer memory board. This helps to capture blocks of digital data from the Analog Devices high speed Analog-to-Digital Converter (ADC) evaluation boards. The board is connected to the PC through a USB port and is used with VisualAnalog® to quickly evaluate the performance of high speed ADCs. The evaluation kit is easy to set up and supports emerging serial interface standards, like JESD204B.

Additional equipment needed includes an Analog Devices high speed ADC evaluation board, a signal source, and a clock source. Once the kit is connected and powered, the evaluation is enabled instantly on the PC. The ADC capture board enables numerous expansion and evaluation possibilities by virtue of its powerful reconfigurable FPGA core.

Features

- Supports multiple ADC channels via single FMC-HPC interface connector
- JESD-204B support for up to eight 6.5Gbps Lanes
- Parallel input at 644 MSPS SDR and 1.2 GSPS DDR
- 256kB FIFO Depth
- Use with VisualAnalog® software
- Based on Virtex-6 FPGA
- Simple USB port interface (2.0)

Typical Data Capture Setup

Figure - AD9250 ADC Evaluation Board (left) connected to HSC-ADC-EVALDZ (right)

Mouser Part Number(s)

HSC-ADC-EVALEZ

To learn more, visit https://www.mouser.com/new/analog-devices/adi-hsc-adc-evalez-eval-board/