Analog Devices Inc. EVAL-TPG-ZYNQ3 Xilinx Zynq®-7000 Evaluation Kit

Description

Analog Devices EVAL-TPG-ZYNQ3 Xilinx Zynq®-7000 Evaluation Kit is optimized for JESD204B and provides a data capture platform for many of the RadioVerse families of wideband transceiver evaluation boards. A single 12V power supply generates power for both the Zynq® and the RadioVerse evaluation boards. The data capture platform interfaces to the RadioVerse evaluation boards through a variety of interfacing standards including JESD207 and JESD204B. The JESD204B interface on the Zynq® evaluation system supports up to 12.5Gbps lane rates. A variety of interface options allows the evaluation kit to interface directly to a PC monitor, keyboard, and mouse as well as a PC running the transceiver evaluation or prototyping software Packages.

Features

- Upgraded with 3-speed grade XC
- 7Z045 FFG900 supporting up to 12.5Gbps JESD204B Lane Rates 1GB (PS) DDR3 Component Memory 1GB (PL) SODIM Memory
- GigE Ethernet
- USB host
- FMC LPC and HPC expansion ports
- 12V adapter powers Zynq® evaluation kit
- Provides power for RadioVerse transceiver evaluation boards via FMC Connector

Kit Contents

- ZC706 evaluation board upgraded with XC7Z045 FFG900-SOC
- Power supply

Associated Products

- [AD9375 RF Transceivers](#)
- [ADRV9375 Radio Cards](#)

Mouser Part Number(s)

[View All Parts](#)

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