TinyFPGA AX1 Development Board

CS-TINYFPGA-04
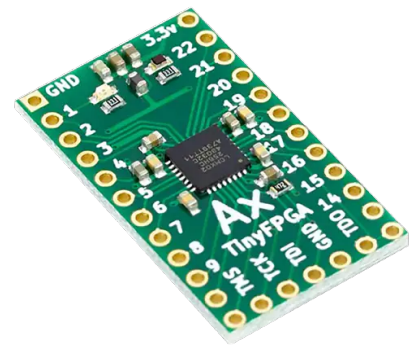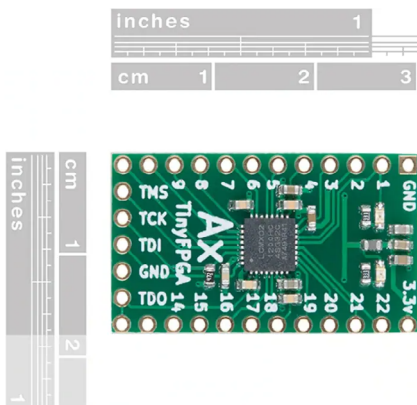
Description

Crowd Supply TinyFPGA AX1 Development Board is a bare-bones breakout board for the Lattice MachXO2-256 FPGA. The compact TinyFPGA AX1 is a development board with a low cost per logic element and features a 3.3V operating voltage. This board communicates via SPI and I^{2}C interfaces. The development board includes 18 user IOs (21 with JTAGEN), 2-kilobits distributed RAM, and flash configuration memory programmable via JTAG. The TinyFPGA AX1 only contains the bare minimum of components for the FPGA to function and measure 18mm x 30.5mm.
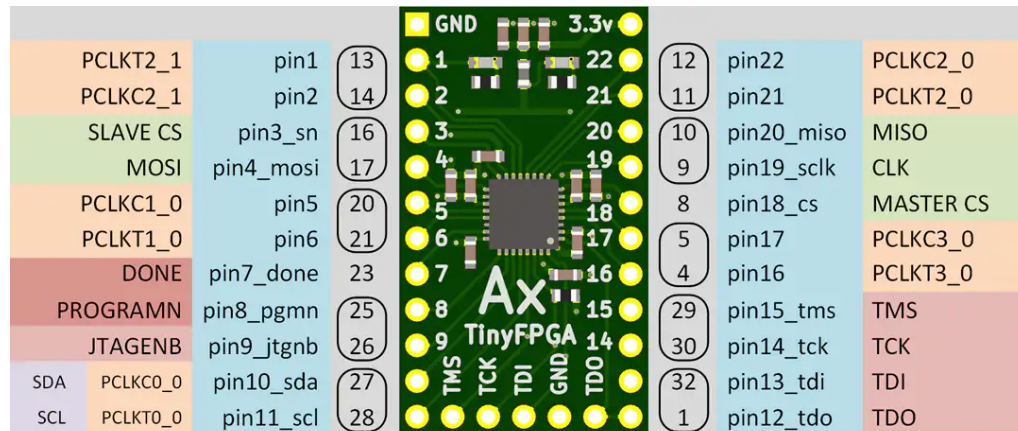
Features

- Tiny 1.2" x 0.7" form factor
- Low power 3.3V operation
- 18x user IOs (21 with JTAGEN)
- Built-in flash configuration memory programmable via JTAG
- 256 4-input look-up tables
- 2 kilobits distributed RAM
- 1x SPI hard-IP
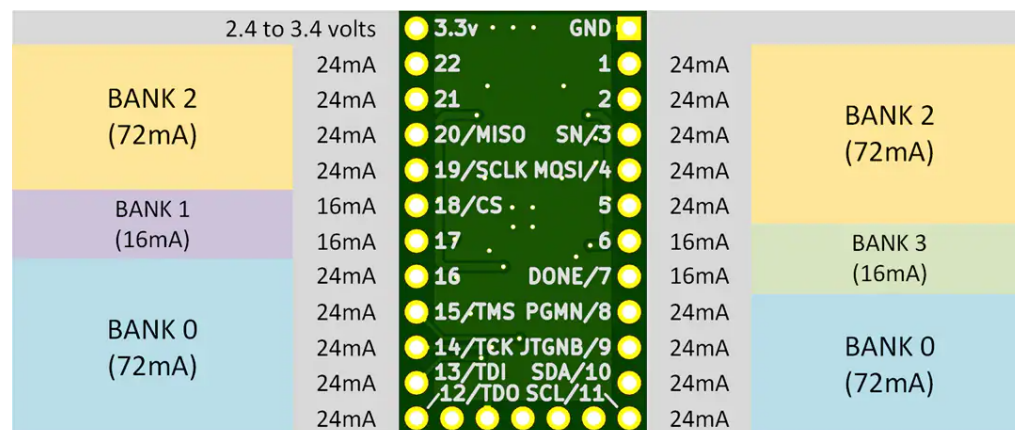- 2x I^{2}C hard-Ips

Board Dimensions

Board Layout (Top)

FPGA Pin Number	PLL CLK Inputs	JTAG	Config
Verilog Port Name	Differential Pair	Hard I2C	Hard SPI

Board Layout (Bottom)

Voltage on IO pins must not exceed 3.6v

Average current of IO bank pins should be less than IO bank max current

Mouser Part Number

[View Part](#)

To learn more, visit https://www.mouser.com/new/crowd-supply/crowd-supply-tinyfpga-ax1-board/

The information contained in this document should be used as a guideline only.