

Table of Contents	
1	TITLE, TOC & REV
2	PCA9460

Revisions			
Rev	Description	Date	Updated by
X1	- Added J22 and J23 for INB14 and INB23. - Update J15, J16, J17 from 2-pin to 3-pin for MODE0(IO1), MODE1(IO2) and MODE2(IO3). - Updated original J18 from 2x6 to 2x8 header, and updated its pins connection. - Updated J1 from 2-pin jumper to 3-pin jumper for INL1 input. - Add one more ground test hook (LK6).	July 8, 2021	Sean Liu
A	Final Release	Aug 11, 2021	Sean Liu

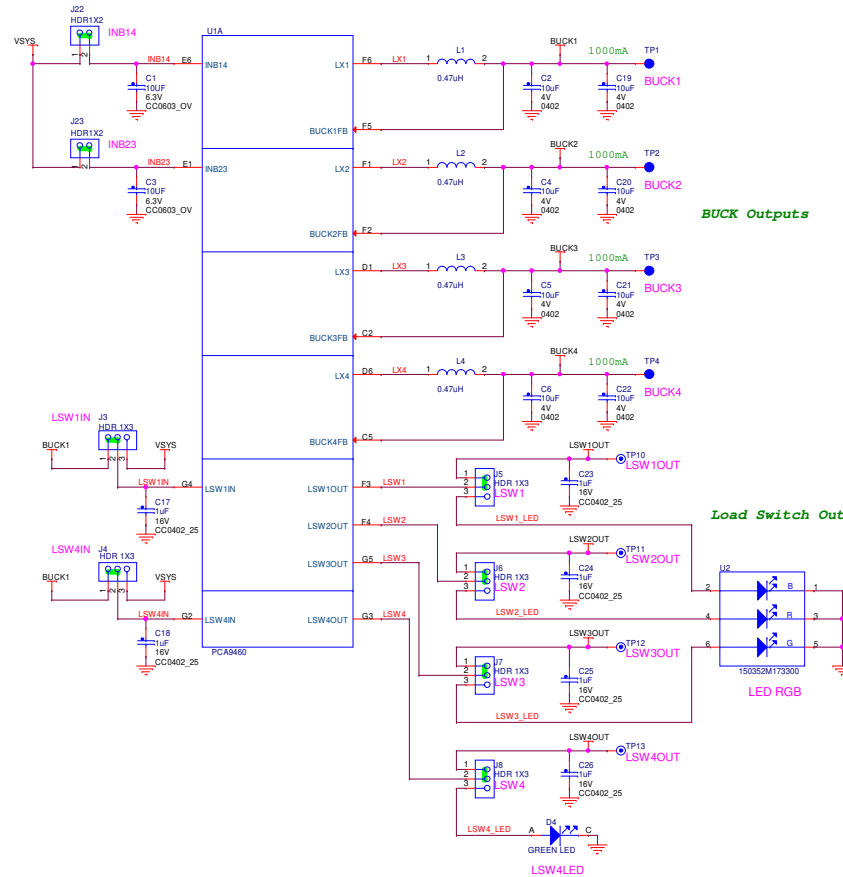
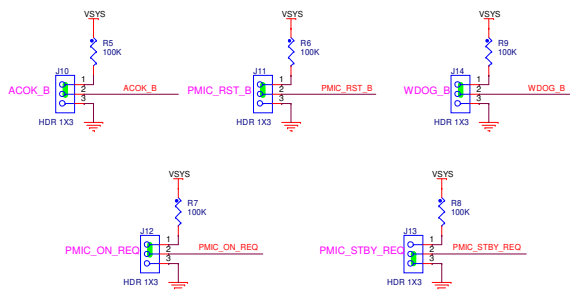
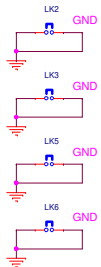
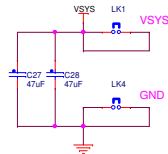
KIT-PCA9460-EVB

Default Jumper Setting

REF DES	JUMPER(DEFAULT)	PAGE NAME
J16,J12,J5,J10,J3,J2,J14,J6, J23,J8,J15,J4,J17,J21,J7,J11, J22,J1,J9	1-2	2. PCA9460
J13	2-3	2. PCA9460

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Classification: Company Internal/Proprietary			
Designer: Sean Liu	Drawing Title: KIT-PCA9460-EVB		
Drawn by: Sean Liu	Page Title: TITLE, TOC & REV		
Approved: <Approver>	Size C	Document Number SCH-52033 PDF: SPF-52033	Rev A
Date: Wednesday, August 11, 2021		Sheet 1 of 2	

VSYS 2.7~5.5V

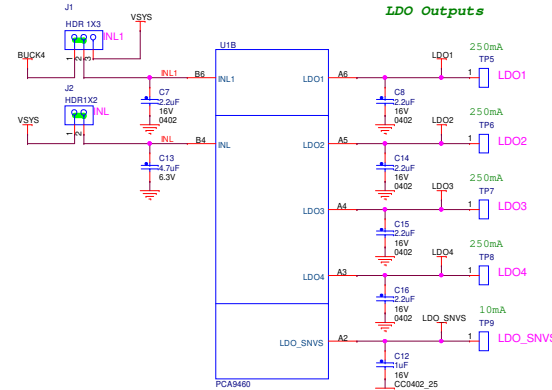
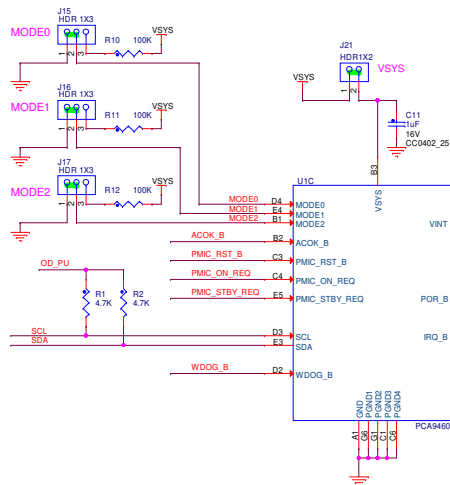


BUCK Outputs

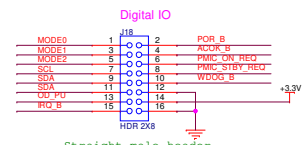
Load Switch Outputs

LED RGB

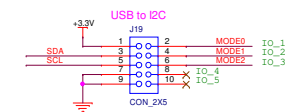
GREEN LED



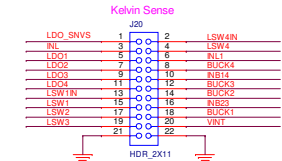
LDO Outputs



Straight male header



Right angle female header for DEBUGINTERFACE Board



Straight male header



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