

Table of Contents	
2	POWER SUPPLY
3	HS & LS DRIVER

2		1	
Revisions			
Rev	Description	Date	Approved
A	Original Release	28-01-2022	Pierre Calmes

FRDMGD3160HB8EVM



Microcontroller Solutions Group
6501 William Cannon Drive West
Austin, TX 78735-8598

This document contains information proprietary to NXP and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of NXP Semiconductors.

Designer: Travis Alexander	Drawing Title: FRDMGD3160HB8EVM	ICAP Classification: CP:	IID:	X	PUR:
Drawn by: Avaris	Page Title: TITLE PAGE				
Approved: Pierre Calmes	Size: C	Document Number: SCH-54896 PDF: SPF-54896	Rev A		
Date: Tuesday, February 08, 2022		Sheet 1 of 3			

[illegible]

FOR USE WITH SPI GEN:
USE SPI 0 FOR LOW SIDE & DAISY CHAIN
USE SPI 1 FOR HIGH SIDE

FOR NORMAL OPERATION CONNECT JC5B 1-
FOR DAISY CHAIN OPERATION JC5B 2-3

FOR NORMAL OPERATION CONNECT JMISO 1-2
FOR DAISY CHAIN OPERATION JMISO 2-3

ASSEMBLY NOTE:
SOLDER SOCKET PINS TO BOTTOM

[illegible]

ASSEMBLY NOTE:
SOLDER SOCKET PINS TO BOTTOM

