

1. Unless Otherwise Specified:

All resistors are in ohms, 5%, 1/8 Watt

All capacitors are in uF, 20%, 50V

All voltages are DC

All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:

_B Denotes - Active-Low Signal

<> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

Revision History

Rev B: Add GPIO control to SW1 and SW2 setting
Add JP10 for VDD current measurement

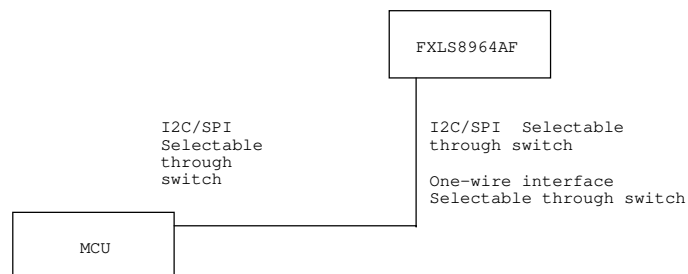
Rev C: Update SW2 silkscreen

JUMPERS & SW SETTING

INSTALL	PARTS
1-2	J9, J10
2-3	J7, J8, SW1
2-3&5-6	SW2

DNP PARTS

J1, J2, R10, R12, R28

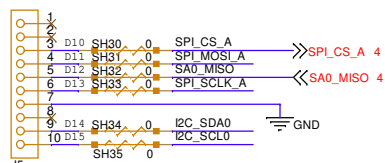
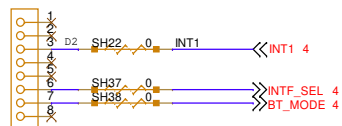


ICAP Classification: CP: ___ IJO: X PUBI: ___			
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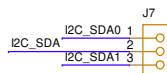
Mother Board 700-28164

SHIELD BOARD ARDUINO HEADER

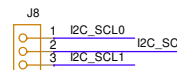
J3 SKT_1X8



J5 SKT_1X10

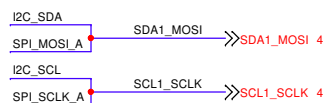


J7 HDR TH 1X3
ASSY_NOTE = 2-3
SILK = 1:I2C_SDA0 3:I2C_SDA1



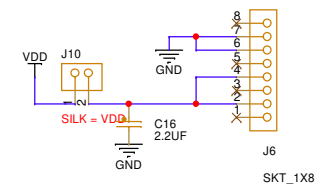
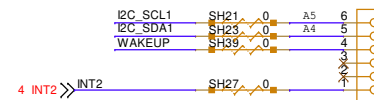
J8 HDR TH 1X3
ASSY_NOTE = 2-3
SILK = 1:I2C_SCL0 3:I2C_SCL1

I2C/SPI SHARED PINS

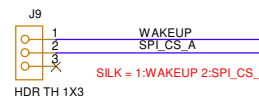


SHIELD BOARD ARDUINO HEADER

J4 SKT_1X6



J6 SKT_1X8

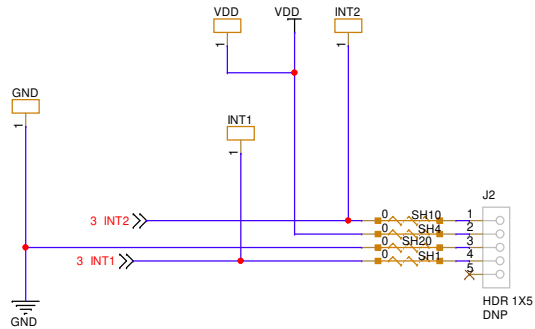


J9 HDR TH 1X3
SILK = 1:WAKEUP 2:SPI_CS_A
For BT_MODE=GND, Connect J9,1-2pins(default)
For BT_MODE=VDD, Connect J9,2-3pins

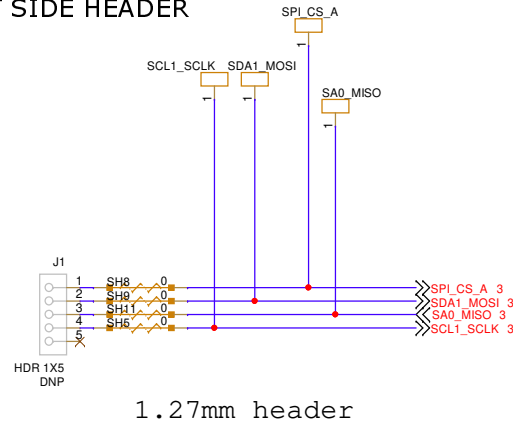


ICAP Classification: CP: IVO: PUBI: X			
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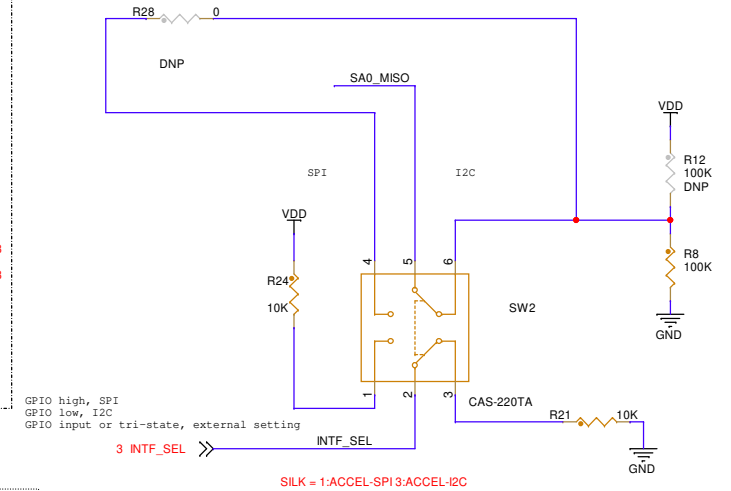
LEFT SIDE HEADER



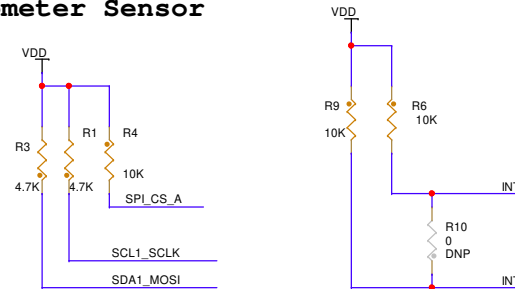
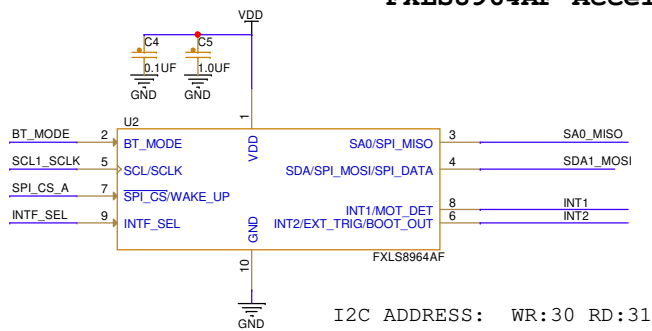
RIGHT SIDE HEADER



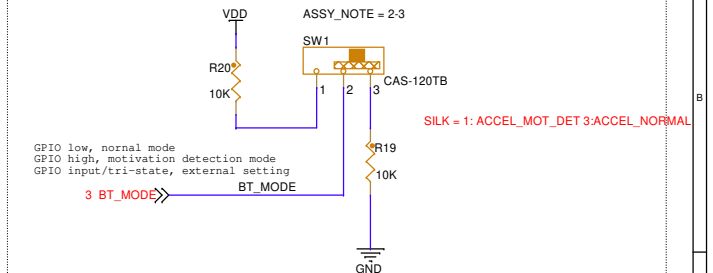
I2C ADDRESS SET& I2C/SPI SELECTION



FXLS8964AF Accelerometer Sensor



BOOT MODE SELECTION



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