

Amphenol

COMMUNICATIONS SOLUTIONS

Multi-Trak

--SFF-TA-1033

--OCP DC-MHS

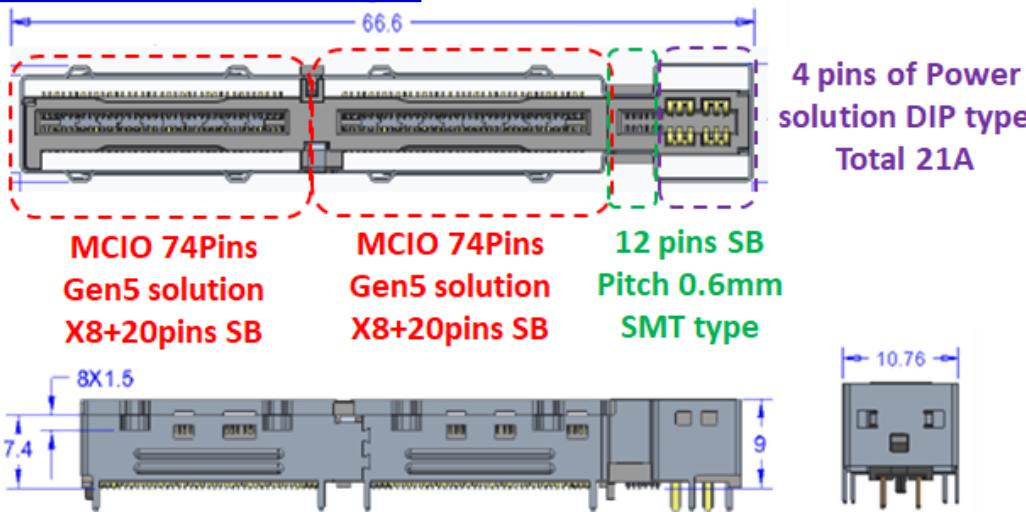
Business Unit: CMIO

Feb , 2023



Design Overview

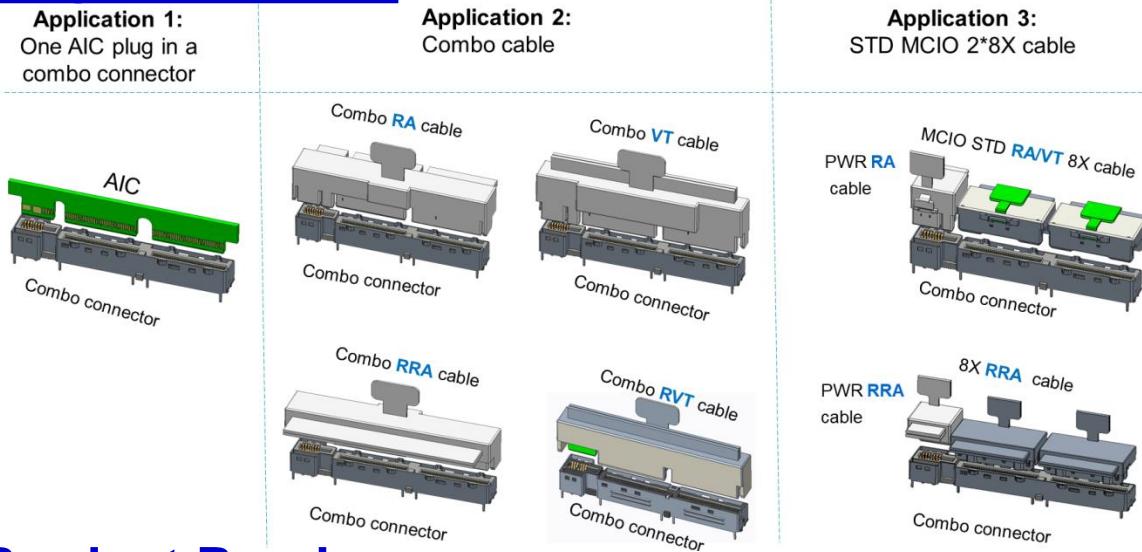
Connector Design



Advantage

- ✓ Combine original PCIE and MCIO to be one connector, include POWER and high/low speed signal.
- ✓ Reverse cable for easy to organize the layout.
- ✓ Modularized design for further expansion, card and cable interactive support.
- ✓ Upgrade to **Gen6** version and apply to **PCI SIG** to be a standard connector.
- ✓ Support different applications for **AIC**, **Combo cable** and **MCIO STD cable**.

Plug Application



Product Roadmap

Form Factor	Type	Illustration	Total Current Support	Speed	Tooling Status	Sample schedule
VT	Power + X16 (STD) (SFF-TA-1033)		21A	Now: Gen5 Future: Gen6	Toolled 2023 2H	Ready
	Power + X8(STD) (SFF-TA-1033)		21A	Now: Gen5 Future: Gen6	Toolled 2023 2H	Ready
	Power (STD) (SFF-TA-1033)		21A	x	Toolled	Ready
	Power + X16 (High Power)		34A	Now: Gen5 Future: Gen6	Per request	August 2023
	Power + X16 (High Power Plus)		55A	Now: Gen5 Future: Gen6	Per request	TBD