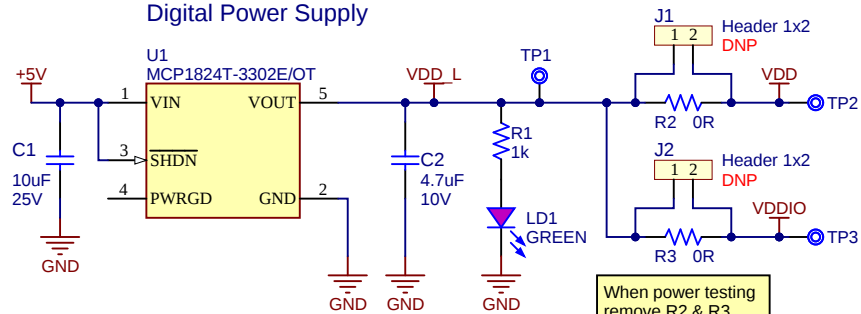
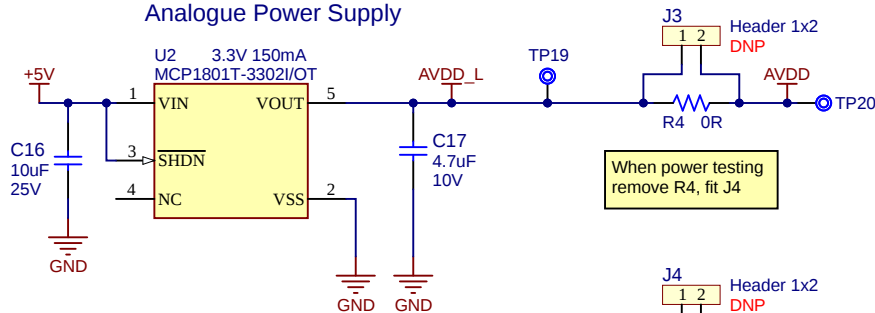


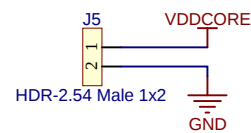
Digital Power Supply



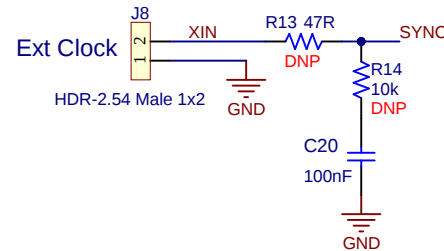
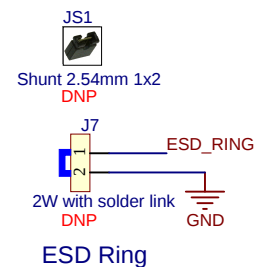
Analogue Power Supply



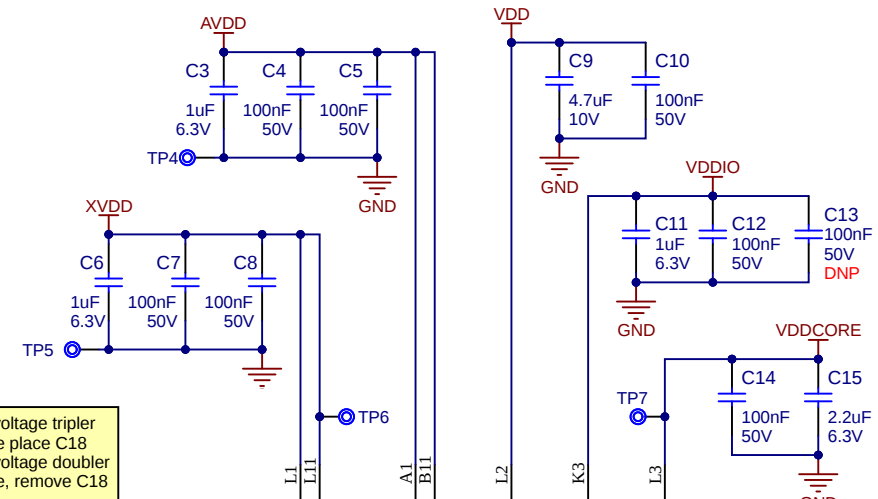
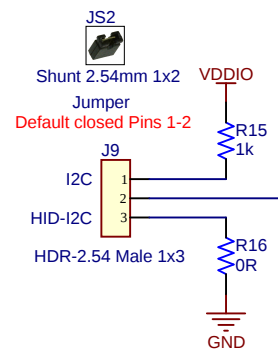
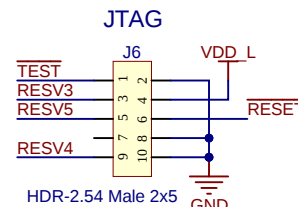
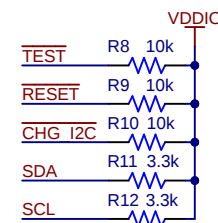
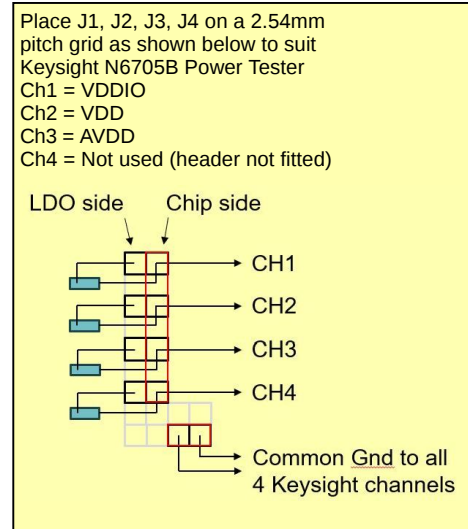
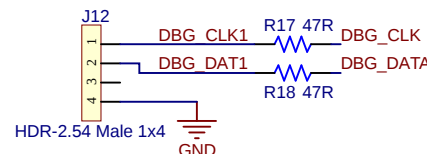
Ext VDDCORE Supply



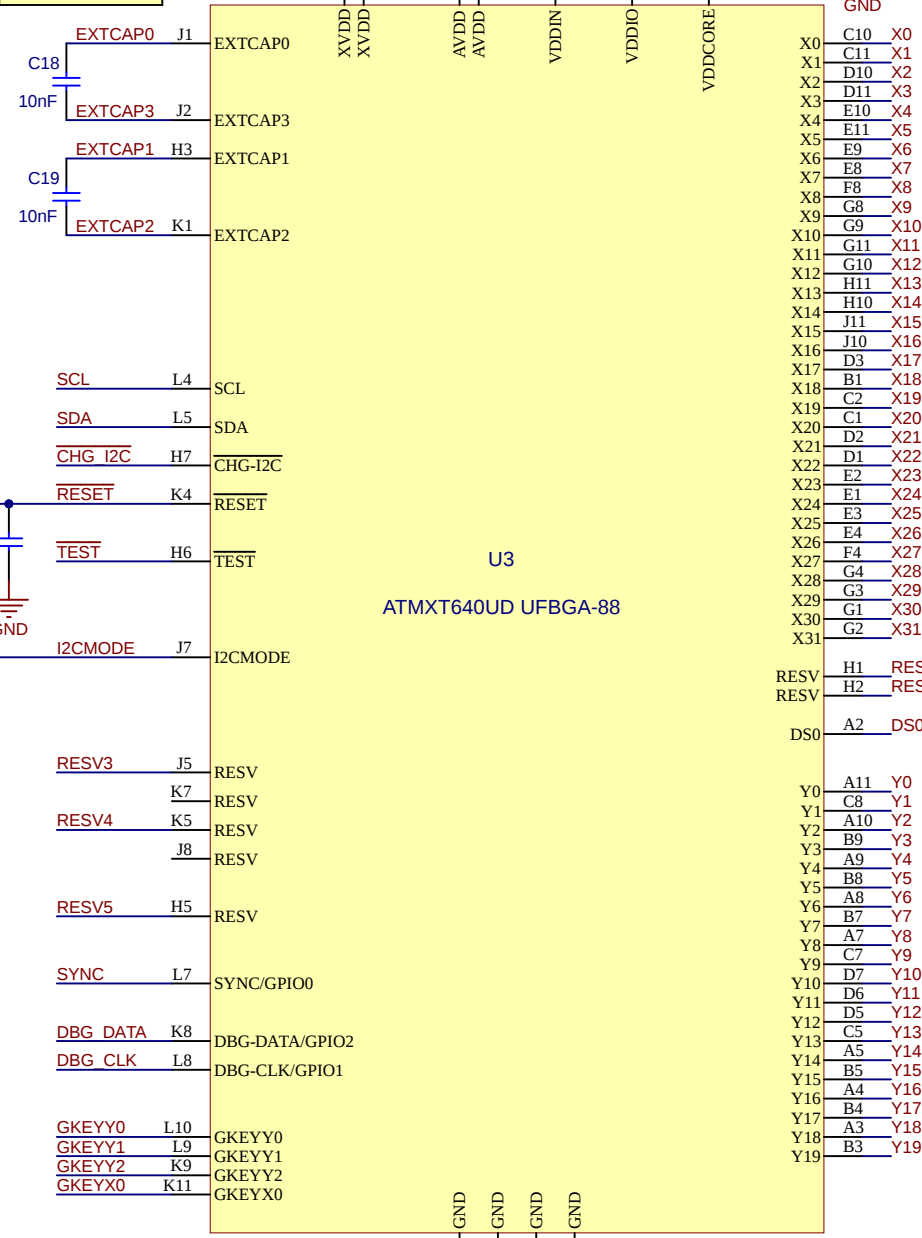
Star point to connect ESD ring to GND flood. Fit near HSBB connector. Break solder link to disconnect ESD Ring from GND. Fit header only if required for testing.



SPI Debug



For voltage tripler mode place C18
For voltage doubler mode, remove C18



- Y0 TP8
- Y1 TP9
- Y18 TP10
- Y19 TP11
- DS0 TP12
- X0 TP13
- X1 TP14
- X28 TP15
- X29 TP16
- X30 TP17
- X31 TP18

- ESD_RING DS0
- GKEYX0
- X31
- X30
- X29
- X28
- X27
- X26
- X25
- X24
- X23
- X22
- X21
- DS0
- ESD_RING DS0
- GKEYY2
- GKEYY1
- GKEYY0
- DS0
- Y19
- Y18
- Y17
- Y16
- Y15
- Y14
- Y13
- Y12
- Y11
- Y10
- Y9
- Y8
- Y7
- Y6
- Y5
- Y4
- Y3
- Y2
- Y1
- Y0
- DS0
- ESD_RING DS0
- DS0
- X0
- X1
- X2
- X3
- X4
- X5
- X6
- X7
- X8
- X9
- X10
- X11
- X12
- X13
- X14
- X15
- X16
- X17
- X18
- X19
- X20
- X21
- X22
- X23
- X24
- X25
- X26
- X27
- X28
- X29
- X30
- X31
- RESV H1
- RESV H2
- DS0 A2
- DS0
- Y0 A11
- Y1 C8
- Y2 A10
- Y3 B9
- Y4 A9
- Y5 B8
- Y6 A8
- Y7 B7
- Y8 A7
- Y9 C7
- Y10 D7
- Y11 D6
- Y12 D5
- Y13 C5
- Y14 A5
- Y15 B5
- Y16 A4
- Y17 B4
- Y18 A3
- Y19 B3

CONN FFC BOTTOM 80POS 0.50MM R/A
FH28H-80S-0.5SH(05)

Project Owner: Patrick Cassidy			
PCB Layout Contact: Patrick Cassidy			
PartNumber: 00227	Project Title mXT640UD EVK Control Board	Variant: [No Variations]	
Sheet Title ATMXT640UD EVK Control Board			
Size A3	SCH #: 03-00227	Rev: 1	Date: 19/02/2021
File: 00227.SchDoc		Rev: 1	Sheet 1 of 1
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