

ITS4040D Evaluation Board

Rev. 1.0

Schematics & Layout



Board Overview

V_{EXT}

Supply for 5V voltage regulator

V_{IN}

Global input voltage from external source

Input Pin terminals

Individual V_{IN} for each channel from external source

STATUS Pin

Global Status Output

GND

Jumper

Enables/disables STATUS Pin LED

Voltage Regulator
providing 5V logic level out of V_{EXT} (optional usage)

Jumper

Enables/disable "VS presence" LED

Jumper

To configure desired V_{IN} source for input signals

V_S -Supply

Two alternative sets of terminals for V_{OUT}
Each channel has a LED option for active state.

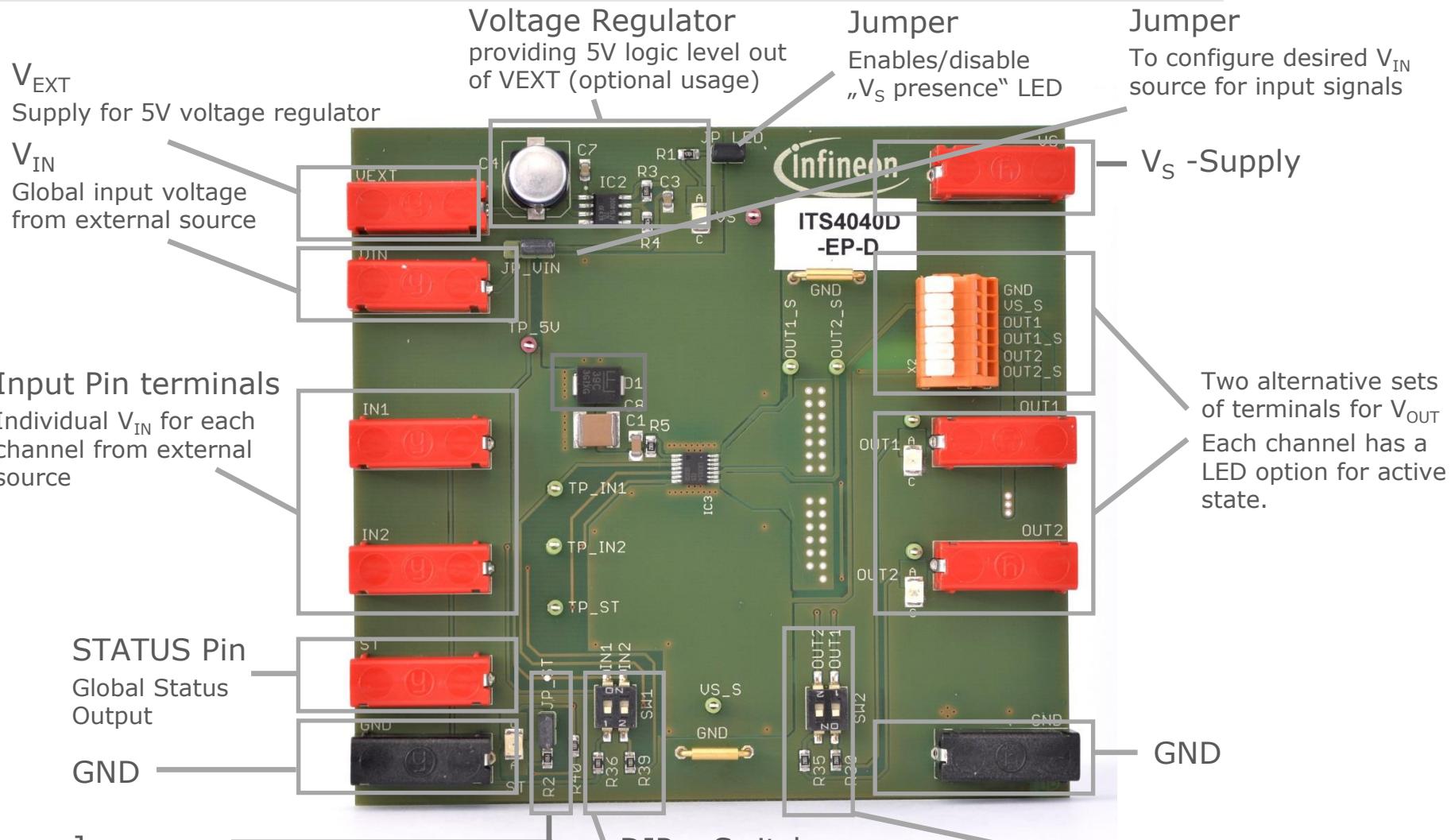
GND

DIP – Switch

Enables/disables LED option per output channel

DIP – Switch

Controls input voltage level High/Low per channel when global V_{IN} from external source is used



Schematics

Voltage Regulator to generate 5V-logic levels for inputs

V_{IN} Terminal: for providing external levels globally to all input pins

US BABU4MM

UIN BABU4MM

UEXT BABU4MM

10 μ /50V

C4 100n

C7 100n

1 μ /15V

R36 1k

1 μ /15V

R40 1k

1 μ /15V

R39 1k

1 μ /15V

TP_IN1

TP_ST

TP_IN2

TP_ST

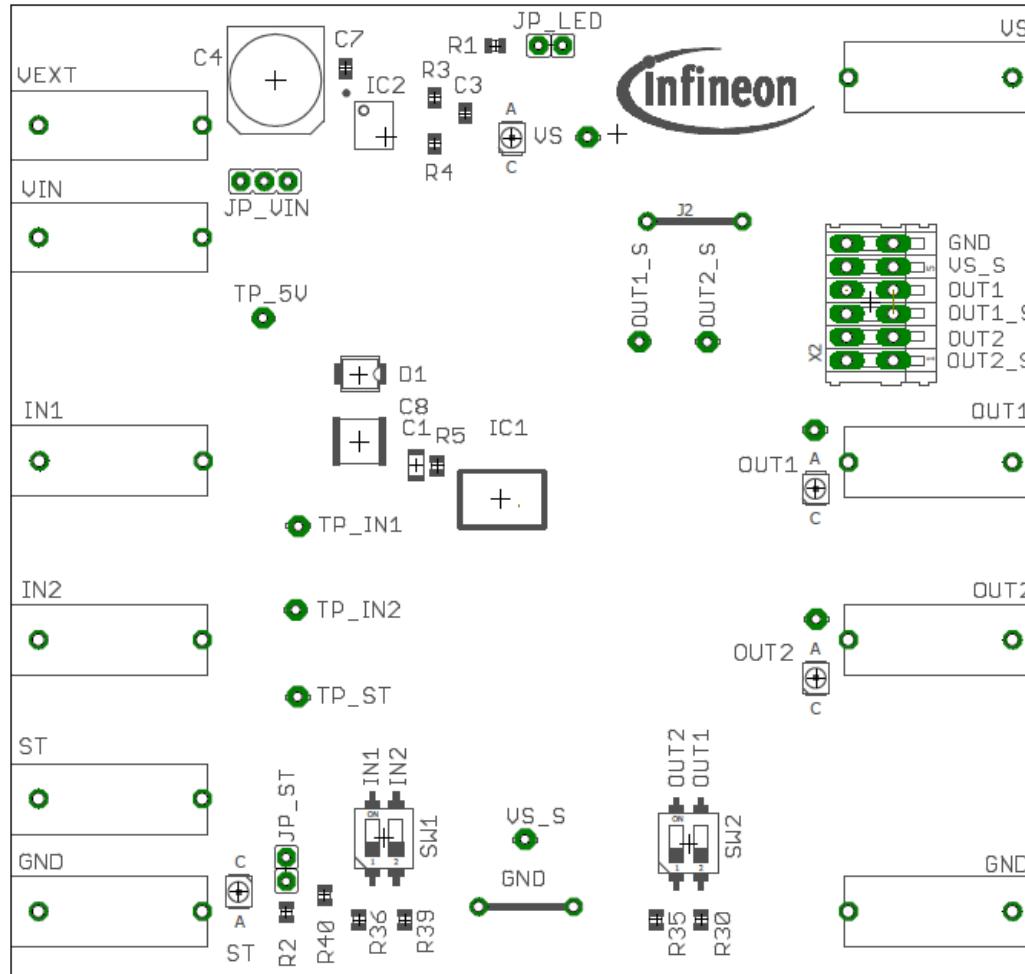
TP_IN1

TP_IN2

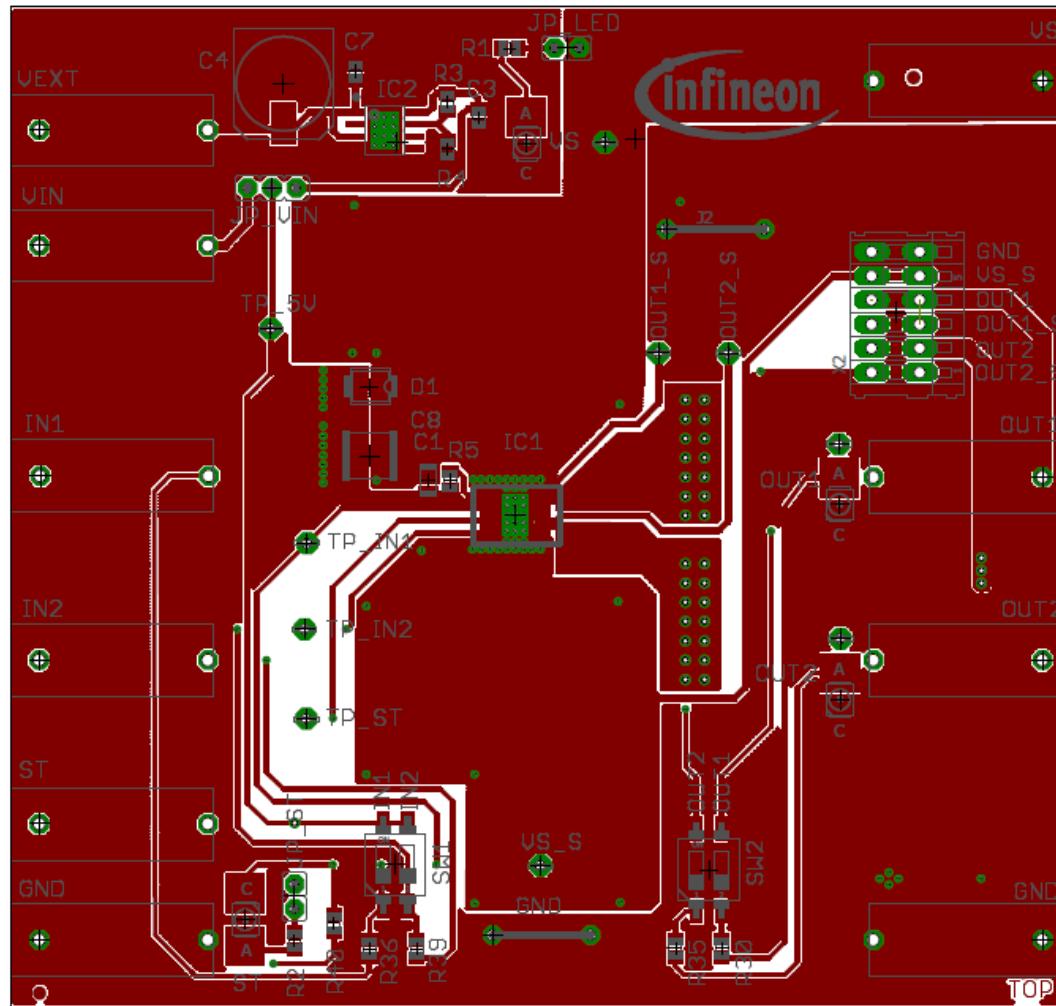
Top View

IC1 = ITS4040D 2-channel Industrial PROFET

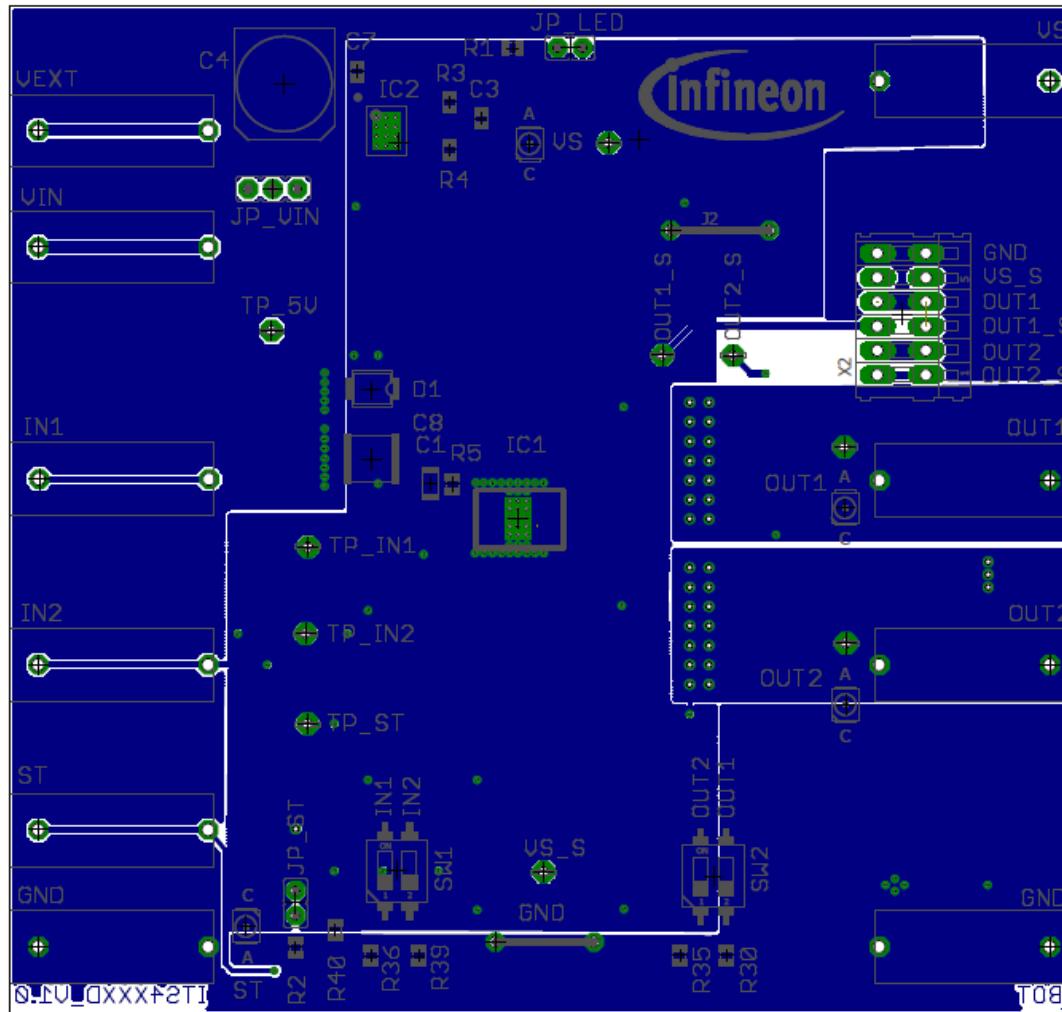
IC2 = IFX30081 VREG as option to generate logic levels for input pins



Top Layer



Bottom Layer





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