



Features

Reference: 5.0V/460MHz/CW

- Gain: 41.0 dB
- Iccq: 120 mA
- OP1dB: 34.0 dBm
- Psat: 35.5 dBm
- PAE at Psat: 64%

Reference: 3.5V/460MHz/CW

- Gain: TBD
- Iccq: TBD
- OP1dB: TBD
- Psat: TBD
- PAE: TBD
- Flexible Bias Voltage and Current
- Process: InGaP HBT

Applications

- UHF
- Automatic Meter Reader
- RFID

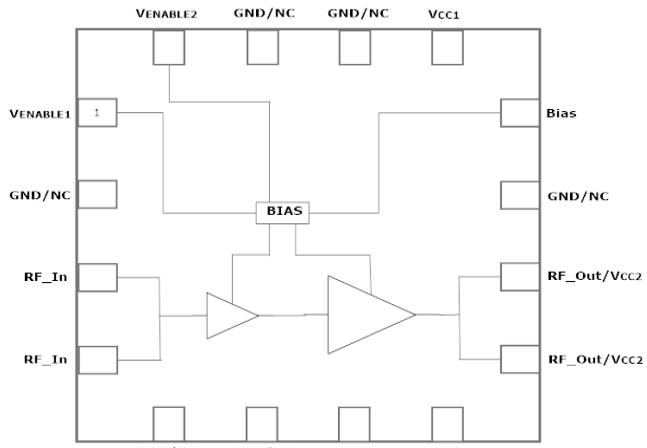
Product Description

GRF5504 is a high efficiency PA that delivers up to 3.5 Watts at Psat with Vcc at 5.0 volts and a low Iccq of 120 mA. PAE at Psat is roughly 64%.

The device can be tuned over a range of frequencies from around 400 MHz to 500 MHz with typical fractional bandwidths of 3 to 5%.

For frequencies in the 700-1000 MHz range, GRF5509 offers slightly higher output power and high efficiency with the same package and pinouts.

Consult with the GRF applications engineering team for custom tuning/evaluation board data.



3.0 x 3.0 mm QFN-16



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GRF5504

High Efficiency, 3.5 Watt PA
Tuning Range: 0.4 to 0.5 GHz

Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Drain Voltage	V _{CC}		5.5	V
Transient Average RF Input Power: (Load VSWR < 2:1; Duration: <1 hour)	P _{IN MAX}		TBD	dBm
Operating Temperature (Package Heat Sink)	T _{AMB}	-40	85	°C
Maximum Junction Temperature (MTTF > 10 ⁶ Hours)	T _{MAX}		170	°C
Maximum Dissipated Power Stage 1 (at Psat)	P _{DISS MAX}		400	mW
Maximum Dissipated Power Stage 2 (at Psat)	P _{DISS MAX}		3800	mW
Ruggedness: V _{CC} : 5.0 volt at Psat (all phase angles)	VSWR	8:1		—
Electrostatic Discharge:				
Charged Device Model: (TBD)	CDM	1500		V
Human Body Model: (TBD)	HBM	250		V
Storage:				
Storage Temperature	T _{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	—



Caution! ESD Sensitive Device

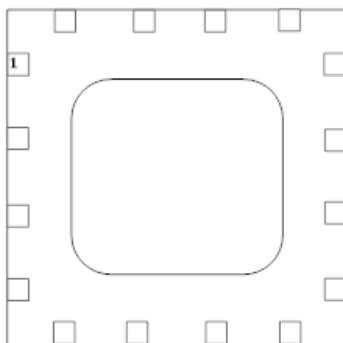


Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF5504 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

[Link to manufacturing note](#)

Pin Out (Top View)



Pin Assignments:

Pin	Name	Description	Note
1	VENABLE1	Enable1 Voltage Input	VENABLE1 and series resistor set I _{CCQ} for the input stage. VENABLE < =0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.
2	GND/NC	No Connect or Ground	No internal connection to die
3	RF_In	RF Input	Pins 3-4 tied together on system board
4	RF_In	RF Input	Pins 3-4 tied together on system board
5	GND/NC	Ground or No Connect	No internal connection to die
6	GND/NC	Ground or No Connect	No internal connection to die
7	GND/NC	Ground or No Connect	No internal connection to die
8	GND/NC	Ground or No Connect	No internal connection to die
9	RF_Out/Vcc2	PA Output/Bias Voltage	Pins 9-10 tied together on system board. Supply Vcc2 here.
10	RF_Out/Vcc2	PA Output/Bias	Pins 9-10 tied together on system board. Supply Vcc2 here.
11	GND/NC	Ground or No Connect	No internal connection to die
12	Bias	Bias Circuit Supply	Connect to Vcc2 through external resistor
13	Vcc1	Bias Voltage	Supply Vcc1 here.
14	GND/NC	Ground or No Connect	No internal connection to die
15	GND/NC	Ground or No Connect	No internal connection to die
16	VENABLE2	Enable2 Voltage Input	VENABLE2 and series resistor set I _{CCQ} for the output stage. VENABLE < =0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.



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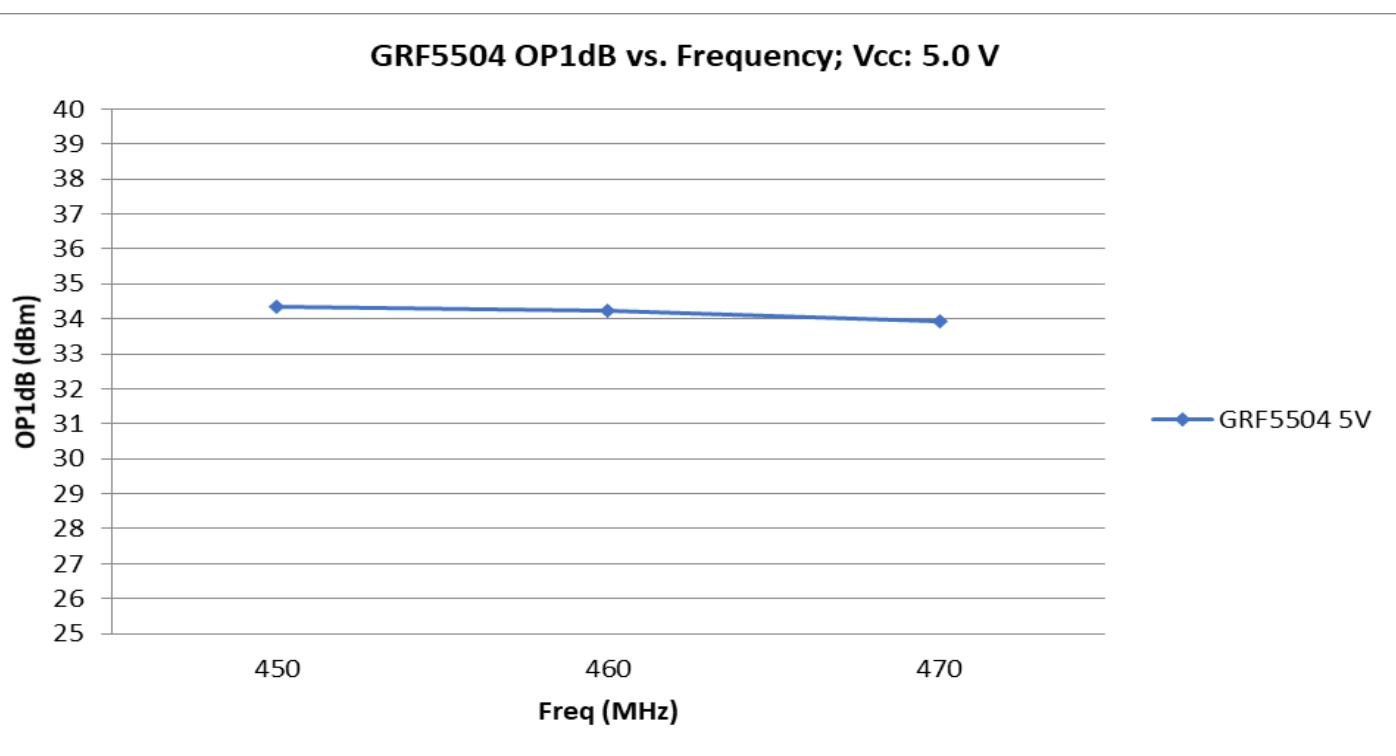
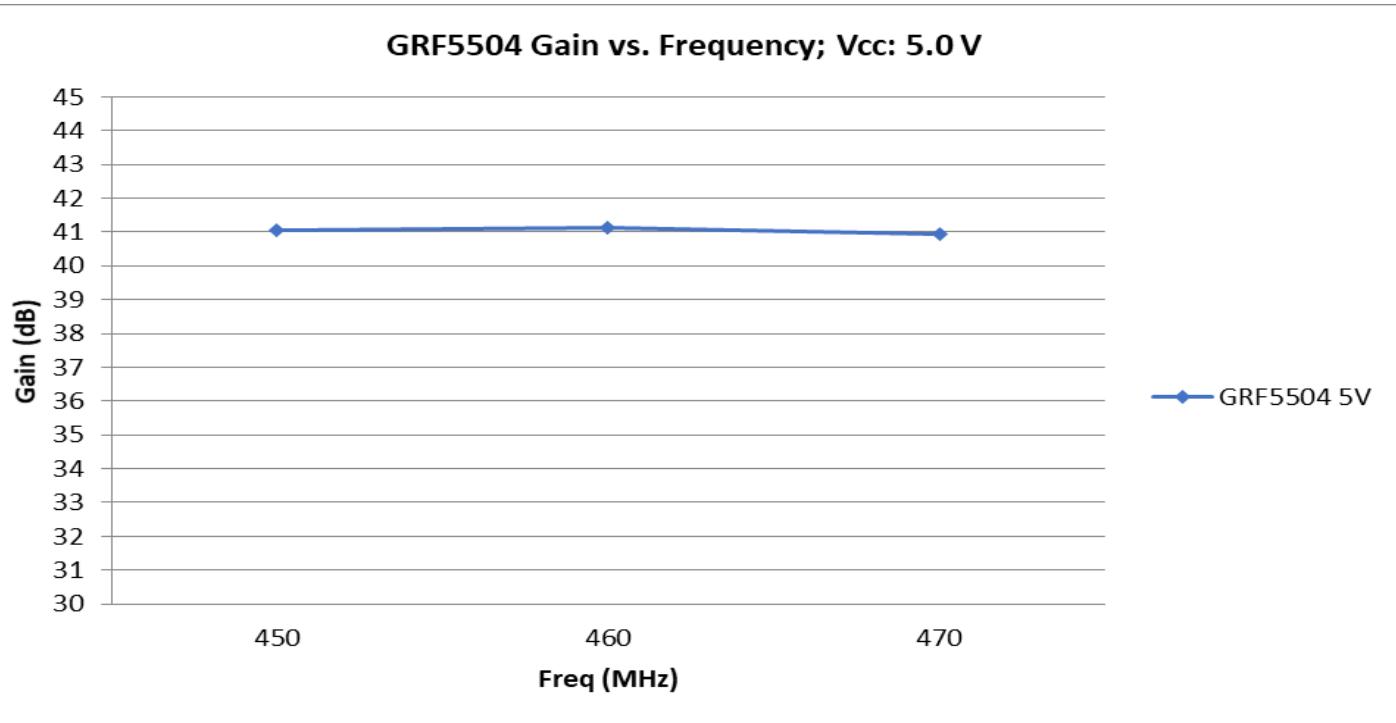
GRF5504

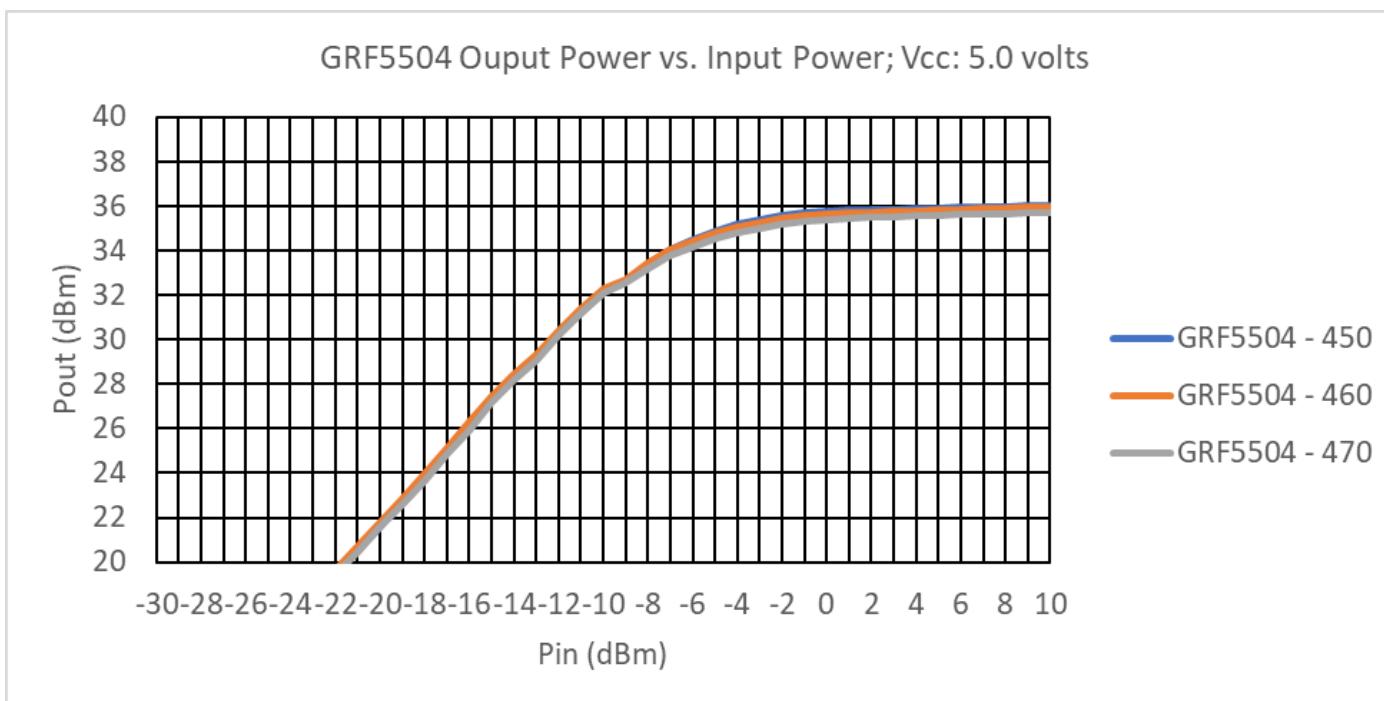
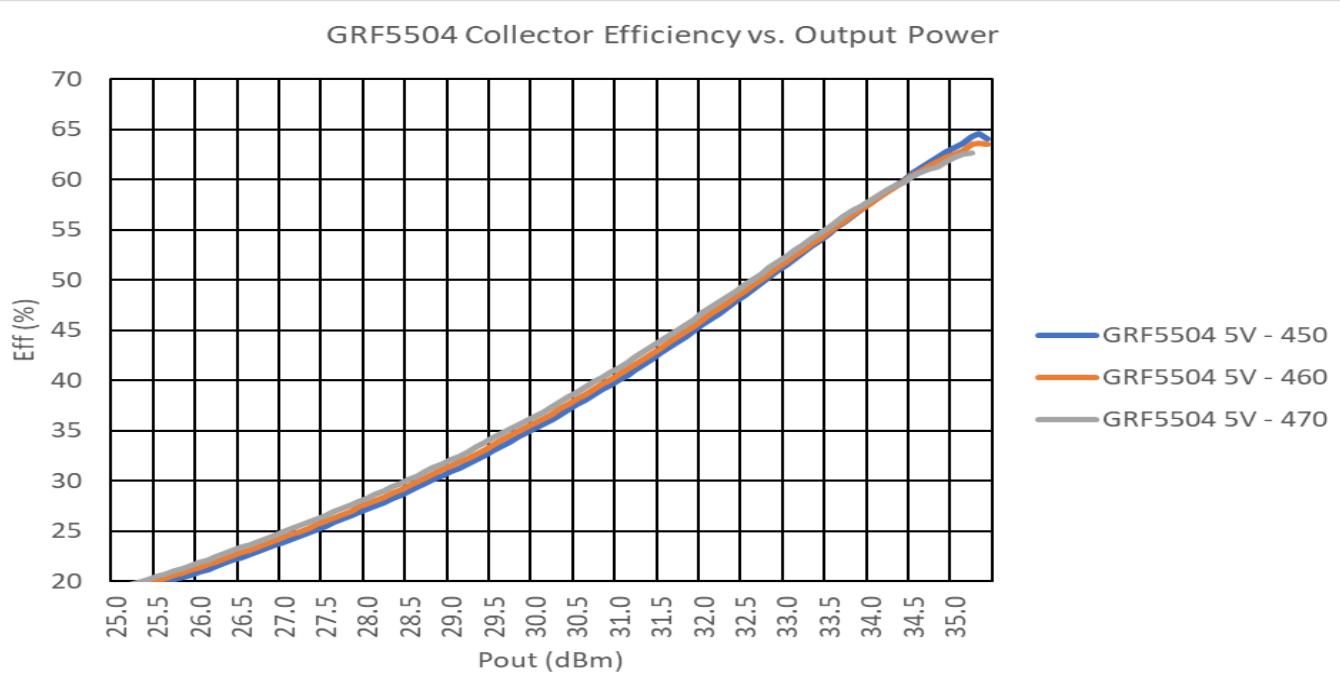
High Efficiency, 3.5 Watt PA
Tuning Range: 0.4 to 0.5 GHz

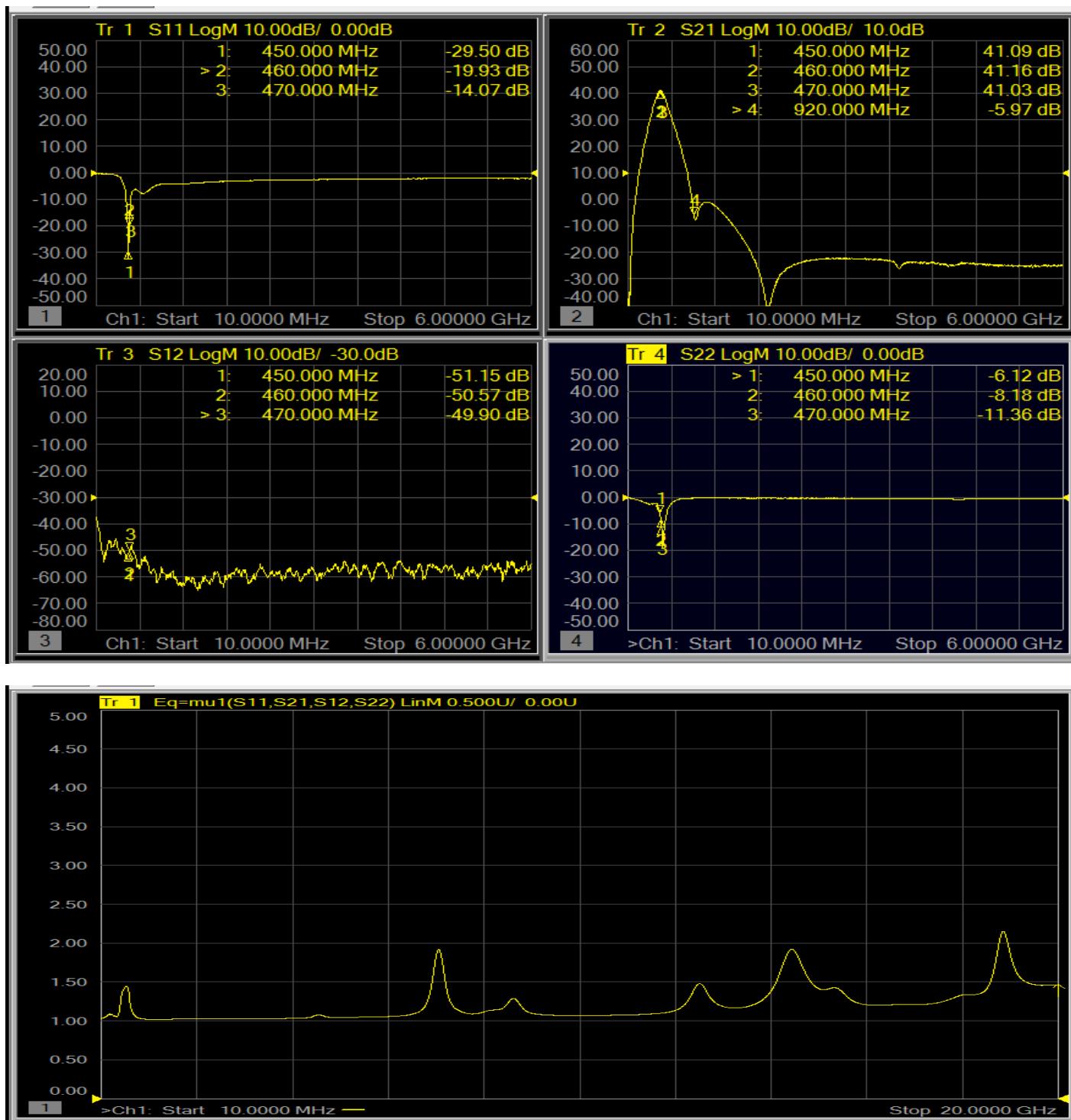
Nominal Operating Parameters:

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Performance: (450 to 470 MHz Tune @25C)						Bias: 5.0 V /120 mA I _{CCQ} unless otherwise noted.
Test Frequency	F _{TEST}		460		MHz	
Gain (Small Signal)	S(2,1)		41.0		dB	
Output 1dB Compression Power	OP1dB		34.0		dBm	
Saturated Output Power	P _{SAT}		35.5		dBm	
Power Added Efficiency (at P _{SAT})	PAE		64		%	
Switching Rise Time	T _{RISE}		TBD		ns	
Switching Fall Time	T _{FALL}		TBD		ns	
Quiescent Supply Current	I _{CCQ}		120		mA	V _{CC} = Venable1/2 = 5.0 volts
Enable Current 1	I _{ENABLE1}		1.2		mA	
Enable Current 2	I _{ENABLE2}		TBD		mA	
Disabled Mode						
Supply Current (Leakage)	I _{CC}		1.0		uA	
Thermal Data (RF Applied)						
Thermal Resistance: (IR Scan Method)	Θ _{JC}		19		°C/W	RF Input: 5 dBm
Junction Temperature @ +85C Reference (package heat sink)	T _{JUNCTION}		123		°C	V _{CC} : 5.0 volts; I _{CC} : 1100mA P _{RF} : 3.5 W; P _{DISS} : 2.0 W

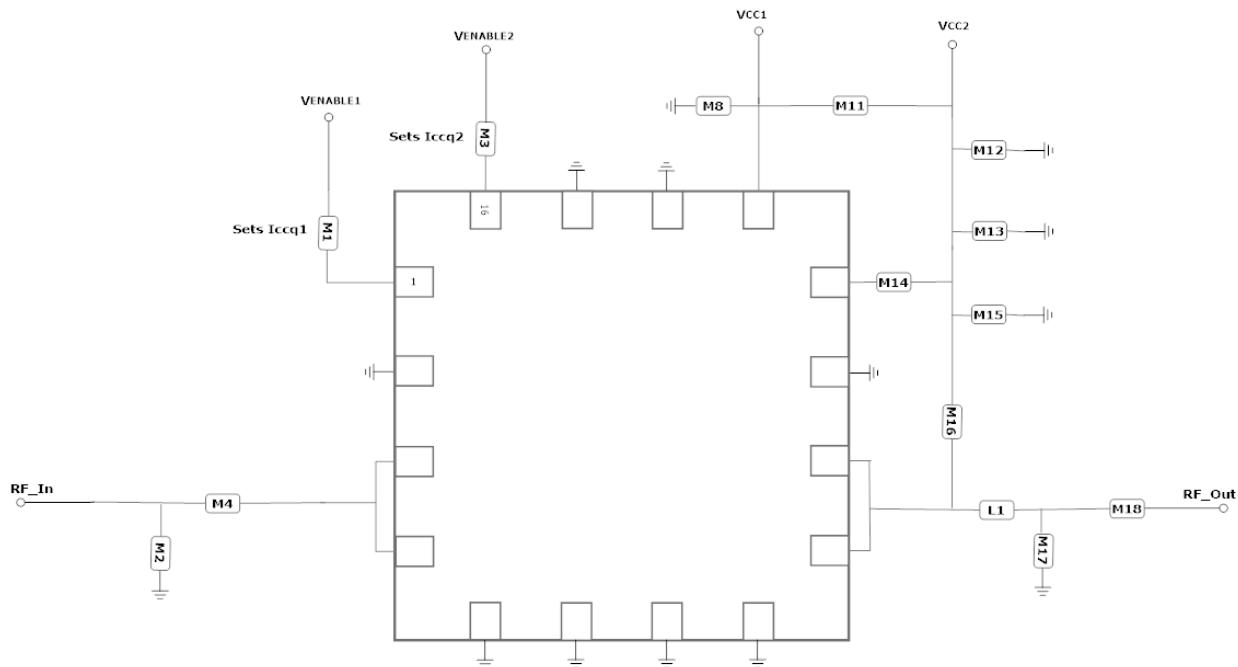
Note: MTTF >10^6 hours for T_{JUNCTION} < =170 degrees C.

GRF5504 Evaluation Board Measured Data; Vcc: 5.0 volts


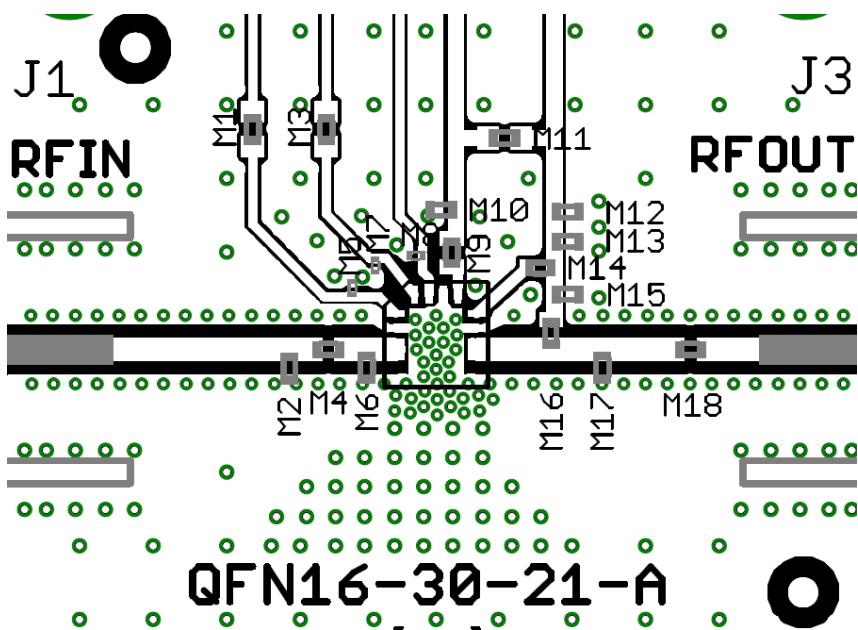
GRF5504 Evaluation Board Measured Data; Vcc: 5.0 volts


GRF5504 Evaluation Board Small Signal S-Pars: (450 to 470 MHz Tune)


Note: Mu factor ≥ 1.0 implies unconditional stability



GRF5504 Application Schematic: (450 to 470 MHz)



GRF5504 Evaluation Board Assembly Drawing



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GRF5504

High Efficiency, 3.5 Watt PA
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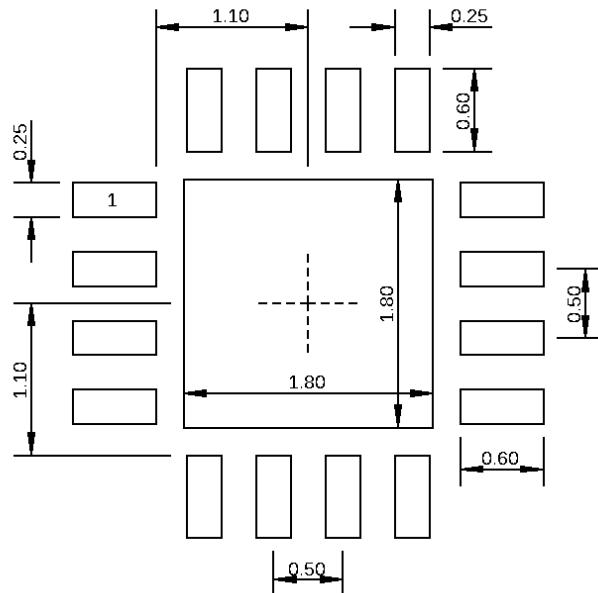
GRF5504 Evaluation Board BOM: (450 to 470 MHz tune)

Vcc= Venable1= Venable2 = 5.0 volts

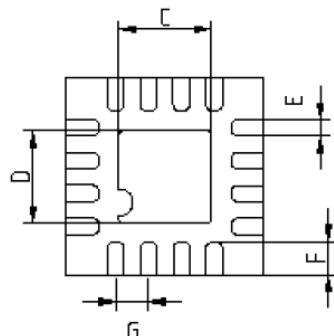
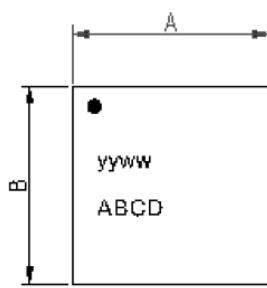
Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Resistor	Various	5%	2.4k Ohm	0402	ok
M2	Inductor	Murata	LQG	6.8 nH	0402	ok
M3	Resistor	Various	5%	5.4k Ohm	0402	ok
M4	Capacitor	Murata	GJM	27 pF	0402	ok
M9	Inductor	Murata	LQW	5.6 nH	0402	ok
M10	Capacitor	Murata	GRM	0.1 uF	0402	ok
M11	Resistor (Jumper)	Various	5%	0 Ohm	0402	ok
M12	Capacitor	Murata	GRM	0.1 uF	0402	ok
M13	Capacitor	Murata	GRM	100 pF	0402	ok
M14	Resistor (Jumper)	Various	5%	0 Ohm	0402	ok
M16	Inductor: High Q	Coilcraft	Micro Spring	23 nH	—	ok
L1 (near M17)	Inductor	Coilcraft	HP	3.3 nH	0402	ok
M17	Capacitor	Murata	GJM	20 pF	0402	ok
M18	Capacitor	Murata	GJM	15 pF	0402	ok
Evaluation Board	QFN16-30-21-A					

GRF5504 Bias Resistor (M1) Selection Curves (TBD)

GRF5504 Bias Resistor (M2) Selection Curves (TBD)



Dimensions in millimeters

3.0 mm QFN-16 Suggested PCB Footprint (Top View)

Bottom View

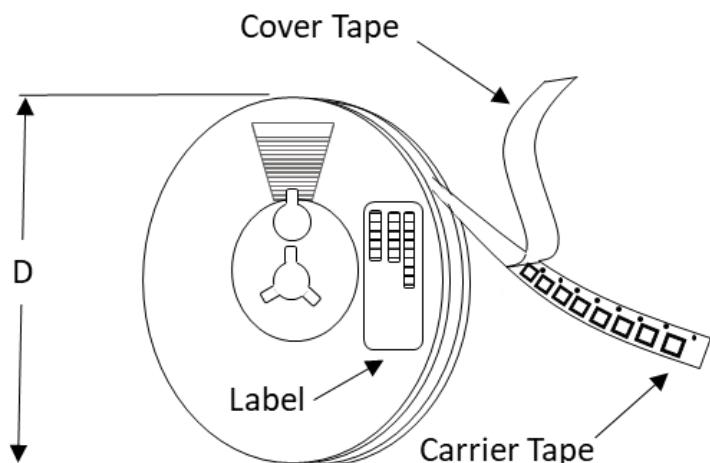
Dimensions (MM)	
A	3.0 Bsc
B	3.0 Bsc
C	1.40 +/- 0.10
D	1.40 +/- 0.10
E	.25 + 0.05 / - 0.07
F	.50 +/- 0.05
G	.50 Bsc.
H	.85 +/- 0.05
J	.20 Ref.

3.0 mm QFN-16 Package Dimensions

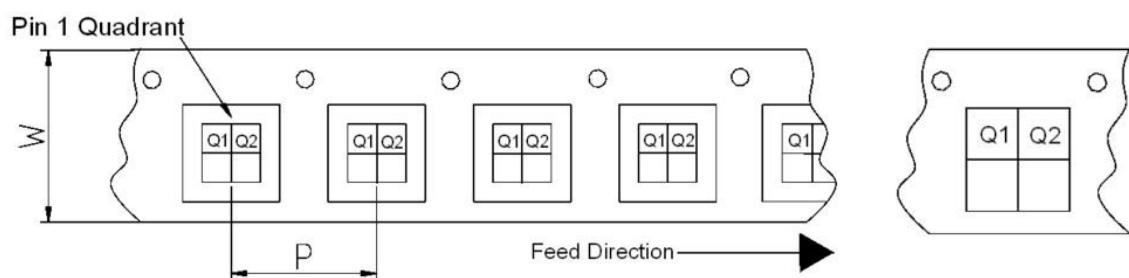
Tape and Reel Information:

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling'. Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



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Tape and Reel Specification and Device Package Information Table

Package				Carrier Tape			Reel	
Type	Dimensions (mm)	Leads	Weight (mg)	Width (W) (mm)	Pocket Pitch (P) (mm)	Pin 1 Quadrant	Diameter (D) (inches)	Units per Reel
QFN	2.0 x 2.0 x 0.50	12	7	8	4	Q1	7	2500
QFN	3.0 x 3.0 x 0.85	16	24	12	8	Q1	7	1500
DFN	1.5 x 1.5 x 0.45	6	4	8	4	Q1	7	2500
DFN	2.0 x 2.0 x 0.75	8	12	8	4	Q1	7	2500
LFM	3.5 x 3.5 x 0.75	See note	TBD	12	8	Q2	7	1500
LFM	4.0 x 4.0 x 0.75	See note	TBD	12	8	Q2	7	1500

Note: Lead count may vary. Reference applicable product data sheet



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High Efficiency, 3.5 Watt PA
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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

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