GeneSiC
SEMICONDUCTOR

SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/images/products_sic/thyristor/GA01PNS80-220_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GA01PNS80-220.

```
*      MODEL OF GeneSiC Semiconductor Inc.
*
*      $Revision:   1.1             $
*      $Date:   30-APR-2015         $
*
*      GeneSiC Semiconductor Inc.
*      43670 Trade Center Place Ste. 155
*      Dulles, VA 20166
*
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*
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
*
* Start of GA01PNS80-220 SPICE Model
*
.MODEL GA01PNS80 D
+ IS       9.2491e-015
+ RS       1.02512
+ N        3.3373
+ IKF      0.00011784
+ EG       3.23
+ XTI      25
+ TRS1     -0.0024
+ CJO      2.7E-11
+ VJ       2.304
+ M        0.376
+ FC       0.5
+ BV       8000
+ IBV      1.00E-03
+ VPK      8000
+ IAVE     1
+ TYPE     SiC_PiN
+ MFG      GeneSiC_Semi
*
* End of GA01PNS80-220 SPICE Model
```