IGBT - Field Stop, Trench
650 V, 75 A

FGHL75T65LQDTL4

Description
Field stop 4th generation Low $V_{CE(sat)}$ IGBT technology and Full current rated copack Diode technology.

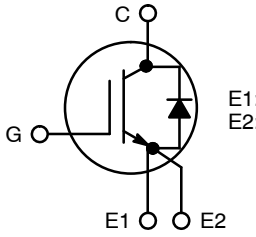
Features
- Maximum Junction Temperature: T_J = 175°C
- Positive Temperature Co−efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(Sat)}$ = 1.15 V (Typ.) @ I_C = 75 A
- 100% of the Part are Tested for I_{LM} (Note 2)
- Smooth & Optimized Switching
- Tight Parameter Distribution
- Co−Packed with Soft and Fast Recovery Diode
- RoHS Compliant

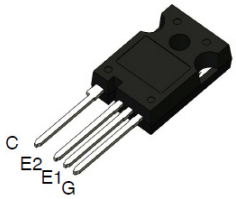
Typical Applications
- Solar Inverter
- UPS, ESS
- PFC, Converters

ON Semiconductor®

www.onsemi.com

V_{CES}	I_C	$V_{CE(Sat)}$
650 V	75 A	1.15 V





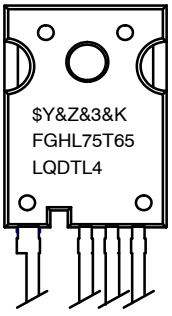
TO−247−4LD
CASE 340CJ

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector to Emitter Voltage	V_{CES}	650	V
Gate to Emitter Voltage Transient Gate to Emitter Voltage	V_{GES}	±20 ±30	V
Collector Current @ T_C = 25°C (Note 1)	I_C	80	A
Collector Current @ T_C = 100°C		75	
Pulsed Collector Current (Note 2)	I_{LM}	300	A
Pulsed Collector Current (Note 3)	I_{CM}	300	A
Diode Forward Current @ T_C = 25°C (Note 1)	I_F	80	A
Diode Forward Current @ T_C = 100°C		75	
Pulsed Diode Maximum Forward Current	I_{FM}	300	A
Maximum Power Dissipation @ T_C = 25°C	P_D	469	W
Maximum Power Dissipation @ T_C = 100°C		234	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	−55 to +175	°C
Maximum Lead Temp. for Soldering Purposes (1/8″ from case for 5 s)	T_L	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
1. Value limit by bond wire.
2. V_{CC} = 400 V, V_{GE} = 15 V, I_C = 300 A, Inductive Load, 100% Tested.
3. Repetitive rating: Pulse width limited by max. Junction temperature.

MARKING DIAGRAM



$Y = ON Semiconductor Logo
&Z = Assembly Plant Code
&3 = 3−Digit Data Code
&K = 2−Digit Lot Traceability Code
FGHL75T65LQDTL4 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
FGHL75T65LQDTL4	TO−247−4LD	30 Units / Rail

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance Junction−to−Case, for IGBT	$R_{\theta JC}$	0.32	°C/W
Thermal Resistance Junction−to−Case, for Diode	$R_{\theta JC}$	0.48	°C/W
Thermal Resistance Junction−to−Ambient	$R_{\theta JA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Collector to Emitter Breakdown Voltage	V_{GE} = 0 V, I_C = 1 mA	BV_{CES}	650	–	–	V
Temperature Coefficient of Breakdown Voltage	V_{GE} = 0 V, I_C = 1 mA	$\Delta BV_{CES} / \Delta T_J$	–	0.6	–	V/°C
Collector to Emitter Cut−off Current	V_{GE} = 0 V, V_{CE} = 650 V	I_{CES}	–	–	250	μA
Gate Leakage Current	V_{GE} = 20 V, V_{CE} = 0 V	I_{GES}	–	–	±400	nA
ON CHARACTERISTICS						
Gate to Emitter Threshold Voltage	V_{GE} = V_{CE}, I_C = 75 mA	$V_{GE(th)}$	3.0	4.5	6.0	V
Collector to Emitter Saturation Voltage	V_{GE} = 15 V, I_C = 75 A, T_J = 25°C V_{GE} = 15 V, I_C = 75 A, T_J = 175°C	$V_{CE(sat)}$	– –	1.15 1.22	1.35 –	V
DYNAMIC CHARACTERISTICS						
Input Capacitance	V_{CE} = 30 V, V_{GE} = 0 V, f = 1 MHz	C_{ies}	–	15030	–	pF
Output Capacitance		C_{oes}	–	181	–	
Reverse Transfer Capacitance		C_{res}	–	68	–	
Gate Charge Total	V_{CC} = 400 V, I_C = 75 V, V_{GE} = 15 V	Q_g	–	779	–	nC
Gate to Emitter Charge		Q_{ge}	–	69	–	
Gate to Collector Charge		Q_{gc}	–	251	–	
SWITCHING CHARACTERISTICS, INDUCTIVE LOAD						
Turn−on Delay Time	T_J = 25°C, V_{CC} = 400 V, I_C = 37.5 A, R_G = 4.7 Ω, V_{GE} = 15 V	$t_{d(on)}$	–	40	–	ns
Rise Time		t_r	–	12	–	
Turn−off Delay Time		$t_{d(off)}$	–	560	–	
Fall Time		t_f	–	144	–	
Turn−on Switching Loss		E_{on}	–	0.51	–	mJ
Turn−off Switching Loss		E_{off}	–	1.39	–	
Total Switching Loss		E_{ts}	–	1.9	–	
Turn−on Delay Time	T_J = 25°C, V_{CC} = 400 V, I_C = 75 A, R_G = 4.7 Ω, V_{GE} = 15 V	$t_{d(on)}$	–	40	–	ns
Rise Time		t_r	–	20	–	
Turn−off Delay Time		$t_{d(off)}$	–	548	–	
Fall Time		t_f	–	112	–	
Turn−on Switching Loss		E_{on}	–	1.01	–	mJ
Turn−off Switching Loss		E_{off}	–	2.53	–	
Total Switching Loss		E_{ts}	–	3.54	–	

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified) (continued)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
SWITCHING CHARACTERISTICS, INDUCTIVE LOAD						
Turn−on Delay Time	T_J = 175°C, V_{CC} = 400 V, I_C = 37.5 A, R_G = 4.7 Ω, V_{GE} = 15 V	$t_{d(on)}$	−	32	−	ns
Rise Time		t_r	−	16	−	
Turn−off Delay Time		$t_{d(off)}$	−	640	−	
Fall Time		t_f	−	212	−	
Turn−on Switching Loss		E_{on}	−	1.45	−	mJ
Turn−off Switching Loss		E_{off}	−	2	−	
Total Switching Loss		E_{ts}	−	3.45	−	
Turn−on Delay Time	T_J = 175°C, V_{CC} = 400 V, I_C = 75 A, R_G = 4.7 Ω, V_{GE} = 15 V	$t_{d(on)}$	−	36	−	ns
Rise Time		t_r	−	28	−	
Turn−off Delay Time		$t_{d(off)}$	−	616	−	
Fall Time		t_f	−	168	−	
Turn−on Switching Loss		E_{on}	−	2.4	−	mJ
Turn−off Switching Loss		E_{off}	−	3.64	−	
Total Switching Loss		E_{ts}	−	6.04	−	
DIODE CHARACTERISTICS						
Diode Forward Voltage	I_F = 75 A, T_J = 25°C I_F = 75 A, T_J = 175°C	V_F	− −	1.65 1.55	2.1 −	V
Reverse Recovery Energy	T_J = 25°C, V_R = 400 V, I_F = 37.5 A, di_F/dt = 1000 A/μs	E_{REC}	−	105	−	μJ
Reverse Recovery Time		T_{rr}	−	59	−	ns
Reverse Recovery Charge		Q_{rr}	−	574	−	nC
Reverse Recovery Current		I_{rr}	−	20	−	A
Reverse Recovery Energy	T_J = 25°C, V_R = 400 V, I_F = 75 A, di_F/dt = 1000 A/μs	E_{REC}	−	152	−	μJ
Reverse Recovery Time		T_{rr}	−	87	−	ns
Reverse Recovery Charge		Q_{rr}	−	794	−	nC
Reverse Recovery Current		I_{rr}	−	18	−	A
Reverse Recovery Energy	T_J = 175°C, V_R = 400 V, I_F = 37.5 A, di_F/dt = 1000 A/μs	E_{REC}	−	550	−	μJ
Reverse Recovery Time		T_{rr}	−	119	−	ns
Reverse Recovery Charge		Q_{rr}	−	2154	−	nC
Reverse Recovery Current		I_{rr}	−	36	−	A
Reverse Recovery Energy	T_J = 175°C, V_R = 400 V, I_F = 75 A, di_F/dt = 1000 A/μs	E_{REC}	−	764	−	μJ
Reverse Recovery Time		T_{rr}	−	145	−	ns
Reverse Recovery Charge		Q_{rr}	−	2947	−	nC
Reverse Recovery Current		I_{rr}	−	40	−	A

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

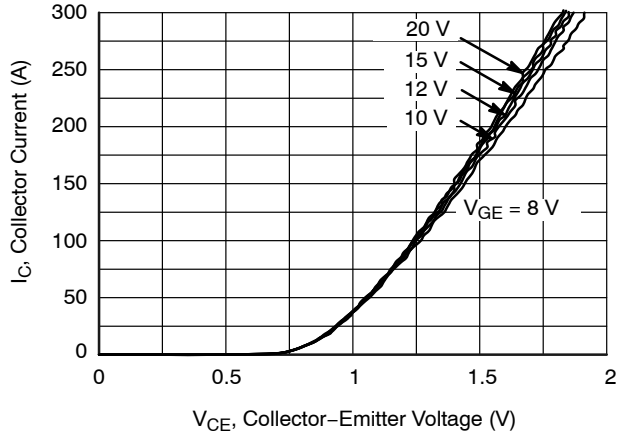


Figure 1. Typical Output Characteristics (T_J = 25°C)

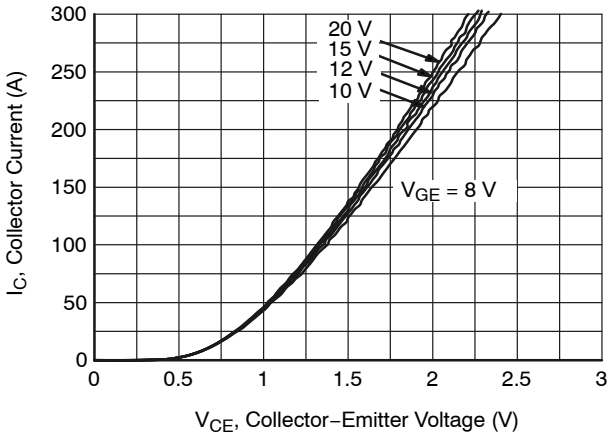


Figure 2. Typical Output Characteristics (T_J = 175°C)

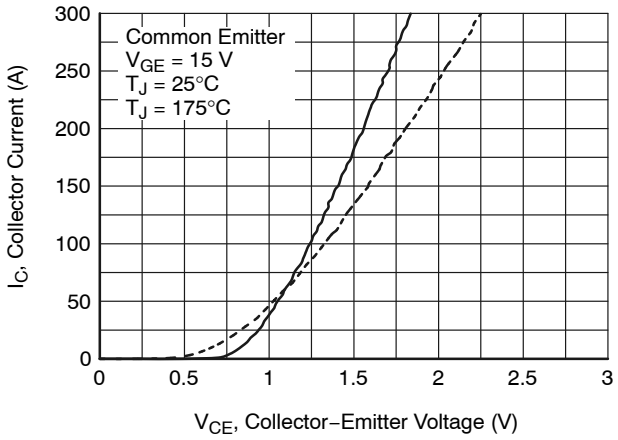


Figure 3. Typical Saturation Voltage Characteristics

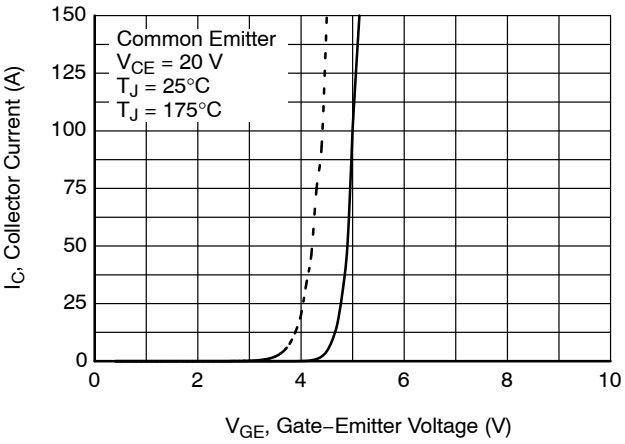


Figure 4. Typical Transfer Characteristics

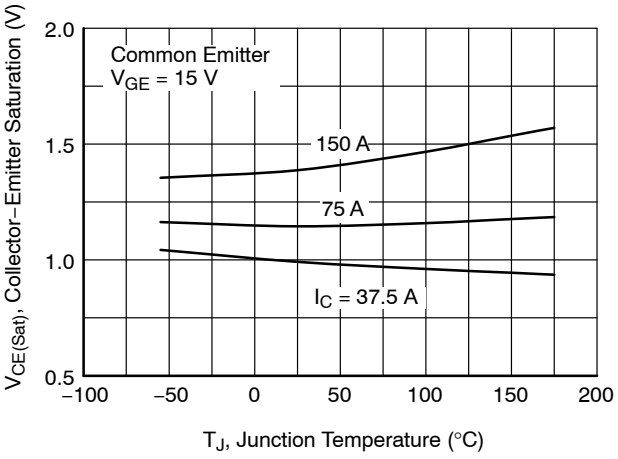


Figure 5. Saturation Voltage vs. Junction Temperature

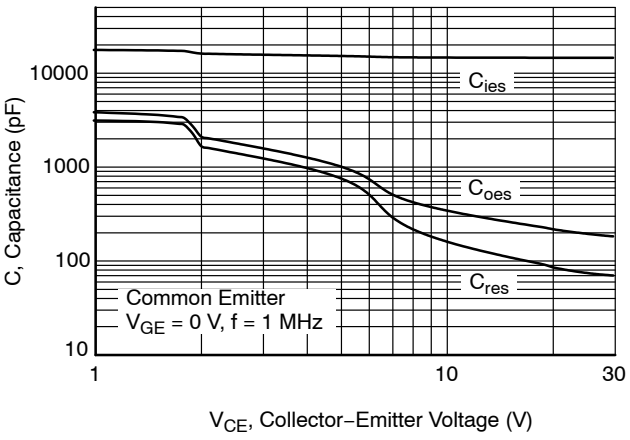


Figure 6. Capacitance Characteristics

TYPICAL CHARACTERISTICS (continued)

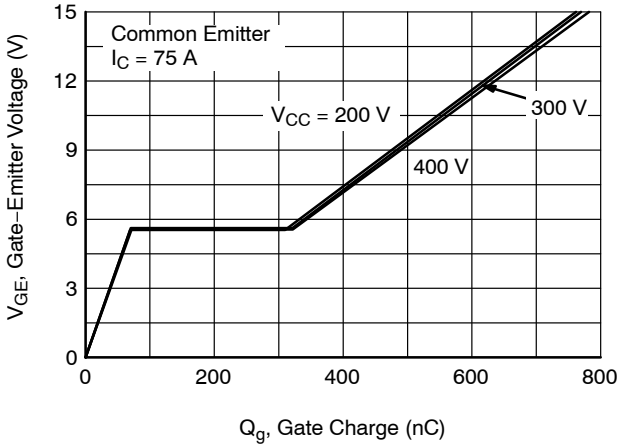


Figure 7. Gate Charge Characteristics

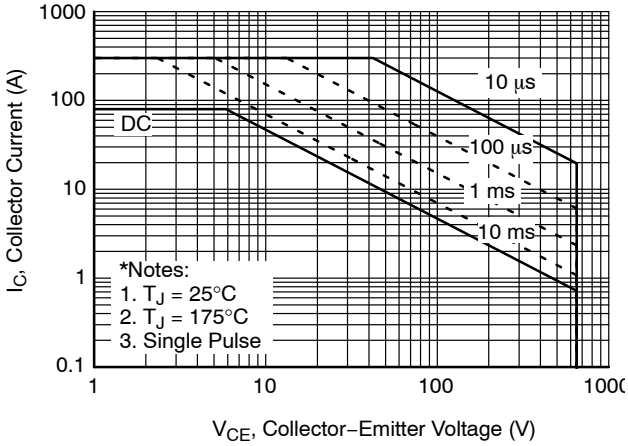


Figure 8. SOA Characteristics

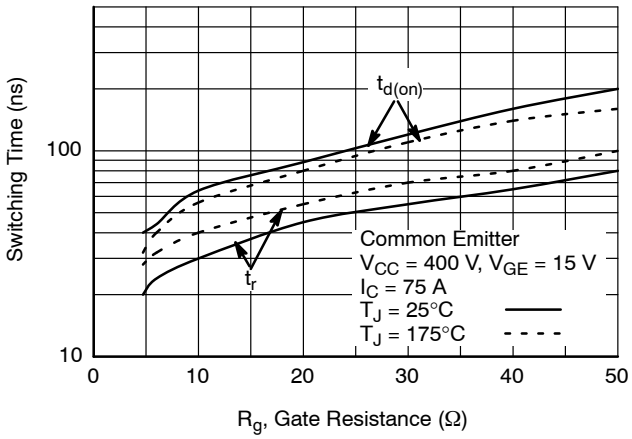


Figure 9. Turn−On Characteristics vs. Gate Resistance

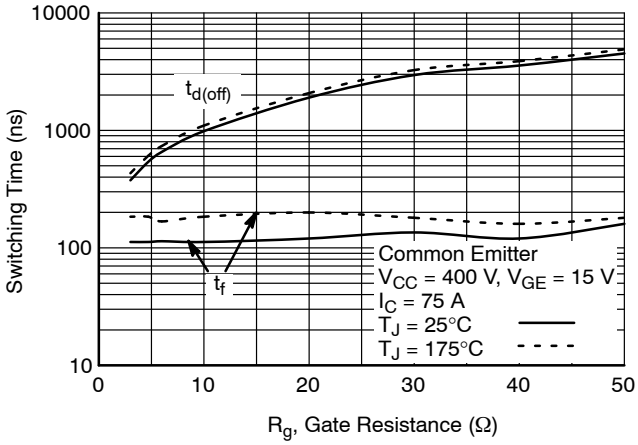


Figure 10. Turn−Off Characteristics vs. Gate Resistance

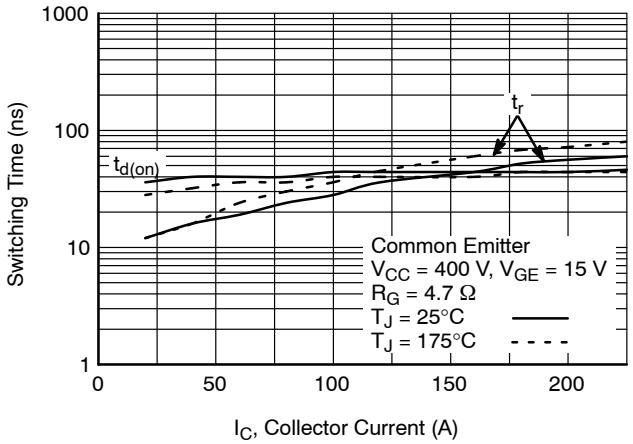


Figure 11. Turn−On Characteristics vs. Collector Current

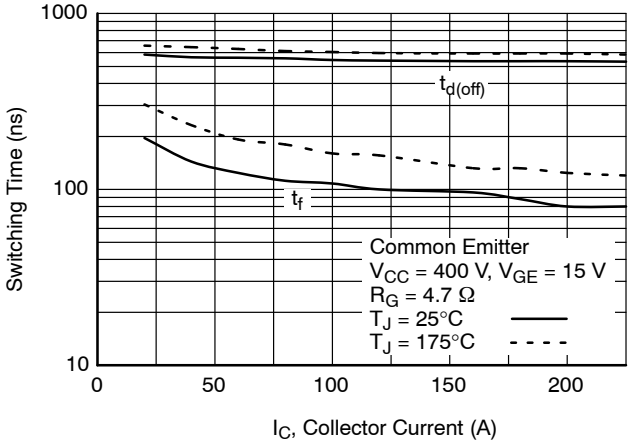


Figure 12. Turn−Off Characteristics vs. Collector Current

TYPICAL CHARACTERISTICS (continued)

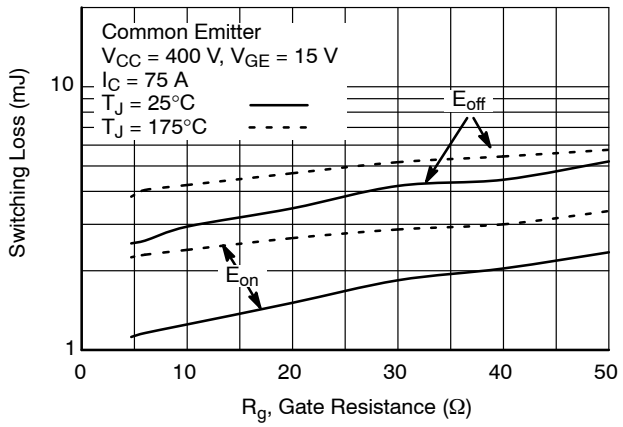


Figure 13. Switching Loss vs. Gate Resistance

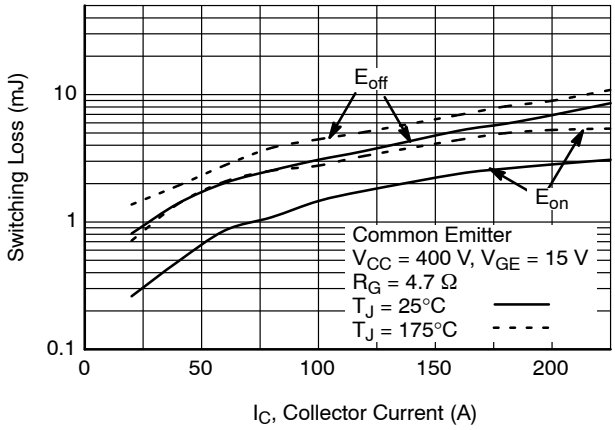


Figure 14. Switching Loss vs. Collector Current

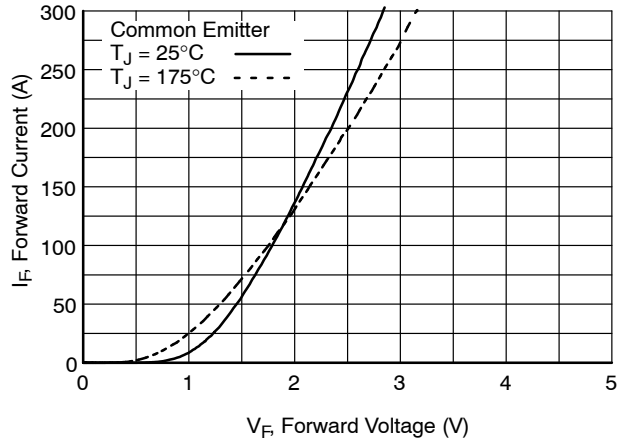


Figure 15. Forward Characteristics

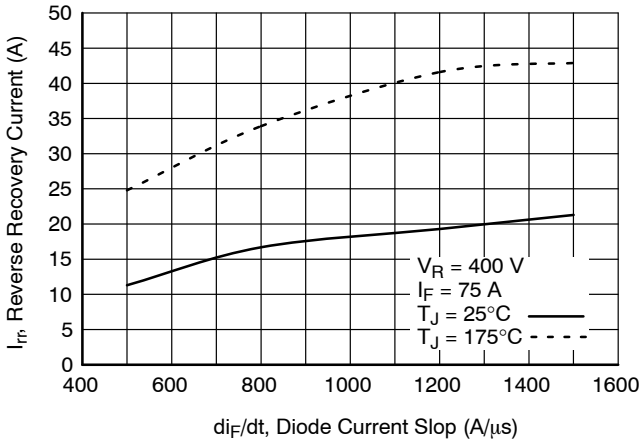


Figure 16. Reverse Recovery Current

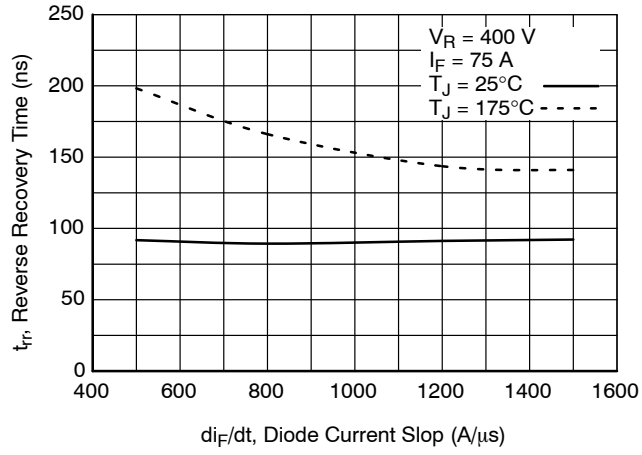


Figure 17. Reverse Recovery Time

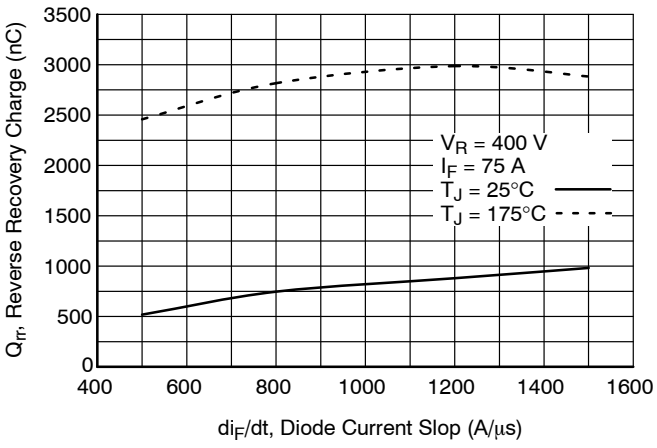


Figure 18. Stored Charge

TYPICAL CHARACTERISTICS (continued)

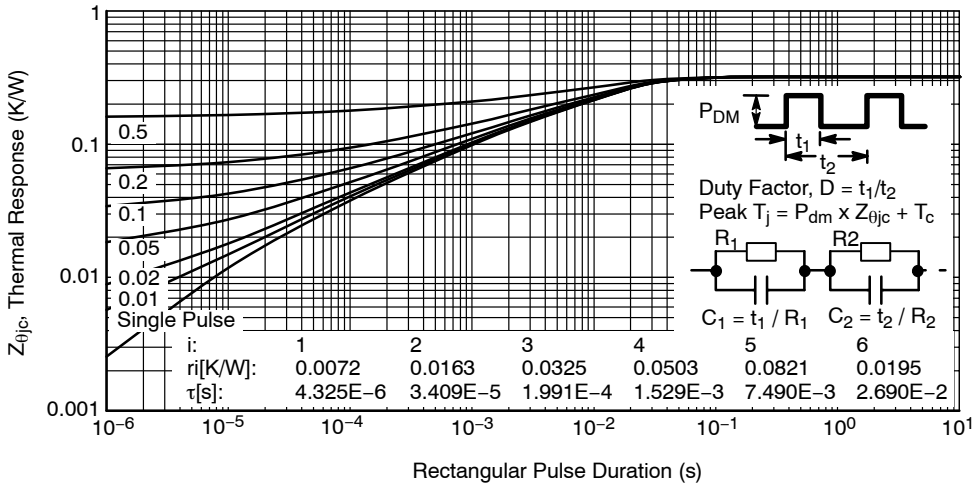


Figure 19. Transient Thermal Impedance of IGBT

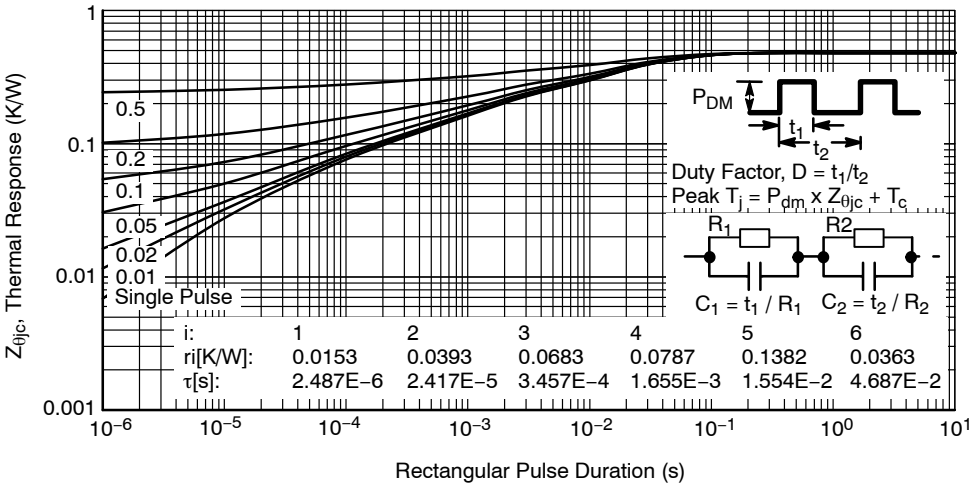


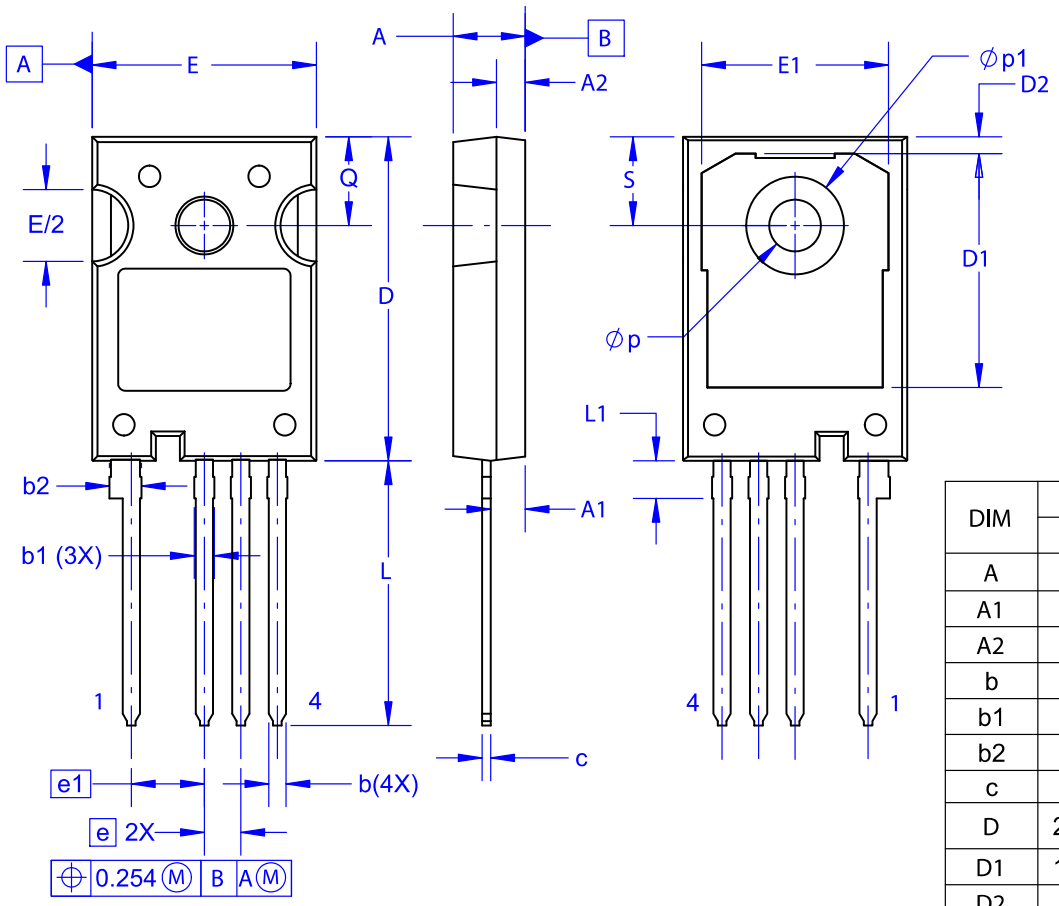
Figure 20. Transient Thermal Impedance of Diode

MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

ON Semiconductor®

TO−247−4LD
CASE 340CJ
ISSUE A

DATE 16 SEP 2019



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.10	2.40	2.70
A2	1.80	2.00	2.20
b	1.07	1.20	1.33
b1	1.20	1.40	1.60
b2	2.02	2.22	2.42
c	0.50	0.60	0.70
D	22.34	22.54	22.74
D1	16.00	16.25	16.50
D2	0.97	1.17	1.37
e	2.54 BSC		
e1	5.08 BSC		
E	15.40	15.60	15.80
E1	12.80	13.00	13.20
E/2	4.80	5.00	5.20
L	18.22	18.42	18.62
L1	2.42	2.62	2.82
p	3.40	3.60	3.80
p1	6.60	6.80	7.00
Q	5.97	6.17	6.37
S	5.97	6.17	6.37

NOTES:

A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
B. DIMENSIONS ARE EXCLUSIVE OF BURRS,MOLD FLASH,AND TIE BAR EXTRUSIONS.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D. DRAWING CONFORMS TO ASME Y14.5-2009.

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