EVM3650C-QW-00A

17V, 6A, 1.2MHz, Ultra-Thin, Synchronous, Step-Down Power Module Evaluation Board

DESCRIPTION

The EVM3650C-QW-00A is an evaluation board designed to demonstrate the capabilities of the MPM3650C, a fully integrated, high-frequency, synchronous, rectified, step-down power module with an internal inductor. It offers a highly compact solution to achieve 6A of continuous output current (I_{OUT}) across a wide 2.75V to 17V input voltage (V_{IN}) range, with excellent load and line regulation. The MPM3650C offers synchronous operation for high efficiency across the I_{OUT} load range.

Constant-on-time (COT) control enables ultra-fast transient response and easy loop design, as well as very tight output regulation.

Full protection features include short-circuit protection (SCP), over-current protection (OCP), under-voltage protection (UVP), and thermal shutdown.

The MPM3650C is available in a QFN-24 (4mmx6mmx1.6mm) package.

ELECTRICAL SPECIFICATIONS

Parameter	Symbol	Value	Units
Input voltage	V_{IN}	2.75 to 17	V
Output voltage	V_{OUT}	1	V
Output current	I_{OUT}	6	A

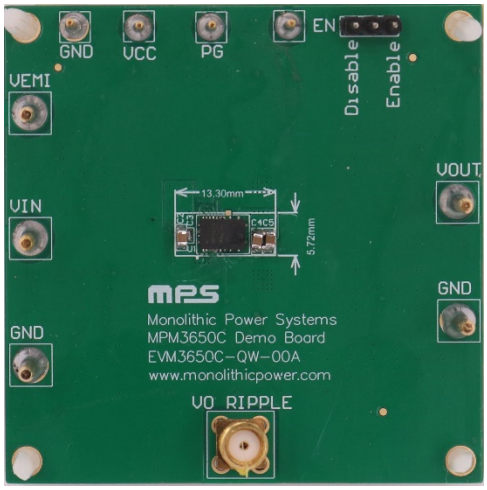
FEATURES

- Wide 2.75V to 17V Operating Input Voltage (V_{IN}) Range
- Output Current (I_{OUT}) for Different Output Voltage (V_{OUT}) Ranges:
  - 6A I_{OUT} for 0.6V to 1.8V V_{OUT}
  - 5A I_{OUT} for 1.8V to 3.3V V_{OUT}
- 3.3V Maximum V_{OUT}
- Forced Continuous Conduction Mode (CCM) for Low Output Voltage Ripple
- High-Efficiency Synchronous Operation
- Supports Pre-Biased Start-Up
- Fixed 1200kHz Switching Frequency (f_{SW})
- Externally Configurable Soft-Start Time (t_{SS})
- Enable (EN) and Power Good (PG) for Power Sequencing
- Over-Current Protection (OVP) with Hiccup Mode
- Thermal Shutdown
- Available in a QFN-24 (4mmx6mmx1.6mm) Package

APPLICATIONS

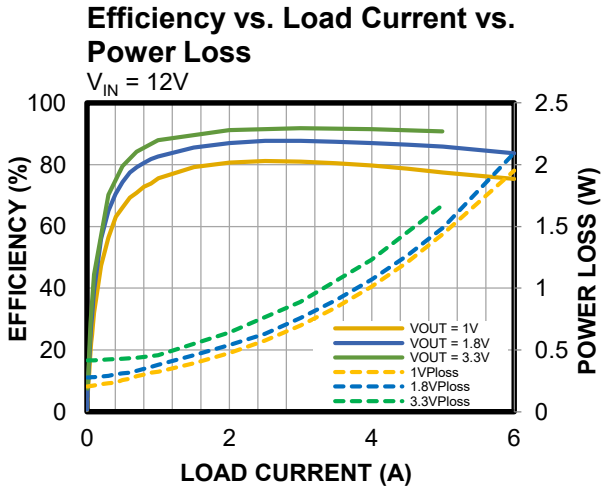
- Field-Programmable Gate Array (FPGA) Power Systems
- Optical Modules
- Telecommunications
- Networking
- Industrial Equipment

EVM3650C-QW-00A EVALUATION BOARD



LxWxH (63.5mmx63.5mmx1.6mm)

Board Number	MPS IC Number
EVM3650C-QW-00A	MPM3650CGQY



QUICK START GUIDE

1. Preset the power supply between 2.75V and 17V.
2. Turn off the power supply.
3. Connect the power supply terminals to:
   a. Positive (+): VIN
   b. Negative (-): GND
4. Connect load terminals (≤6A) to:
   a. Positive (+): VOUT
   b. Negative (-): GND
5. After making the connections, turn on the power supply. The board should start up automatically.

EVALUATION BOARD SCHEMATIC

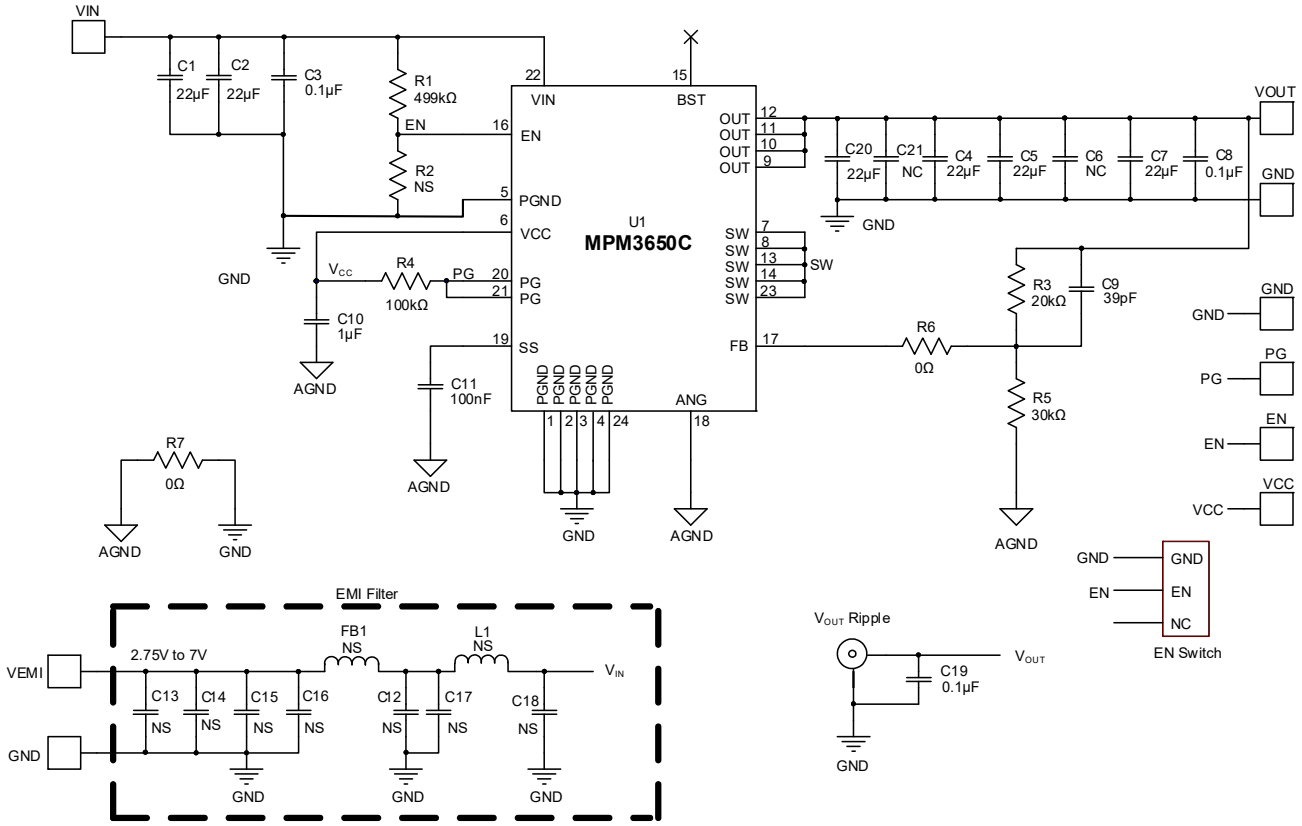


Figure 1: Evaluation Board Schematic

EVM3650C-QW-00A BILL OF MATERIALS

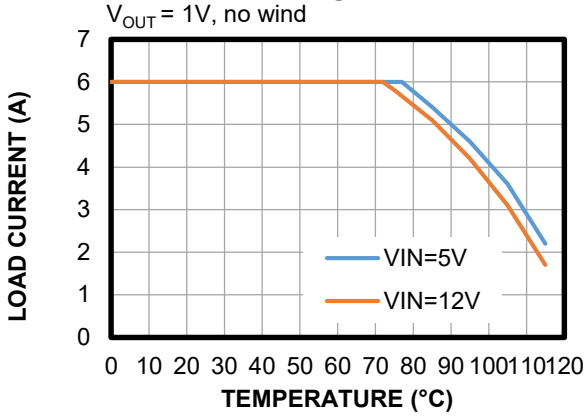
Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer PN
6	C1, C2, C4, C5, C20, C7	22µF	Ceramic capacitor, 25V, X5R	0805	Murata	GRM21BR61E226ME44L
4	C3, C8, C11, C19	0.1µF	Ceramic capacitor, 25V, X5R	0402	Wurth	885012105018
1	C10	1µF	Ceramic capacitor, 25V, X5R	0402	Murata	GRM155R61E105KA12D
1	C9	39pF	Ceramic capacitor, 50V, C0G	0402	Murata	GRM1555C1H390JA01D
1	R1	499kΩ	Film resistor, 1%	0402	Yageo	RC0402FR-07499KL
1	R4	100kΩ	Film resistor, 1%	0402	Yageo	RC0402FR-07100KL
2	R7, R6	0Ω	Film resistor, 1%	0402	Yageo	RC0402FR-070RL
1	R3	20kΩ	Film resistor, 1%	0402	Yageo	RC0402FR-0720KL
1	R5	30kΩ	Film resistor, 1%	0402	Yageo	RC0402FR-0730KL
1	EN	2.54mm	3-pin, single-row, straight socket header	DIP	Wurth	61300311821
0	V_{OUT} ripple	NS				
5	VIN, VEMI, GND, GND, VOUT	Φ2.0	2 copper pins	DIP	Custom	
4	EN, GND, VCC, PG	Φ1.0	1 copper pin	DIP	Custom	
1	U1	MPM3650C	Step-down power module, 17V, 6A, 1.2MHz	QFN-24 (4mmx 6mm)	MPS	MPM3650CGQW

EVB TEST RESULTS

Performance curves and waveforms are tested on the evaluation board, V_{IN} = 12V, V_{OUT} = 1V, T_A = 25°C, unless otherwise noted.

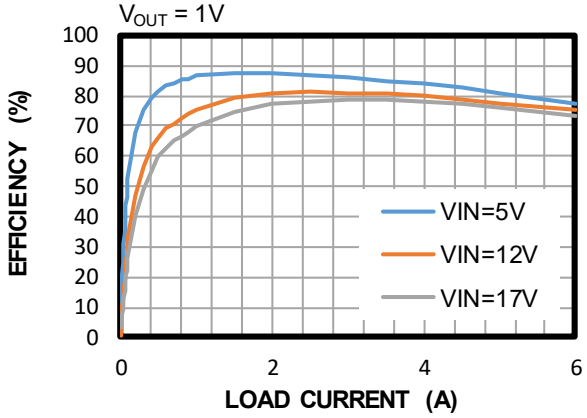
Thermal Derating

V_{OUT} = 1V, no wind



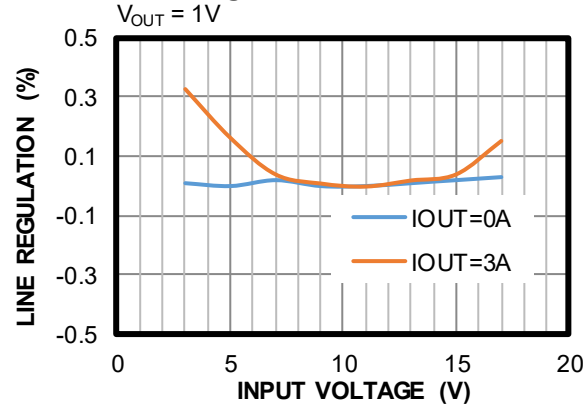
Efficiency vs. Load Current

V_{OUT} = 1V



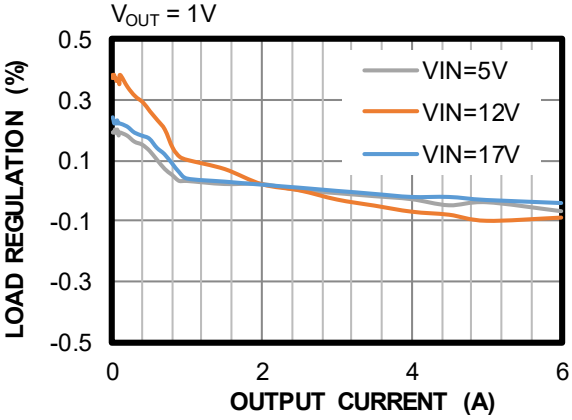
Line Regulation

V_{OUT} = 1V



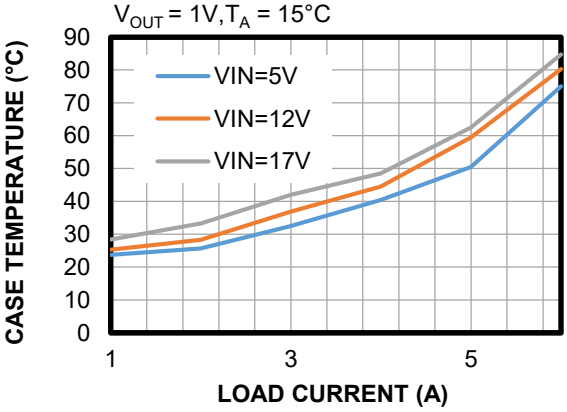
Load Regulation

V_{OUT} = 1V



Case Temperature vs. Load Current

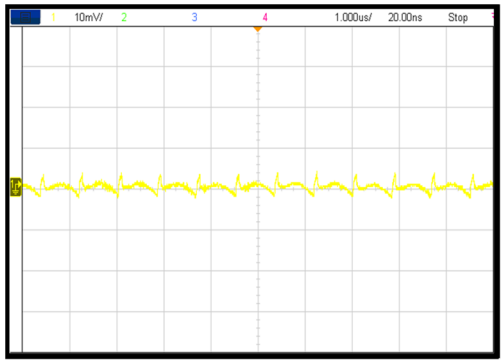
V_{OUT} = 1V, T_A = 15°C



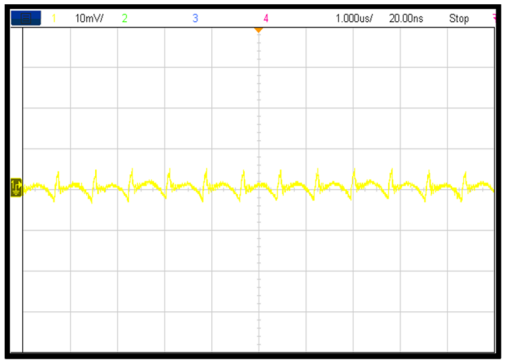
EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board, V_{IN} = 12V, V_{OUT} = 1V, T_A = 25°C, unless otherwise noted.

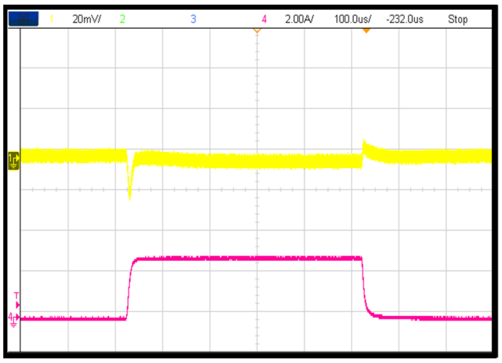
V_{OUT} Ripple
I_{OUT} = 0A



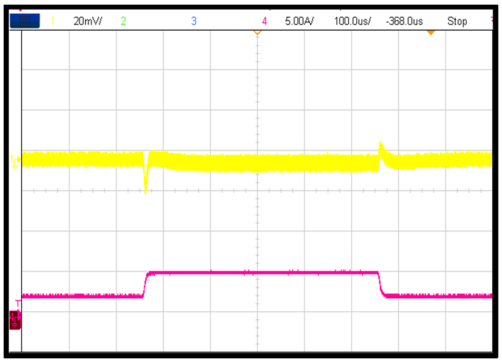
V_{OUT} Ripple
I_{OUT} = 6A



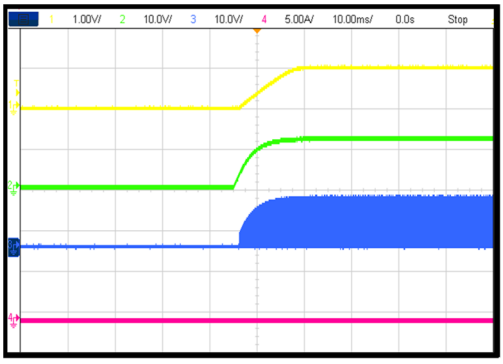
Load Transient
I_{OUT} = 0A to 3A



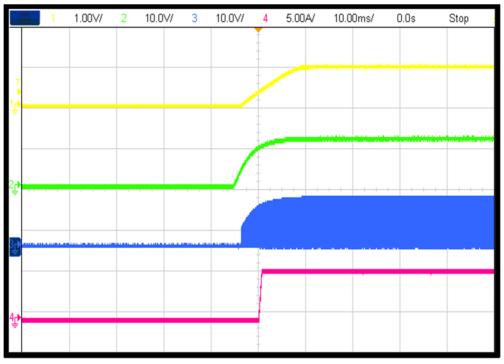
Load Transient
I_{OUT} = 3A to 6A



Start-Up through VIN
I_{OUT} = 0A



Start-Up through VIN
I_{OUT} = 6A

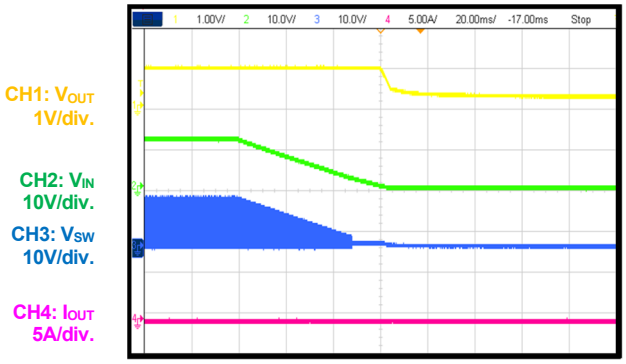


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board, V_{IN} = 12V, V_{OUT} = 1V, T_A = 25°C, unless otherwise noted.

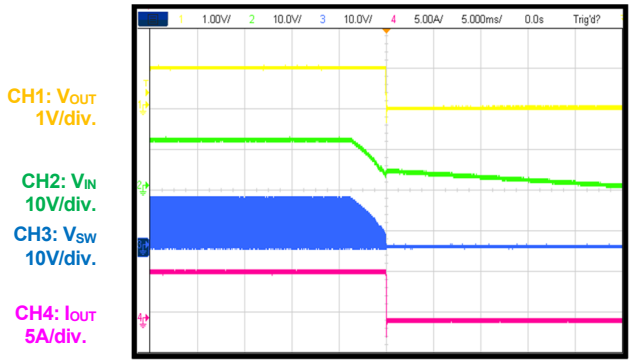
Shutdown through VIN

I_{OUT} = 0A



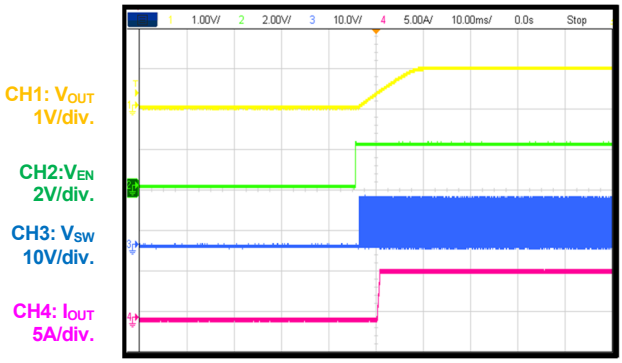
Shutdown through VIN

I_{OUT} = 6A



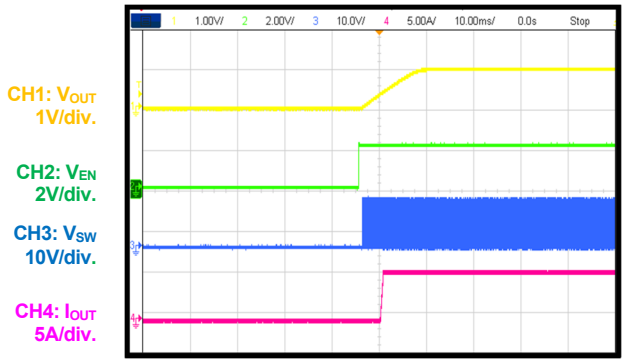
Start-Up through EN

I_{OUT} = 0A



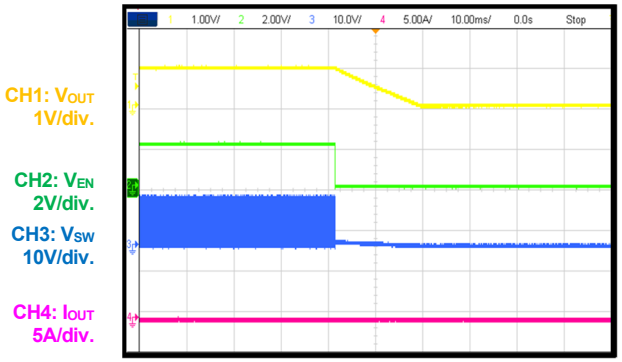
Start-Up through EN

I_{OUT} = 6A



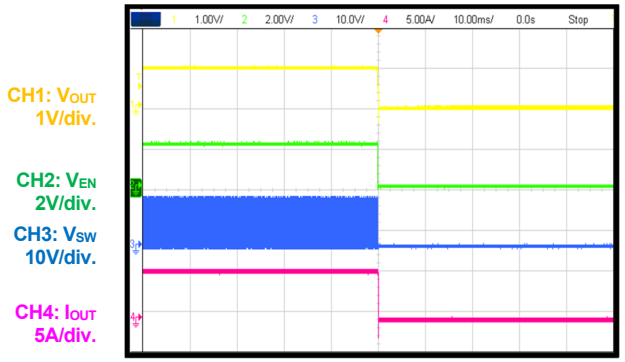
Shutdown through EN

I_{OUT} = 0A



Shutdown through EN

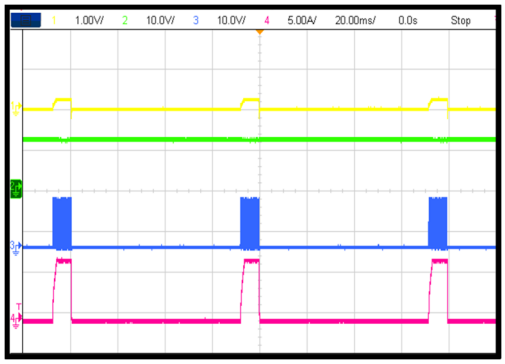
I_{OUT} = 6A



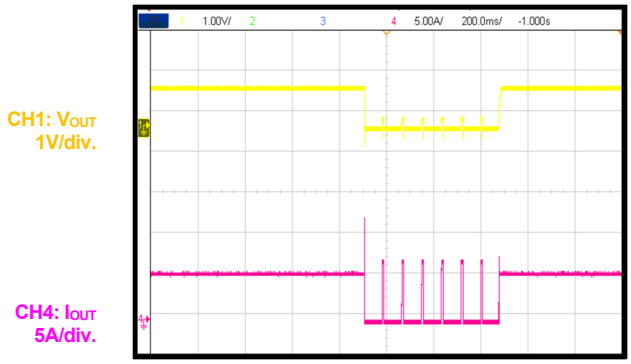
EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board, V_{IN} = 12V, V_{OUT} = 1V, T_A = 25°C, unless otherwise noted.

SCP Steady State



SCP Entry and Recovery



PCB LAYOUT

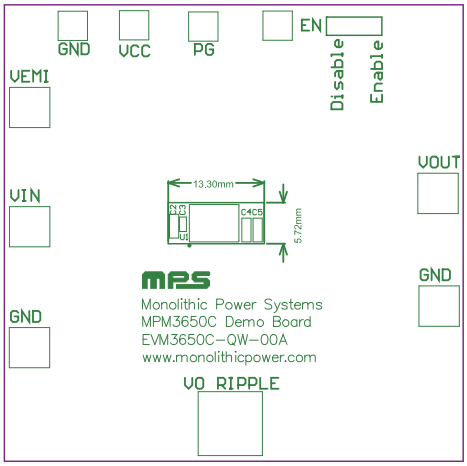


Figure 2: Top Silk

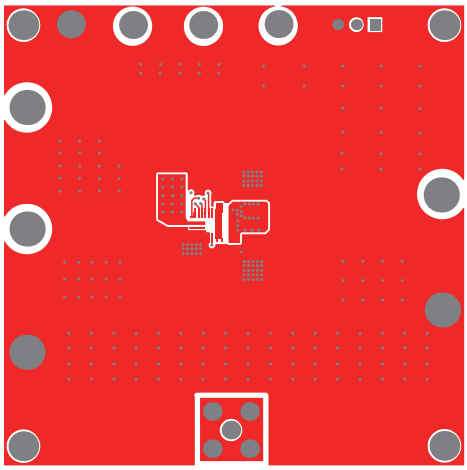


Figure 3: Top Layer

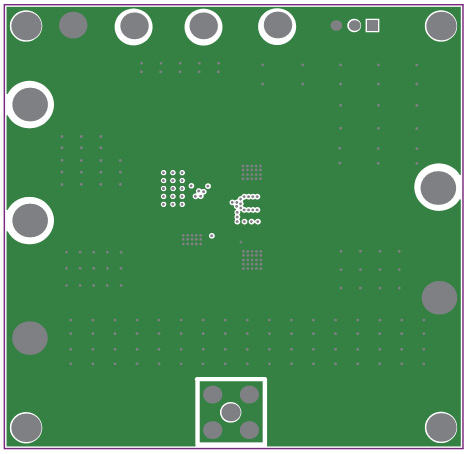


Figure 4: Mid-Layer 1

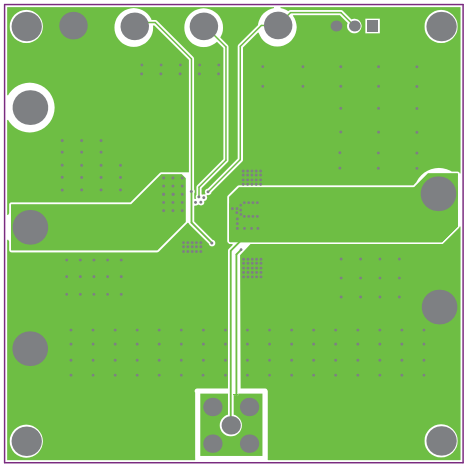


Figure 5: Mid-Layer 2

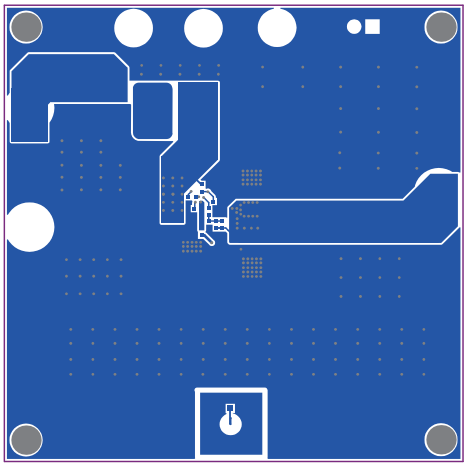


Figure 6: Bottom Layer

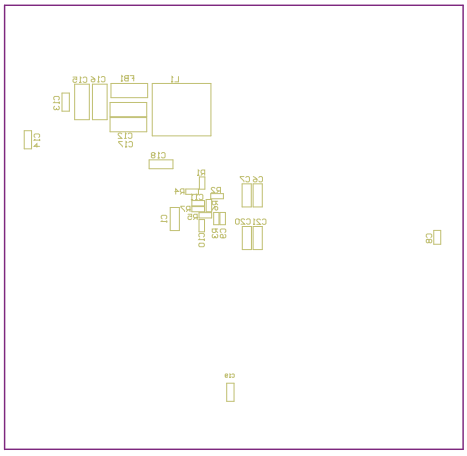


Figure 7: Bottom Silk

REVISION HISTORY

Revision #	Revision Date	Description	Pages Updated
1.0	08/05/2021	Initial Release	-