

5.5" 4-Lane MIPI 720p LCD Display Module With maXTouch® Technology AC40T08A



Preface

The MPU32-MIPI-DISPLAY-720P with maXTouch® technology (AC40T08A) is designed to evaluate the Microchip graphics display solution and graphics library for 32-bit microcontrollers and microprocessors.

This AC40T08A is electrically and mechanically compatible with Microchip MPU Curiosity boards.

The figure below illustrates the AC40T08A maXTouch display module.



1. Introduction

This section describes the features of the Microchip AC40T08A and provides an overview of the 5.5" 4-Lane MIPI 720p LCD Display Module kit.

1.1 Features

The key features of the Microchip AC40T08A kit are:

- Display
 - 5.5-inch display module
 - Himax HX8394-F LCD driver
 - LCD 720 x 1280 resolution
 - 4-lane MIPI serial interface
- Backlight
 - Microchip MIC2289 white LED backlight driver
- Touch
 - Microchip mXT336UD maXTouch capacitive touch screen controller
 - Support for up to 10 touches
 - 1.1 mm soda-lime glass
- Power Supply
 - Microchip MIC23150 buck regulator for 3.3V generation
- Hardware Identification System
 - Programmed Microchip 24AA014H 1-Kbit I²C EEPROM

Kit contents

- One 5.5-inch display module with its control board
- One 34-way flexible flat cable (FFC)

1.2 Kit Overview

The Microchip 5.5" 4-Lane MIPI 720p LCD Display Module kit is an extension board for Microchip MPU Curiosity boards, with 720 x 1280 MIPI LCD resolution and a capacitive touch sensor with a maXTouch controller. The LCD is composed of the following interfaces:

- A four-lane MIPI interface
- An I²C interface for maXTouch controller and EEPROM identification and communications
- A digital backlight brightness control
- Static GPIO for the LCD module control (Module Enable, Display Control and Global Reset)

1.3 Additional Resources

For additional information, refer to the information available via these links:

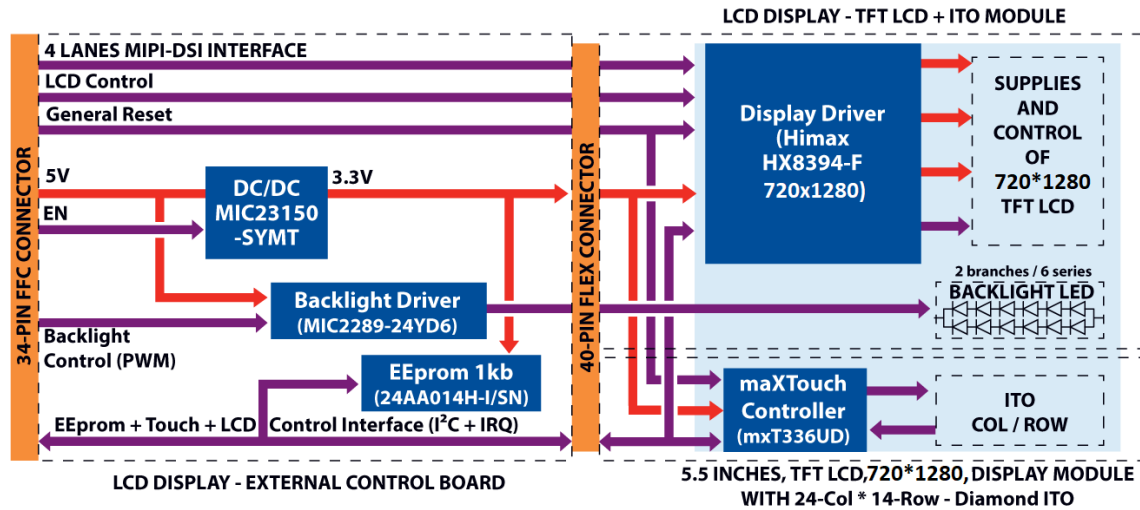
- Himax HX8394-F LCD Driver IC (www.himax.com.tw)
- Microchip mXT336UD maXTouch® Capacitive Touchscreen Controller (www.microchip.com/en-us/product/ATMXT336UD)
- Microchip MIC2289 White LED backlight driver (www.microchip.com/en-us/product/MIC2289)
- Microchip MIC23150 Synchronous Buck Regulator (www.microchip.com/en-us/product/MIC23150)
- Microchip 24AA014H 1 Kbit I²C Serial EEPROM (www.microchip.com/en-us/product/24AA014H)

2. LCD Module Overview

This section covers the specifications of the AC40T08A kit and provides a high-level description of the board's major components and interfaces.

2.1 Block Diagram

Figure 2-1. AC40T08A Block Diagram

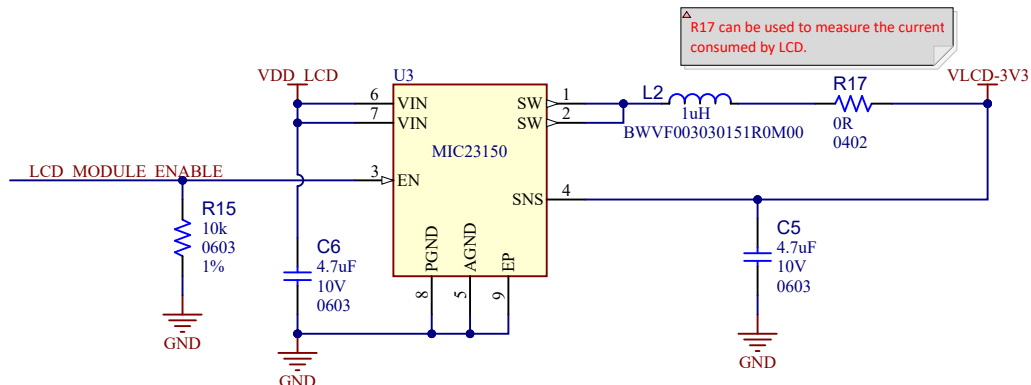


2.2 System Power-On/Power-Off

VDD_LCD is a 5V power input from the external processor board. It must be converted to the 3.3V VLCD_3V3 power rail for the EEPROM memory and the MIPI display.

The MIC23150 is implemented, which is a synchronous Buck regulator with a HyperLight Load® mode. HyperLight Load technology provides high efficiency at light loads and ultra-fast transient response, making it suitable for supplying processor core voltages.

Figure 2-2. System Power-On/Power-Off



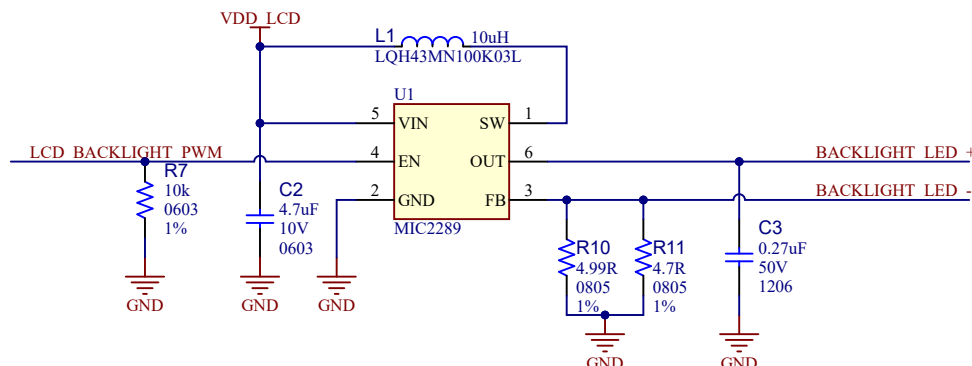
2.3 Backlight Control

The backlight management of the LCD module is ensured by a Microchip MIC2289 PWM boost-switching regulator optimized for constant-current white LED driver applications.

The MIC2289 implements a constant frequency 1.2 MHz PWM control scheme. The added benefit of the constant-frequency PWM scheme, in comparison to the variable-frequency one, is the significantly lower noise and input ripple injected into the input power source.

For more details on the MIC2289 backlight driver, see [Additional Resources](#).

Figure 2-3. MIC2289 Backlight Driver Schematic



By default, after MPU NRST is released, the backlight driver is off and a command on LCD_BACKLIGHT_PWM must be applied to power on the LCD module backlight.

2.4 maXTouch® Capacitive Touchscreen Controller

The module's touch screen interface is based on the Microchip maXTouch mXT336UD Touch Controller and operates on the touch sensor at connector J2. The touch controller scans the touch sensor and signals the host with an active-low interrupt signal (MXT_IRQ on J1) when new touch data is available.

Data communication with the maXTouch controller is performed via the I2C interface (LCD_I2C_DATA and LCD_I2C_CLK on J1). The I2C address of the touch controller is fixed at 0x4A and is not configurable.



Important: The maXTouch has no pull-up resistors featured on the I2C Bus (LCD_I2C_DATA and LCD_I2C_CLK) or its interrupt line (MXT_IRQ). They must be featured on the host board driving the display

Details of the maXTouch communication protocol are beyond the scope of this document. This module is pre-loaded with a configuration already optimized for the maXTouch touch sensor and panel, hence the developer will only focus on interfacing with the device. When developing the maXTouch controller interface during evaluation and host development, care should be taken to avoid changing the maXTouch configuration or committing changes to non-volatile storage on the maXTouch controller. To start with host interface development, users need to leverage the existing code available from Linux which is available at www.microchip.com/linux for more details or from MPLAB® Harmony, which is available at www.microchip.com/mplab/mplab-harmony.

For additional information regarding maXTouch devices, refer to www.microchip.com.

2.5 LCD Module Identification With EEPROM

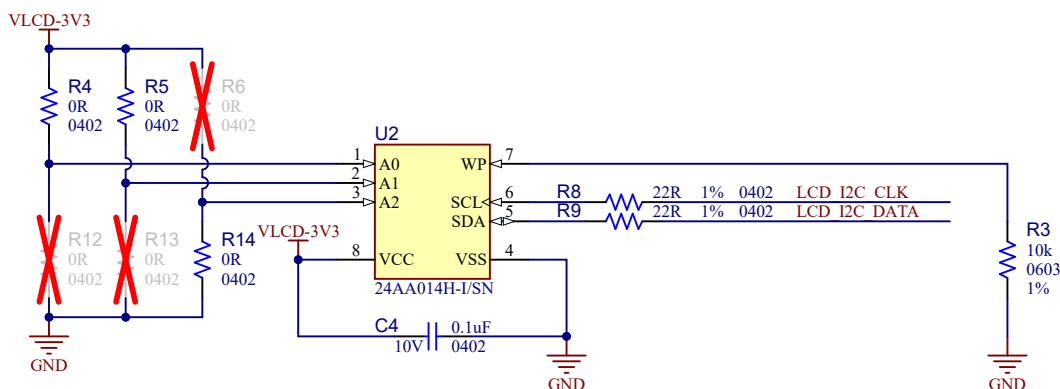
The 5.5" 4-Lane MIPI 720p LCD Display Module kit embeds a 24AA014H 1-Kbit EEPROM used for LCD module identification.

The Microchip 24AA014H is a 1-Kbit serial EEPROM with operation down to 1.7V. The device is organized as a single block of 128 x 8-bit memory with a 2-wire serial interface. Low-current design

permits operation with maximum standby and active currents of only 1 μA and 400 μA , respectively. The device has a page write capability of up to 16 bytes of data.

For more details on the 24AA014H EEPROM memory, refer to [Additional Resources](#).

Figure 2-4. 24AA014H Module Identification Schematic



I²C Address : 0x53



Important: The 24AA014H EEPROM is accessible through the I²C bus at 0x53 address. No other components with the same I²C address can be connected on the same bus.



Important: It is recommended not to erase the data stored in the 24AA014H EEPROM. This data is necessary at the system level for the LCD module identification.

The data contained is a 42-byte word described in the table below.

Table 2-1. EEPROM Memory Mapping

Byte(s) N ^o (1)	Type	Information	Default Value
[0]	Decimal	Number of bytes	0x2A (42 bytes)
[5:2]	ASCII String [4]	Microchip division	0x4D505520 (MPU)
[8:7]	ASCII String [2]	Manufacturer country	0x434E (CN)
[21:10]	ASCII String [12]	Ordering code	0x414334305430384120202020 (AC40T08A)
[32:23]	ASCII String [10]	Main device embeds	0x48583833393420202020 (HX8394)
[34]	Decimal	Manufacturing code (year)	0x18 (2024) ⁽²⁾
[35]	Decimal	Manufacturing code (week)	0x0A (Week 10) ⁽²⁾
[37]	ASCII String [1]	Hardware revision	0x31 (Revision 1)
[39]	ASCII String [1]	Mapping revision	0x41 (Revision A)
[41]	Hexa Data	CRC-8 checksum	N/A

Notes:

1. Unspecified bytes are used as data separators and contain the value 0x2D.
2. These values are examples and may vary, depending on the batch.

3. Connecting AC40T08A to a Microchip MPU Curiosity Board

The AC40T08A can be easily mounted on the MIPI display-capable Microchip MPU Curiosity boards. A connection using a flexible flat cable (FFC), which is contained in the box with the display, is necessary to control the LCD module by software.



Important: In this section, the Curiosity board pictured is just an example. Refer to the respective host board documentation for more information about connection details, cables and accessories.

Four connectors are used between the LCD module and the Microchip MPU Curiosity board for the mechanical connection, thus ensuring stability of the ensemble.

3.1 Microchip MPU Curiosity Board Connector

Microchip MPU Curiosity boards are evaluation platforms that provide a full Microchip embedded microprocessor experience to users. This platform consists of a series of microcontrollers and extension boards, which are supported by Linux and/or by MPLAB® X IDE, MPLAB Harmony drivers, demo code, etc.

The AC40T08A is connected to the MPU host board with an FFC to the J1 connector, the schematic and pinout of which are described below.

Figure 3-1. Microchip MPU Curiosity Board Connector Schematic

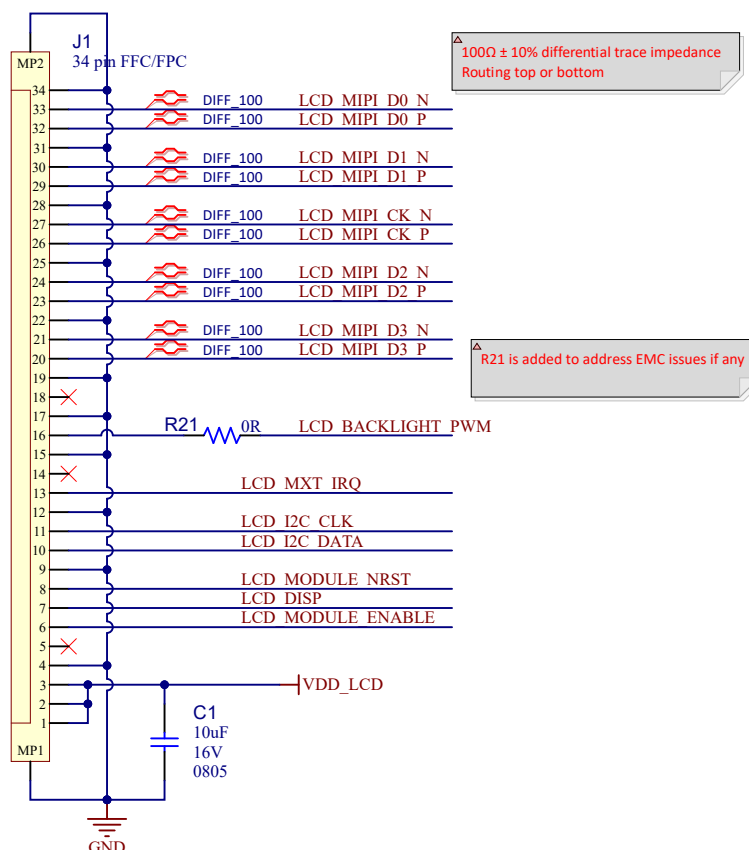


Table 3-1. Microchip MPU Curiosity Board Connector Pinout

Pin Number	Signal Name	I/O Type	Description
33	LCD_MIPI_D0_N	Input	Negative MIPI Differential Data Line 0
32	LCD_MIPI_D0_P	Input	Positive MIPI Differential Data Line 0
30	LCD_MIPI_D1_N	Input	Negative MIPI Differential Data Line 1
29	LCD_MIPI_D1_P	Input	Positive MIPI Differential Data Line 1
27	LCD_MIPI_CLK_N	Input	Negative MIPI Differential Clock Line
26	LCD_MIPI_CLK_P	Input	Positive MIPI Differential Clock Line
24	LCD_MIPI_D2_N	Input	Negative MIPI Differential Data Line 2
23	LCD_MIPI_D2_P	Input	Positive MIPI Differential Data Line 2
21	LCD_MIPI_D3_N	Input	Negative MIPI Differential Data Line 3
20	LCD_MIPI_D3_P	Input	Positive MIPI Differential Data Line 3
16	LCD_BACKLIGHT_PWM	Input	Backlight Driver Control
13	LCD_MXT_IRQ	Output	Touch Screen Controller Interrupt
11	LCD_I2C_CLK	Input	Touch Screen Controller I ² C Clock Line
10	LCD_I2C_DATA	Bidir	Touch Screen Controller I ² C Data Line
8	LCD_MODULE_NRST	Input	LCD Module General Reset Line (LCD Driver, Touch Screen Controller)
7	LCD_DISP	Input	LCD Driver Enable
6	LCD_MODULE_EN	Input	LCD Module Enable
18,14,5	NC	NC	Not Connected
3, 2, 1	VDD_LCD	Power	LCD Module 5V Main Supply
34,31, 28, 25, 22, 19, 17, 15, 12, 9, 4	GND	Power	Ground Supply

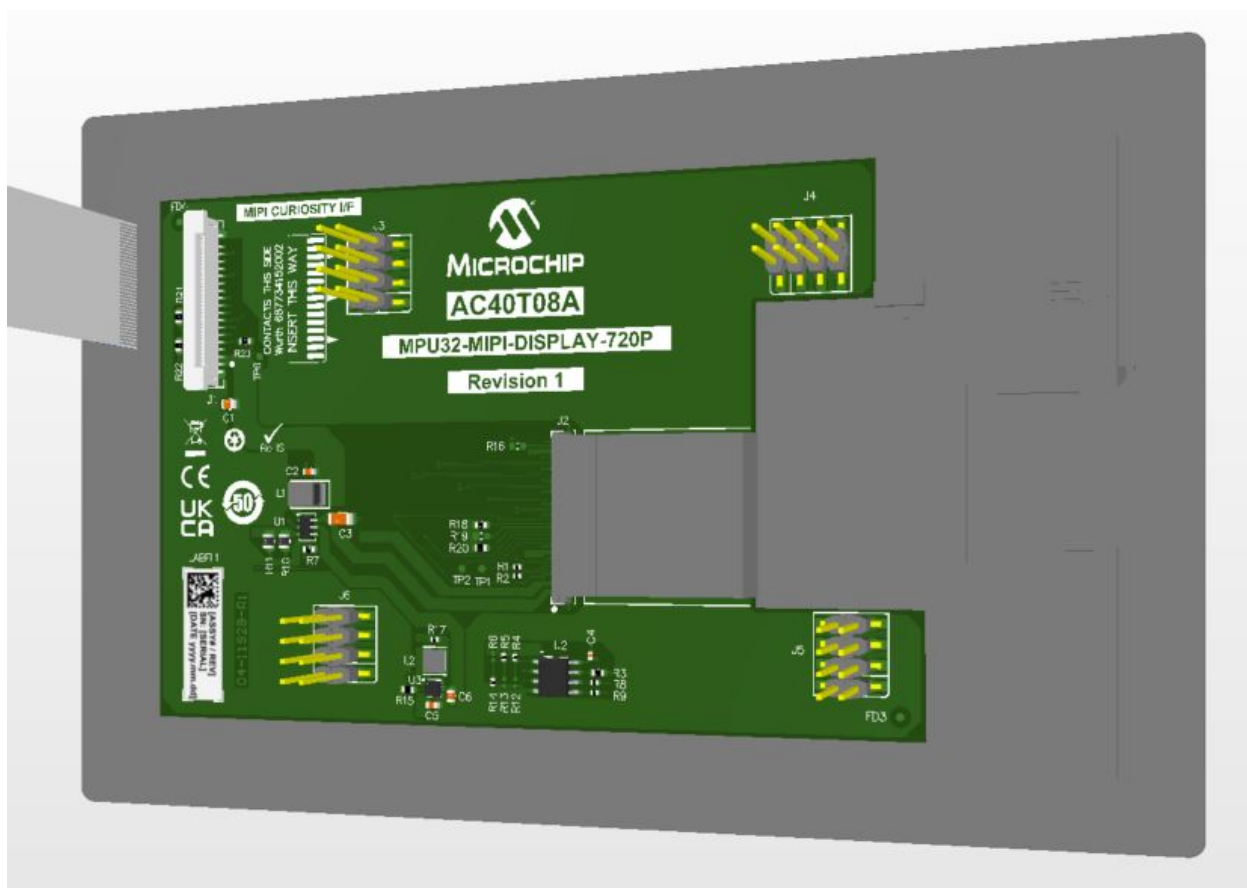
3.2 Connecting the LCD Module to the Curiosity Board

3.2.1 Connecting the Flexible Flat Cable (FFC)

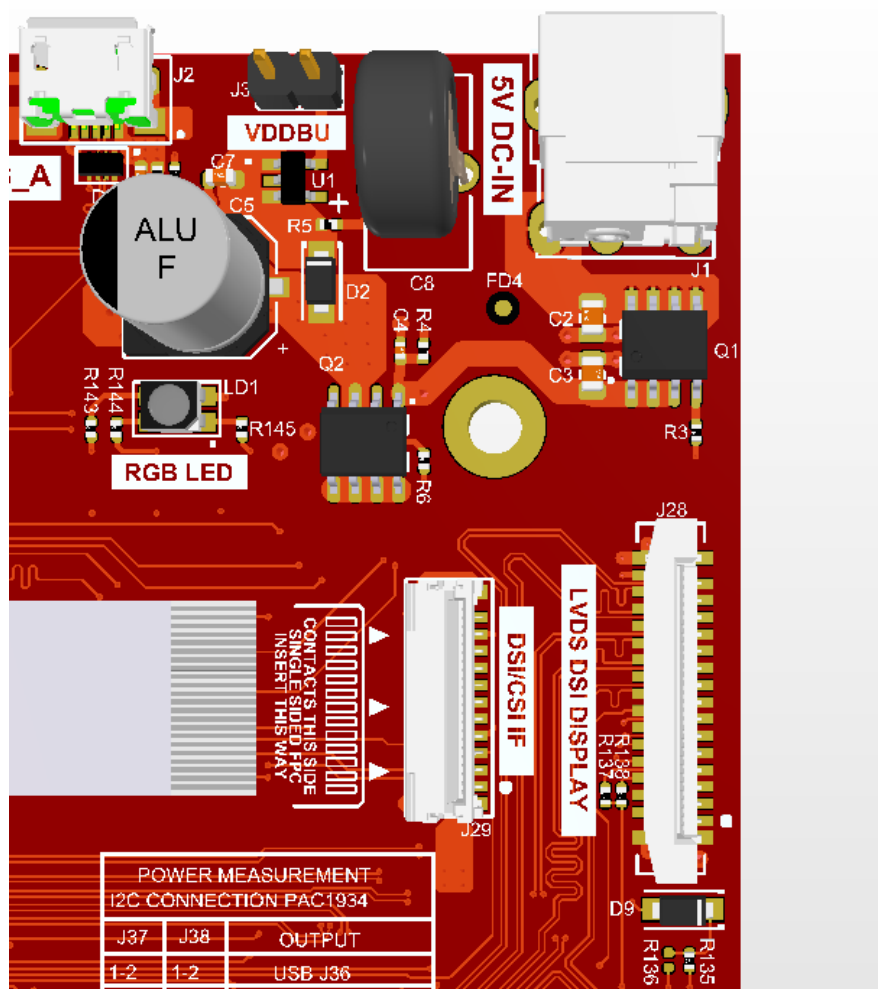
To connect the LCD module to the host board using the flexible flat cable (FFC), follow the sequence below:

- First :
 - Connect the FFC to the LCD module as illustrated in [Figure 3-2](#).
 - Ensure that the metallic fingers of the FFC are facing towards the interior of the board when the FFC is inserted in the header J1, as shown in [Figure 3-2](#).
 - Verify that Pin 1 of the FFC is aligned with Pin 1 of the J1 connector of the AC40T08A board.

Figure 3-2. Connecting the FFC to the LCD Module



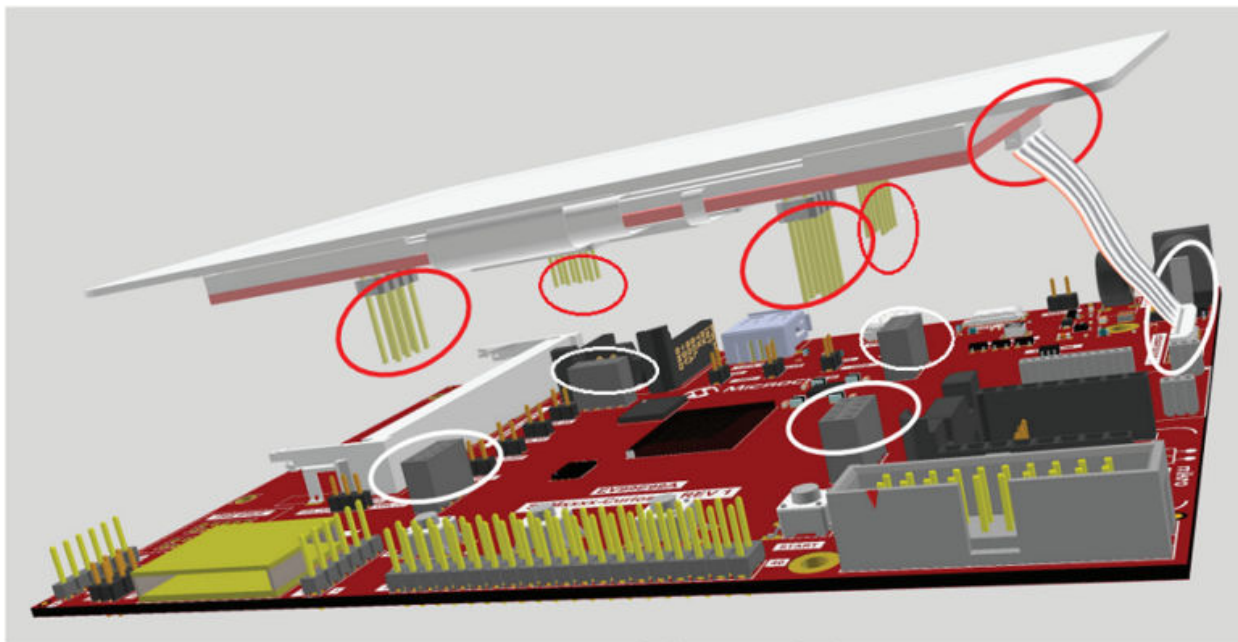
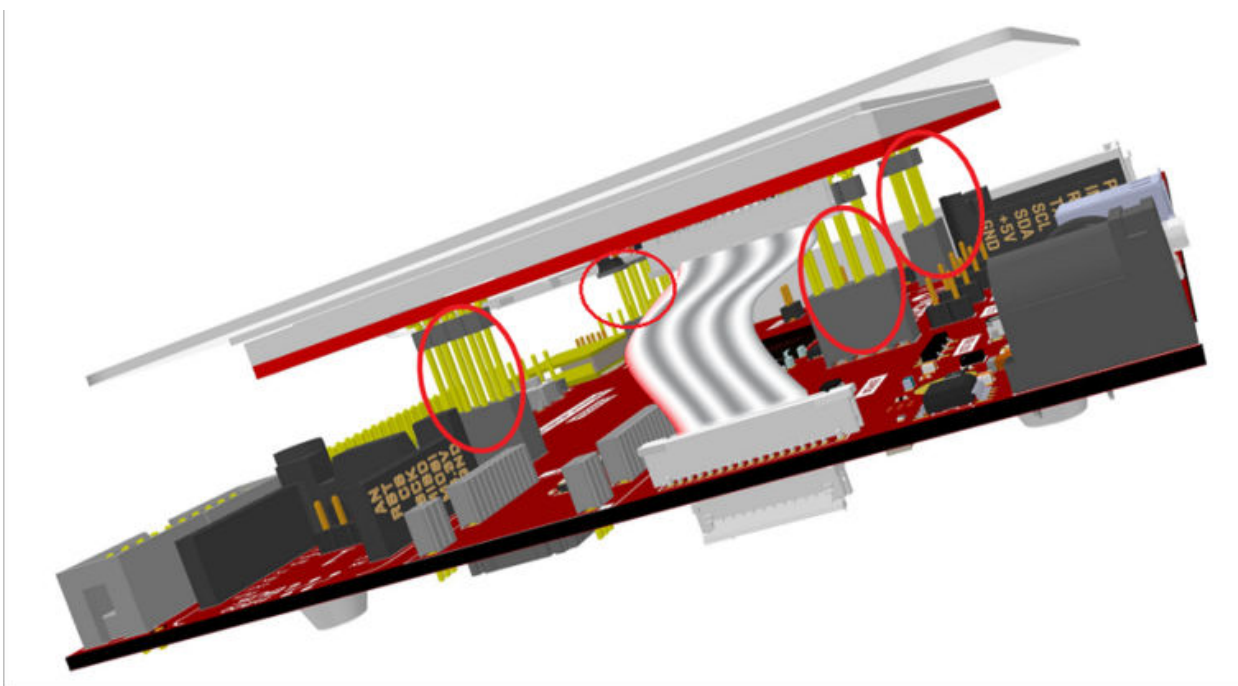
- Second:
 - Connect the FFC to the host board as illustrated in [Figure 3-3](#).
 - Ensure that the FFC metallic fingers are facing towards the outside of the board when it is inserted in the connector, as shown in [Figure 3-3](#).
 - Verify that Pin 1 of the FFC is aligned with Pin 1 of the connector.

Figure 3-3. Connecting the FFC to the Curiosity Board

3.2.2 Mechanical Connection of the LCD Module with the Curiosity Board

Once the FFC connecting the board to the module is in place, follow the steps below to position the AC40T08A correctly on the Curiosity board.

1. Position the LCD module so that the four 2x4-pin headers (circled in red) are above the Curiosity board headers (circled in white) as illustrated in [Figure 3-4](#) below.
2. Be sure that the FFC connecting the two boards is not twisted.
3. Push the LCD module headers into the Curiosity board headers as shown in [Figure 3-5](#).
4. Make sure that all headers are perfectly aligned and mated in the Curiosity board sockets.

Figure 3-4. Positioning the LCD Module**Figure 3-5.** Correct Placement of the LCD Module

4. LCD Module Specifications

Table 4-1. Module Parameters

Parameter	Value
LCD Module size	5.5 inches
Overall dimensions	97 mm (H) 152 mm (W) 20 mm (T)
Overall weight	152g \pm 5g

Table 4-2. Absolute Maximum Specifications

Parameter	Value
Operating temperature	-20°C to +70°C
Storage temperature	-30°C to +80°C
VDD_LCD	-0.3 to +3.6V
Maximum continuous pin current, any control or drive pin	\pm 20 mA
Voltage forced onto any pin	-0.5V to +3.6V



Important: Stresses beyond those listed in the above table may cause permanent damage to the device. This is a stress rating only and the functional operation of the device under these conditions or others beyond those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum specification conditions for extended periods may affect device reliability.

Table 4-3. Recommended Operating Conditions

Parameter	Value
VDD_LCD (LCD Module input voltage)	5V \pm 250 mV
Supply ripple + noise	\pm 100 mV

Table 4-4. DC Specifications

Parameter	Description	Min.	Typ.	Max.	Unit	Conditions
V _{IL}	Low-Input logic level (I ² C, LCD_DISP, NRST)	-0.5	–	0.3 VDDIO	V	3.2V < VDDIO < 3.4V
V _{IH}	High-Input logic level (I ² C, LCD_DISP, NRST)	0.7 VDDIO	–	–	V	3.2V < VDDIO < 3.4V
V _{OL}	Low-Output logic level (MXT_IRQ, I ² C)	–	–	0.2 VDDIO	V	3.2V < VDDIO < 3.4V
V _{OH}	High-Output logic level (MXT_IRQ, I ² C)	0.8 VDDIO	–	–	V	3.2V < VDDIO < 3.4V
V _{IL}	Low-Input logic level (LCD_MODULE_EN)	–	–	0.5	V	4.75V < VDD_LCD < 5.25V
V _{IH}	High-Input logic level (LCD_MODULE_EN)	0.5	0.8	1.2	V	4.75V < VDD_LCD < 5.25V
V _{IL}	Low-Input logic level (LCD_BACKLIGHT_PWM)	–	–	0.4	V	4.75V < VDD_LCD < 5.25V
V _{IH}	High-Input logic level (LCD_BACKLIGHT_PWM)	1.1	–	–	V	4.75V < VDD_LCD < 5.25V

Table 4-5. I2C Compatible Bus Specifications

Parameter	Operation
Touchscreen controller address	0x4A
EEPROM memory address	0x53
Maximum bus speed (SCL)	400 kHz
Bus voltage	3.3V

Table 4-6. LCD Panel Specifications

Parameter	Value
Display size	5.5 inches
LCD type	TFT/Normally Black
Display mode	Transmissive
Resolution	720 x 1280
View direction	FREE
Dimensions	129.11 mm (L) x 70.84 mm (H) x 1.63 mm (T)
Active area	120.96 mm x 68.04 mm
Pixel size	0.0945 mm x 0.0945 mm
Pixel arrangement	Vertical Stripe
Display colors	16.7M

Table 4-7. Backlight Specifications

Parameter	Description	Min.	Typ.	Max.	Units
V_F	Forward voltage ($T_A = 25^\circ\text{C}$, $I_F = \text{Typ}$)	16.8	18.6	20.4	V
I_F	Forward current ($T_A = 25^\circ\text{C}$)	–	40	–	mA
	LED configuration	2 branches of 6x white LEDs in series			
L_V	Luminance	–	300	–	cd/m ²
P_d	Maximum power dissipation	–	–	800	mW
V_{AK}	Backlight driving voltage	4.75	5	5.25	V

5. Schematics

Figure 5-1. AC40T08A Block Diagram

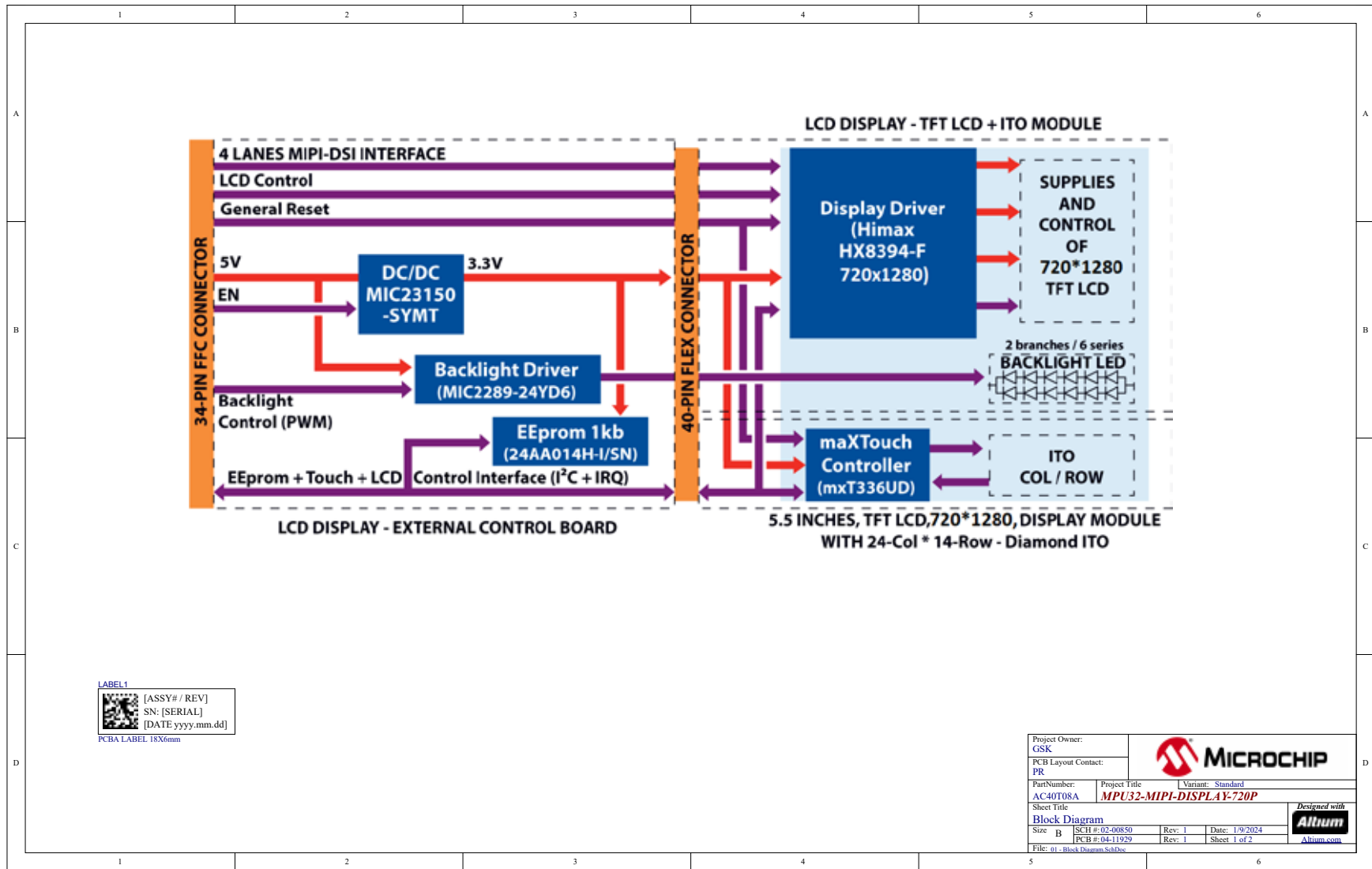
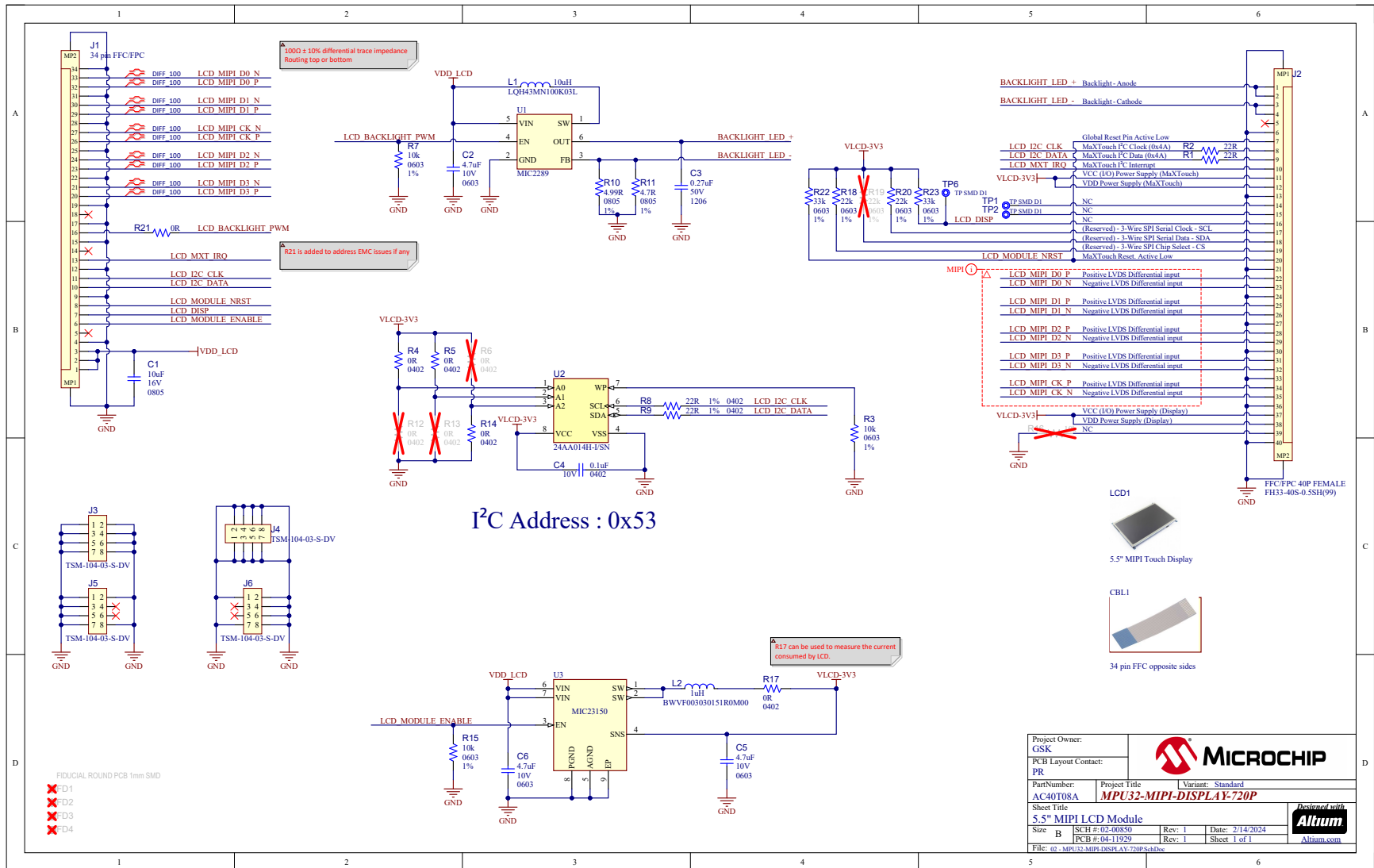


Figure 5-2. AC40T08A Display Schematic

6. Ordering Information

Ordering Code	Board Marking
AC40T08A	AC40T08A MPU32-MIPI-DISPLAY-720P

7. Revision History

7.1 Rev. A - 04/2024

Changes
First issue.

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