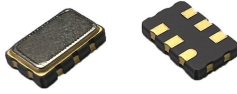




# PLETRONICS HC55J Series 3.3V HCSL Clock Oscillator



HC55JV  
5.0 x 3.2 x 1.3 mm  
LCC Ceramic Package

## Features

- Quartz crystal controlled Precision Square Wave Oscillator
- HCSL Output
- Enable/Disable Function on pad 1
- Low Jitter
- 3.3V Supply Voltage
- 100 - 175 MHz Frequency Range (3rd OT & Fund. Mode)

## Applications

Driving A/Ds, D/As, FPGAs  
Fibre Channel  
Ethernet, GbE, SynchE  
Medical  
Storage Area Networking  
COTS  
Telecom  
PON

## Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Frequency Range <sup>2</sup> (Fo)	100	-	175	MHz	Consult factory for other options
Frequency Stability <sup>2</sup> $\pm 20 = 20^*$ , $\pm 25 = 44$ , $\pm 50 = 45$	$\pm 20$	-	$\pm 50$	ppm	Includes supply voltage change, load change, aging for 1 year at $25^\circ\text{C} \pm 2^\circ\text{C}$ , shock, vibration and temperatures. *limited frequencies, see Page 2
Operating Temperature Range <sup>2</sup>	-10 -20 -40	-	+70 +70 +85	$^\circ\text{C}$	Standard range Extended range <b>C</b> option Extended range <b>E</b> option
Supply Voltage <sup>1,2</sup> (V <sub>CC</sub> )	2.97	3.30	3.63	V	
Supply Current (I <sub>CC</sub> )	-	40	50	mA	
Output Type	HCSL				Load = 50 $\Omega$ to ground
Output High Level (V <sub>OH</sub> )	0.66	-	0.85	V	See load circuit
Output Low Level (V <sub>OL</sub> )	-0.15	-	0.15	V	See load circuit
Output Swing (V <sub>OPP</sub> )	0.4	-	-	V	See load circuit
Output T <sub>RISE</sub> and T <sub>FALL</sub>	-	0.2	0.5	ns	V <sub>th</sub> is 20% and 80% of V <sub>OPP</sub>
Startup Time	-	-	2	ms	Time for output to reach specified frequency
Duty Cycle	45	-	55	%	At output crossing point
V <sub>DISABLE</sub> (V <sub>IL</sub> )	-	-	30	%V <sub>CC</sub>	Referenced to ground
V <sub>ENABLE</sub> (V <sub>IH</sub> )	70	-			
Enable Time	-	-	2	ms	Time for output to reach a logic high state
Disable Time	-	-	200	ns	Time for output to reach a high Z state
Enable/Disable Internal Pullup	30	70	150	K $\Omega$	To V <sub>CC</sub> , Pin 1 open or $\geq 0.7V_{CC}$
Output Leakage V <sub>OUT</sub> = V <sub>CC</sub> V <sub>OUT</sub> = 0V	- -10	-	+10 -	$\mu\text{A}$	Pad 1 low, device disabled
Standby Current	-	-	15	$\mu\text{A}$	
Jitter	-	0.1		ps	12 kHz to 20 MHz offset
Phase Noise 10 Hz 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 10 MHz	-	-74 -100 -126 -142 -146 -152 -156	-	dBc/Hz	25 $^\circ\text{C} \pm 2^\circ\text{C}$ at 100 MHz
Storage Temperature Range	-55	-	+125	$^\circ\text{C}$	

Notes: Specifications with Pad 1 E/D open circuit

<sup>1</sup> Place an appropriate power supply bypass capacitor as close as possible to V<sub>CC</sub> for best performance

<sup>2</sup> Specified by part number



# PLETRONICS HC55J Series 3.3V HCSL Clock Oscillator

## Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V <sub>CC</sub>	Frequency in MHz	Optional T&R Packaging code
HC55	45	J	E	V	- 100.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	V = 3.3V ± 10%	100 - 175 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std for 1K pcs)

\* Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

## Device Marking

PFFF.FH

• YMDx

P = Pletronics  
FFF.F = Frequency in MHz  
H = HCSL Output  
YMD = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3	4	5	6	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2022	2023	2024	2025	2026	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

## Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)  
Font is Courier New  
Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)  
Font is Arial



RoHS Compliant

2nd Lvl Interconnect

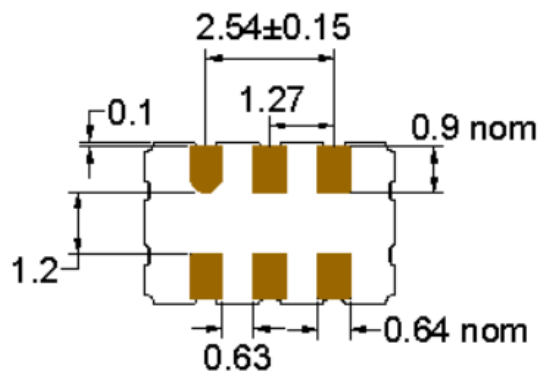
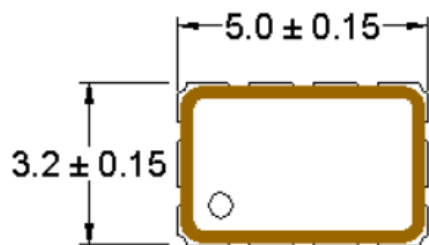
Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's  
Weight of the Device: 0.055 grams  
Moisture Sensitivity Level: 1 As defined in J-STD-020D  
Second Level Interconnect code: e4

## Mechanical Dimensions



## Dimensions in mm

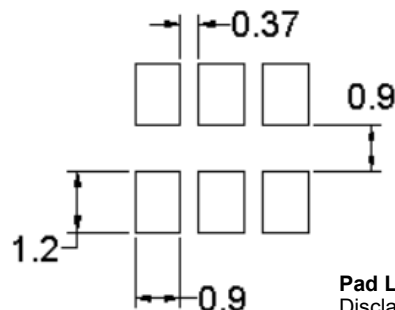
## Pad Connections

Pad	Function
1	Enable/Disable
2	NC
3	Ground
4	Output
5	Output N
6	Vcc

## ENABLE/DISABLE

Pad 1	Outputs
V <sub>IH</sub> /Open	Active
V <sub>IL</sub> /Gnd	Disabled/Tristate

## Solder Pad Layout



### Pad Layout

Disclaimer: Recommended layout shown.  
Adjust layout as needed for individual  
process requirements.

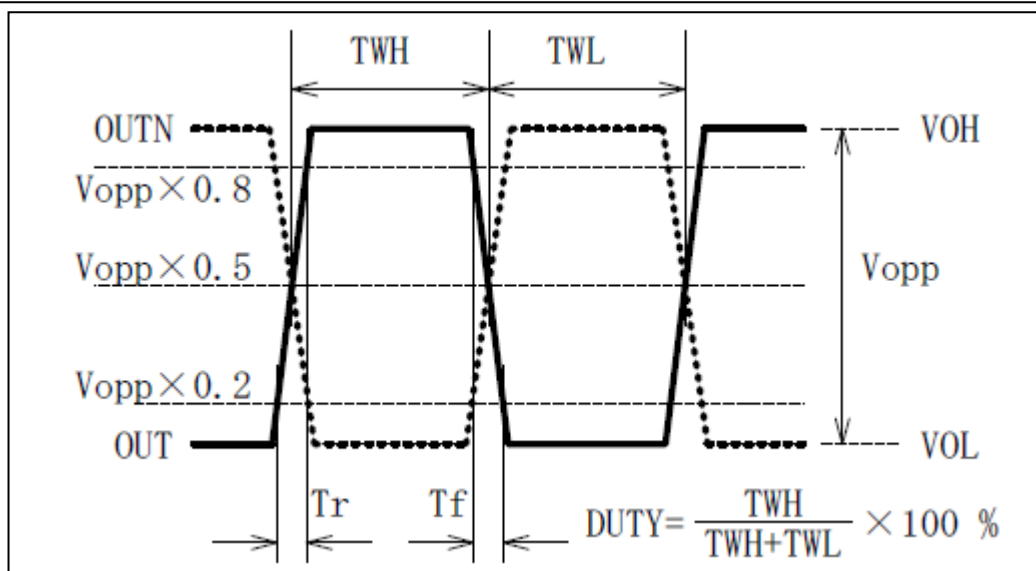
Contacts (pads): Gold (0.3 to 1.0  $\mu$ m) over Nickel (1.27 to 8.89  $\mu$ m)

For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



The diagram shows the timing of three signals: E/D (clock), OUTN (active-low), and OUT (active-low). The E/D signal is a square wave. The OUTN and OUT signals are also square waves, with OUTN being the inverse of OUT. The circuit includes a 0.01µF capacitor on Vcc, 0-33 ohm series resistors, and 50 ohm termination resistors connected to Zo 50 ohm loads.



Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

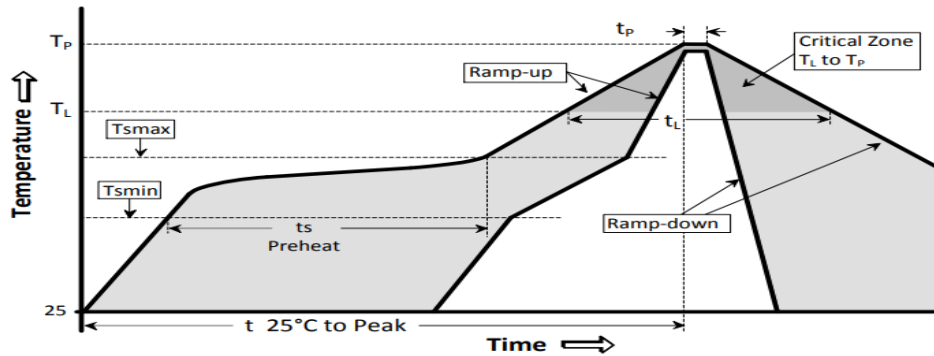
Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Machine Model	200V	JESD22-A115

Parameter	Unit
V <sub>CC</sub> Supply Voltage	-0.3V to +4.0V
V <sub>i</sub> Input Voltage	-0.3V to V <sub>CC</sub> + 0.3V
V <sub>o</sub> Output Voltage	-0.3V to V <sub>CC</sub> + 0.3V

Pg 4

## Reflow Cycle

Maximum Reflow Conditions in accordance with IPC/JEDEC J-STD-020C "Pb-free"

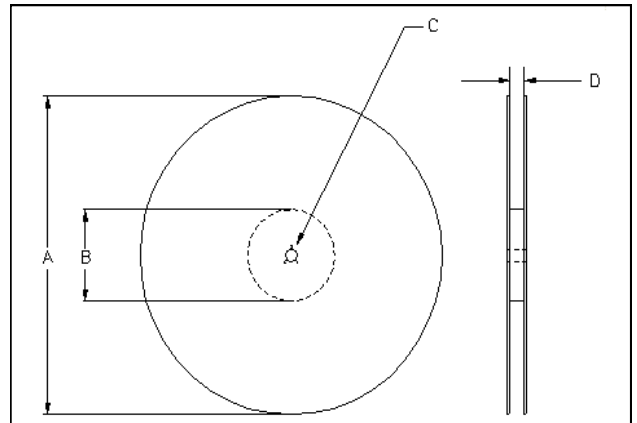
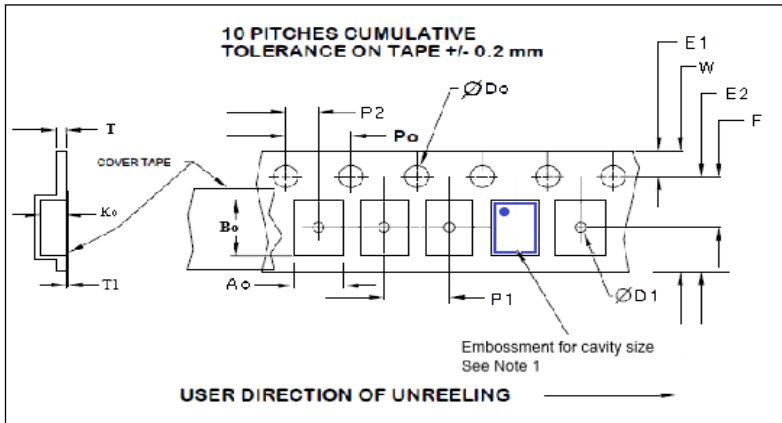


The part may be reflowed 2 times without degradation (typical for lead free processing).

Temperature Profile	Symbol	Condition	Unit
Average ramp-up rate	(T <sub>smax</sub> to T <sub>p</sub> )	3°C / second max	°C / s
Ramp down Rate	T <sub>cool</sub>	6°C / second max	°C / s
Time 25°C to Peak Temperature	T <sub>to-peak</sub>	8 minutes max	min
<b>Preheat</b>			
Temperature min	T <sub>smin</sub>	150	°C
Temperature max	T <sub>smax</sub>	200	°C
Time T <sub>smin</sub> to T <sub>smax</sub>	t <sub>s</sub>	60 – 180	sec
<b>Soldering above liquidus</b>			
Temperature liquidus	T <sub>L</sub>	217	°C
Time above liquidus	t <sub>L</sub>	60 – 150	sec
<b>Peak temperature</b>			
Peak Temperature	T <sub>p</sub>	260	°C
Time within 5°C of peak temperature	t <sub>p</sub>	20 – 40	sec

## Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm (or 16mm) tape, 8mm pitch.



Tape Variable Dimensions Table 2

Tape Size	E2 typ	F	P1	W max	Ao	Bo	Ko
12mm	10.25	5.5 ±0.05	8.0 ±0.1	12.2	3.6±0.1	5.4±0.1	1.4±0.1
16mm	14.25	7.5 ±0.05	8.0 ±0.1	16.3	3.6±0.1	5.4±0.1	1.4±0.1

Dimensions in mm Drawing Not to scale

Note 1: Embossed cavity to conform to EIA- 481-B

Tape Constant Dimensions Table 1

Tape Size	Do	D1 typ	E1	Po	P2	T max	T1 max
12mm	1.5 ±0.1	1.5	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.3	0.1
16mm	-0.0	1.5			2.0 ±0.1		

Reel Dimensions (may vary) Table 3

Reel Size	A	B	C	D
Inches	mm	Inches	mm	mm
7	7.0	180	2.50	60
				13.0 ±0.5 -0.2
				Tape size +0.4 +2.0 -0.0



# **PLETRONICS HC55J Series 3.3V HCSL Clock Oscillator**

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