

VITA 57.4 FMC+ HSPC/HSPCe LOOPBACK CARD

VITA 57.4 COMPLIANT FMC+ PROVIDES FOR FPGA CARRIER CARDS

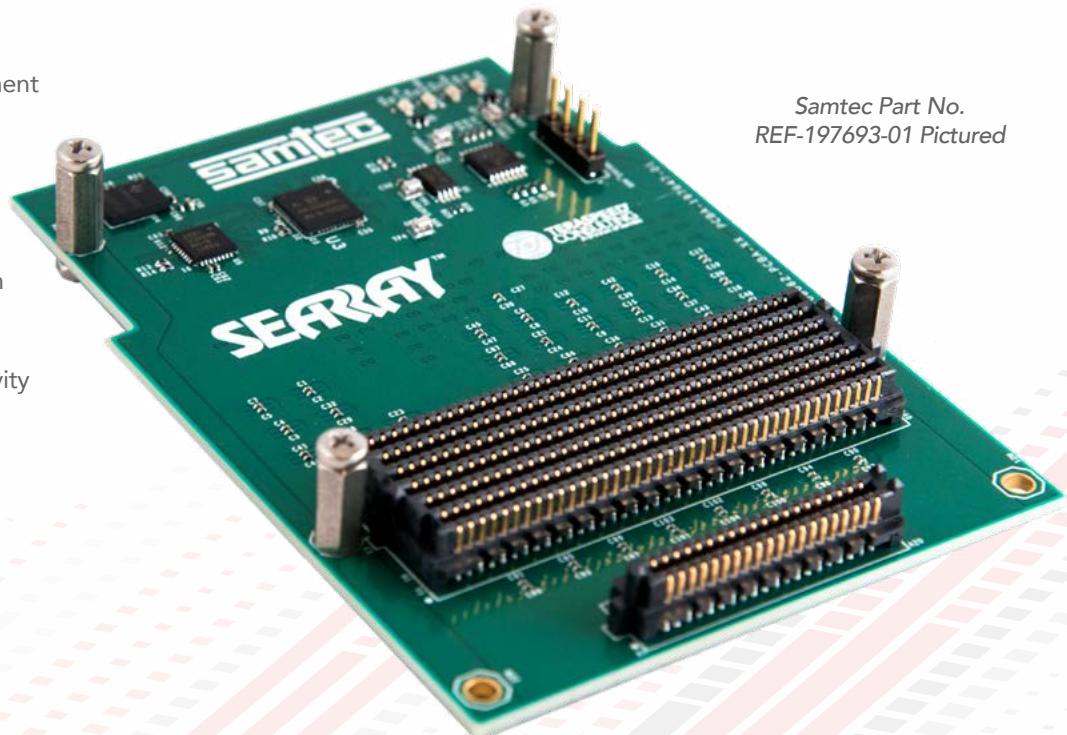
Samtec's VITA 57.4 FMC+ HSPC/HSPCe Loopback Card provides FPGA designers an easy to use loopback option for testing low-speed and high-speed multi-gigabit transceivers on any FPGA development board or FPGA carrier card. It can run system data or BER testing on all channels in parallel. This makes evaluation and development with an FPGA much easier and is an ideal substitute for 28 Gbps test equipment.

FEATURES:

- VITA 57.4 FPGA Mezzanine Card Plus (FMC+) form factor
- High Serial Pin Count (HSPC) VITA 57.4 FMC+ connector (Samtec P/N: ASP-184330-01)
- High Serial Pin Count Extension (HSPCe) VITA 57.4 FMC+ connector (Samtec P/N: ASP-186900-01)
- Features optimized SI performance via Samtec Final Inch® BOR PCB trace routing for HSPC and HSPCe connectors
- Supports up to 32 high-speed multi-gigabit transceivers. TX and RX operating at up to 28 Gbps per channel
- Default Reference Clock Frequency = 156.25 MHz

APPLICATIONS:

- FPGA Development
- FPGA Carrier Card Development
- Datacom
- Broadcast
- Aerospace/Defense
- Industrial and Instrumentation
- High-speed ADCs and DACs
- Next generation RF connectivity
- High-speed serial memory
- High-density fiber optics



Samtec Part No.
REF-197693-01 Pictured

KIT INCLUDES:

- HSPC/HSPCe Loopback Card
- Quick Start Guide

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For additional information or support on your VITA 57.4 FMC+ HSPC/HSPCe design, please visit www.samtec.com/standards/kits-and-boards/hspc-hspce-fmcp.

- VITA 57.4 FPGA Mezzanine Card Plus (FMC+) form factor
- High Serial Pin Count (HSPC) Connector
- High Serial Pin Count Extension (HSPCe) Connector
- FMC I/O voltage: VADJ=1.2 V, 1.5 V, 1.8 V, 2.5 V or 3.3 V (FPGA carrier card dependent)

VITA 57.4 FMC+ HSPC/HSPCe LOOPBACK CARD BLOCK DIAGRAM

