

# Voltage Controlled Oscillator

## 7.06 - 7.90 GHz



MAOC-009261

Rev. V7

### Features

- Low Phase Noise
- Wide Tuning Range
- Divide-by-Two Output
- Integrated Buffer Amplifier
- Excellent Temperature Stability
- +5 V Bias Supply
- Lead-Free 5 mm 32-Lead PQFN Package
- RoHS\* Compliant

### Applications

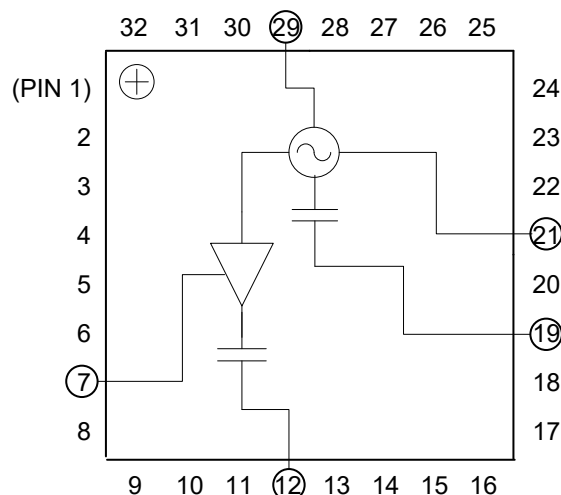
- Point-to-Point Radio
- Point-to-Multipoint Radio
- Communications Systems
- Low Phase Noise applications

### Description

The MAOC-009261 is an InGaP HBT-based voltage controlled oscillator for frequency generation. No external matching components are required. This VCO is easily integrated into a phase lock loop using the divide-by-two output. The extremely low phase noise makes this part ideal for many radio applications including high capacity digital radios.

The 5 mm PQFN package has a lead-free finish that is RoHS compliant and compatible with a 260°C reflow temperature. The package also features low lead inductance and an excellent thermal path.

### Block Diagram



### Pin Designations<sup>2</sup>

Pin #	Function
1 - 6, 8 - 11, 13 - 18, 20, 22 - 28, 30 - 32	N/C
7	V <sub>BUFFER</sub>
12	RF/2
19	RF
21	V <sub>CC</sub>
29	V <sub>TUNE</sub>

2. The exposed pad centered on the package bottom must be connected to RF and DC ground. Connecting all N/C pins to RF/DC Ground in the layout is also recommended.

### Ordering Information<sup>1</sup>

Part Number	Package
MAOC-009261-TR0500	500 piece reel
MAOC-009261-TR1000	1000 piece reel
MAOC-009261-001SMB	Sample Board

1. Reference Application Note M513 for reel size information.

\* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

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**Electrical Specifications:  $T_A = +25^\circ\text{C}$ ,  $V_{CC} = V_{BUFFER} = 5\text{ V}^3$ ,  $Z_0 = 50\ \Omega$**

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Output Power	RF Port, 7.06 - 7.9 GHz RF/2 Port, 3.53 - 3.95 GHz	dBm	8 -2	12 2	—
SSB Phase Noise $V_{CC}=V_{BUFFER}=V_{TUNE}=5\text{V}$	RF Port, 10 KHZ Offset RF Port, 100 KHZ Offset	dBc/Hz	—	-93 -116	—
Harmonics/Subharmonics $V_{CC}=V_{BUFFER}=V_{TUNE}=5\text{V}$	RF Port, $\frac{1}{2} F_0$ RF Port, $2 F_0$	dBc	—	-24 -17	—
Pulling (Sensitivity to Match) $V_{CC}=V_{BUFFER}=V_{TUNE}=5\text{V}$	RF Port, VSWR = 1.95:1 to 2.25:1	MHz pk-pk	—	8.7	—
Pushing (Sensitivity to Supply Voltage)	RF Port, $V_{TUNE} = 5\text{ V}$ RF/2 Port, $V_{TUNE} = 5\text{ V}$	MHz/V	—	2 1	—
Frequency Drift Rate (Sensitivity to Temperature)	RF Port, 7.06 - 7.9 GHz RF/2 Port, 3.53 - 3.95 GHz	MHz/ $^\circ\text{C}$	—	0.6 0.3	—
Output Return Loss	RF Port, 7.06 - 7.9 GHz RF/2 Port, 3.53 - 3.95 GHz	dB	—	5 13	—
Tuning Sensitivity @ RF Port	$V_{TUNE} = 5\text{ V}$	GHz/V	—	0.12	—
Supply Current	$I_{TOTAL} (I_{CC} + I_{BUFFER})$ $I_{CC}$ $I_{BUFFER}$	mA	—	175 155 20	205 175 30
Tune Voltage <sup>4</sup>	$V_{TUNE}$	V	1	—	13
Tuning Current Leakage	$V_{TUNE} = 13\text{ V}$	$\mu\text{A}$	—	5	10

3. VCO can operate over the 4.75 V to 5.25 V supply voltage range.

### Absolute Maximum Ratings<sup>4,5,6</sup>

Parameter	Absolute Maximum
Supply Voltage ( $V_{CC}$ & $V_{BUFFER}$ )	+5.5 Vdc
$V_{TUNE}$	0 to +15 Vdc
Storage Temperature	-55 $^\circ\text{C}$ to +150 $^\circ\text{C}$
Operating Temperature	-40 $^\circ\text{C}$ to +85 $^\circ\text{C}$
Case Temperature ( $T_C$ )	+100 $^\circ\text{C}$ @ exposed pad
Junction Temperature <sup>7</sup>	+135 $^\circ\text{C}$

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- Operating at nominal conditions with  $T_J \leq 135^\circ\text{C}$  will ensure  $\text{MTTF} > 1 \times 10^6$  hours.
- Junction Temperature ( $T_J$ ) =  $T_C + \Theta_{jc} * (V * I)$   
Typical thermal resistance ( $\Theta_{jc}$ ) = 35 $^\circ\text{C/W}$ .  
a) For  $T_C = 25^\circ\text{C}$ ,  $T_J = 56^\circ\text{C}$  @ 5 V, 175 mA  
b) For  $T_C = 85^\circ\text{C}$ ,  $T_J = 117^\circ\text{C}$  @ 5 V, 180 mA

### Handling Procedures

Please observe the following precautions to avoid damage:

### Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1A devices.

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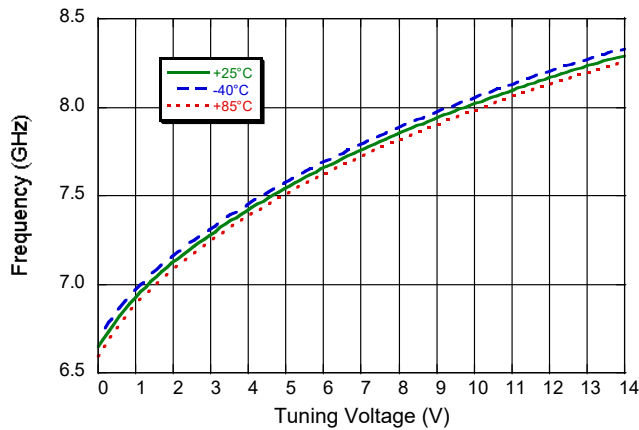


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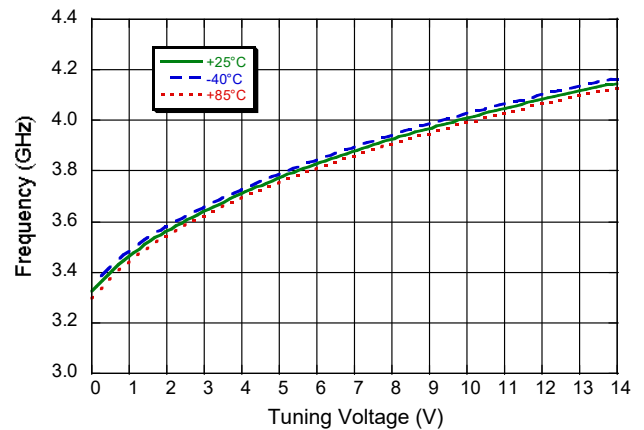
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Typical Performance Curves:  $V_{CC} = V_{BUFFER} = 5\text{ V}$ ,  $T_A = +25^\circ\text{C}$  (unless otherwise indicated)

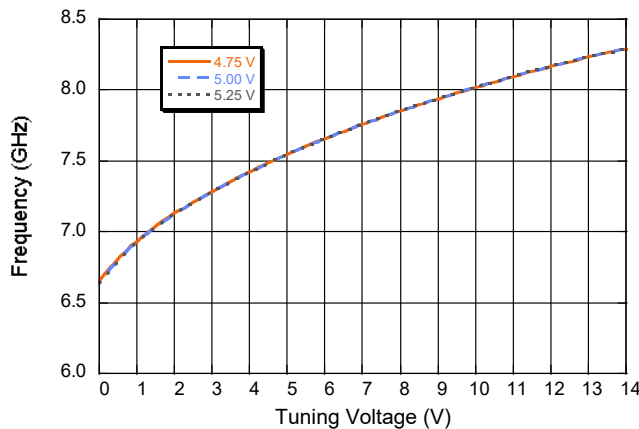
Output Frequency vs. Tuning Voltage - RF Port



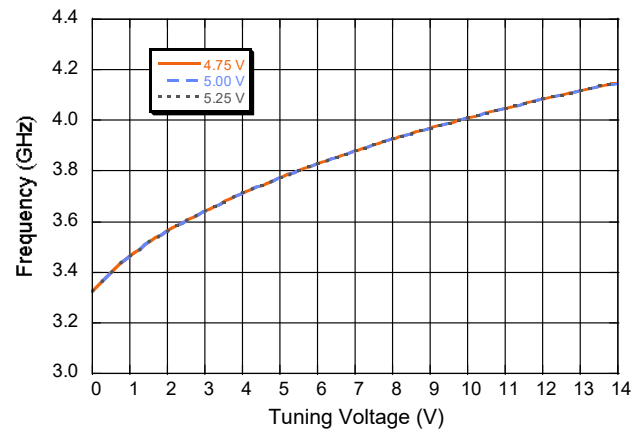
Output Frequency vs. Tuning Voltage - RF/2 Port



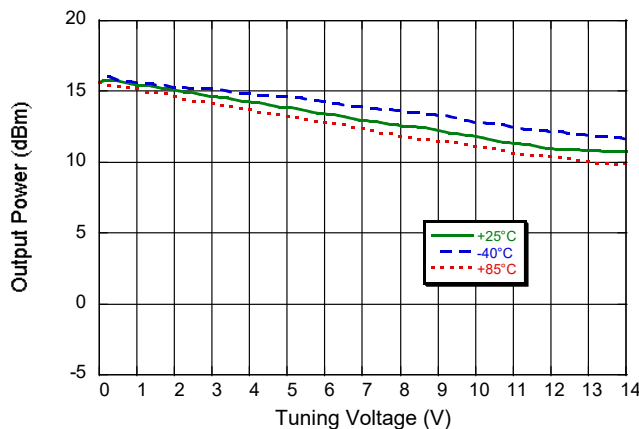
Output Frequency vs. Tuning / Supply Voltage - RF Port



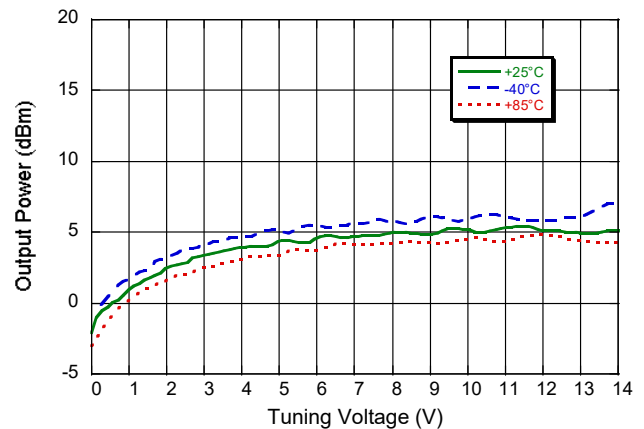
Output Frequency vs. Tuning / Supply Voltage - RF/2 Port



Output Power vs. Tuning Voltage - RF Port



Output Power vs. Tuning Voltage - RF/2 Port



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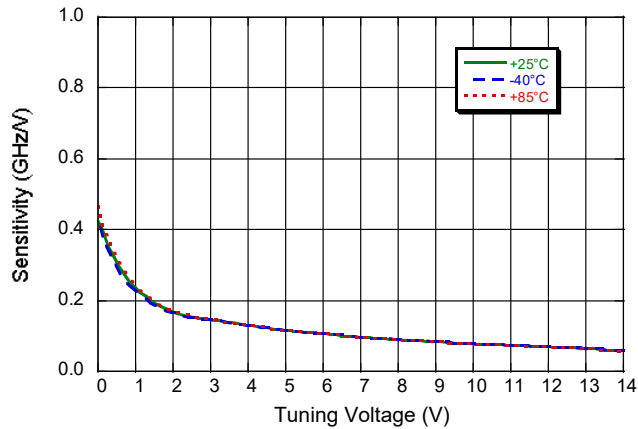
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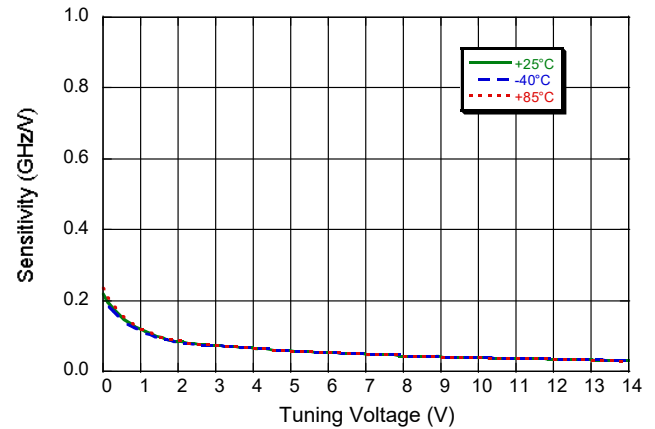
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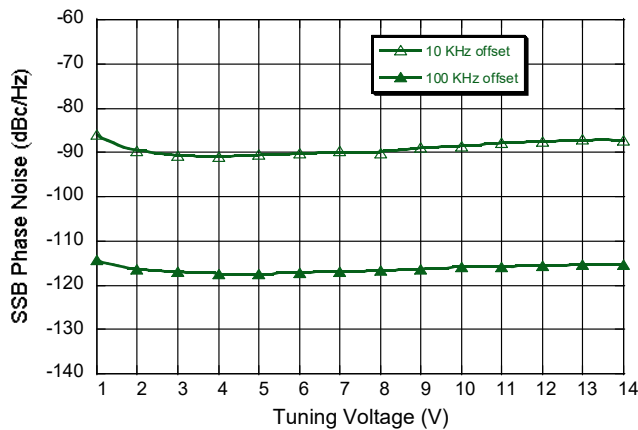
Frequency Sensitivity vs. Tuning Voltage - RF Port



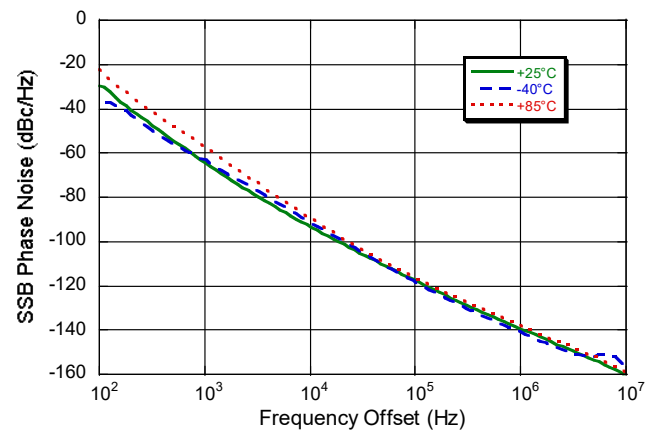
Frequency Sensitivity vs. Tuning Voltage - RF/2 Port



Single Side Band Phase Noise vs. Tuning Voltage  
RF Port



Single Side Band Phase Noise vs. Frequency Offset  
RF Port ( $V_{TUNE} = 5\text{ V}$ )



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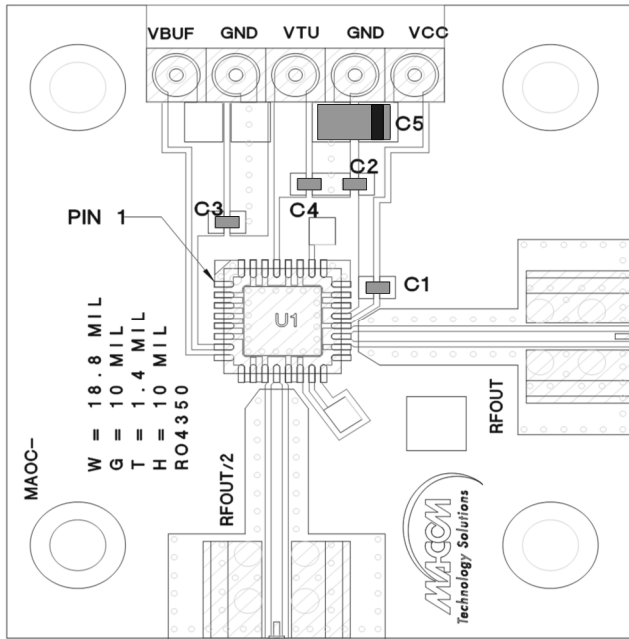
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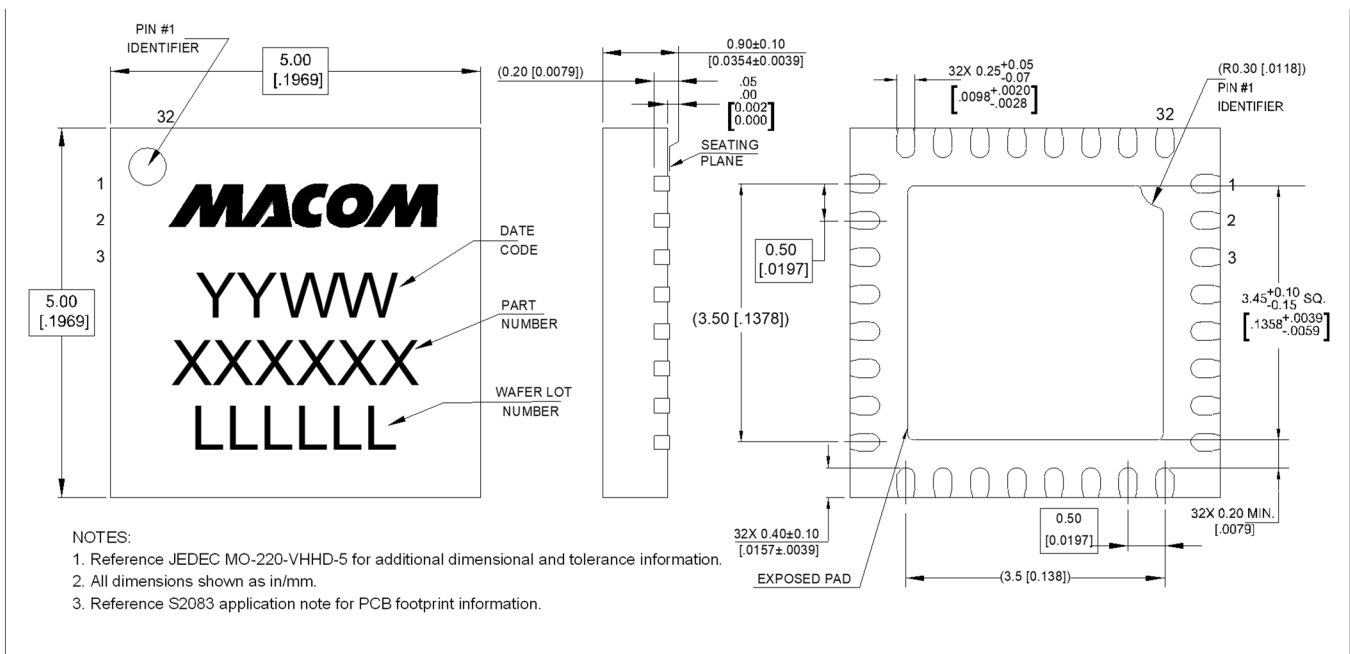
### Sample Board



### Parts List

Component	Value	Case Size
C1	100 pF	0402
C2, C3, C4	0.1 $\mu$ F	0402
C5	10 $\mu$ F Tantalum	1206

### Lead-Free 5 mm 32-Lead PQFN†



† Reference Application Note S2083 for lead-free solder reflow recommendations.  
Meets JEDEC moisture sensitivity level 1 requirements in accordance to JEDEC J-STD-020D.  
Plating is NiPdAuAg over Copper.

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