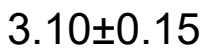
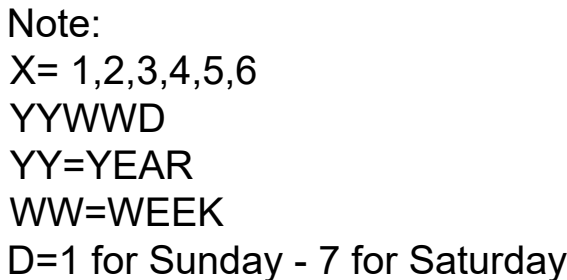
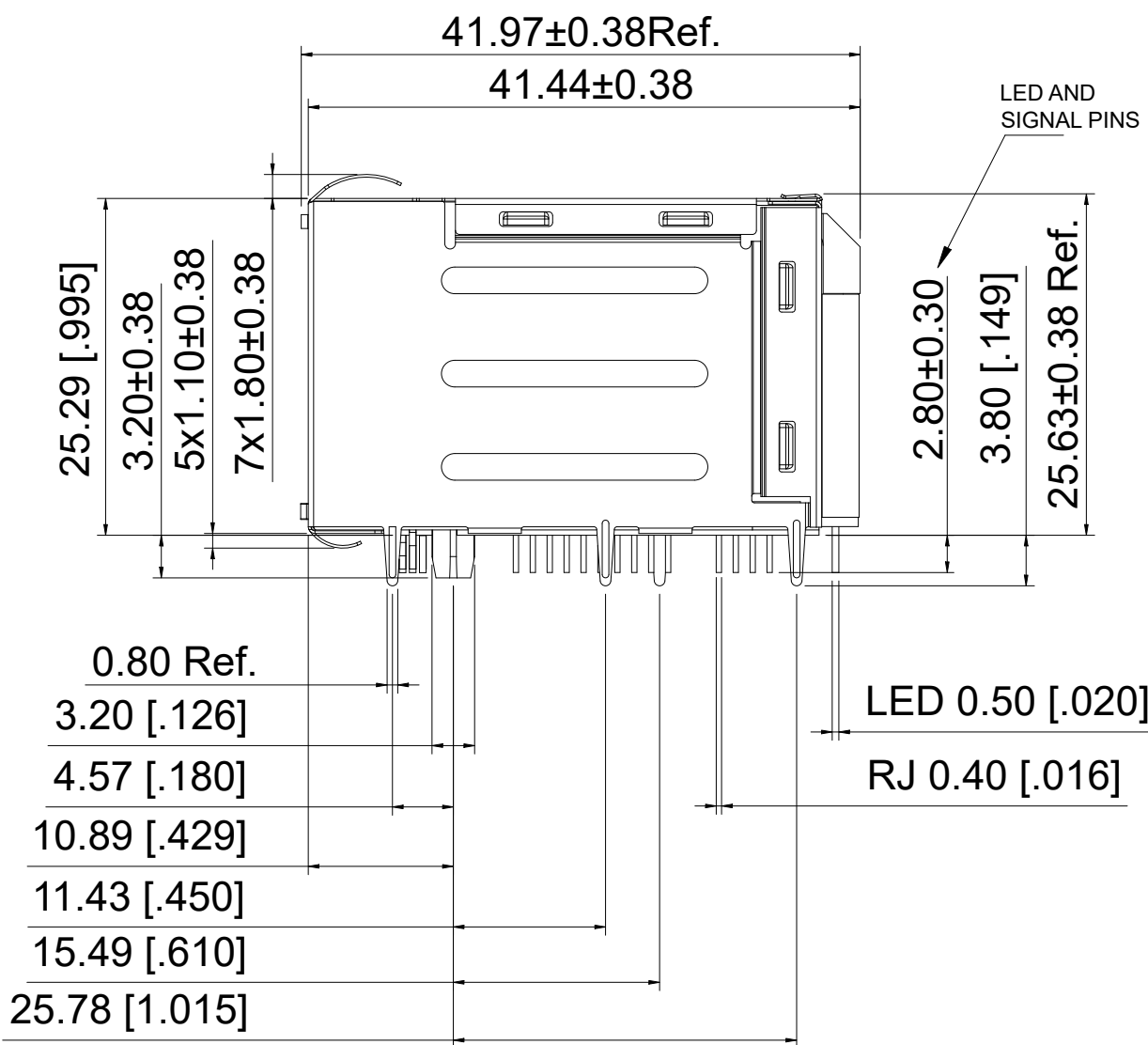


GF	2497271-6
0.0762 μm [3μ"]	2497271-5
0.1524 μm [6μ"]	2497271-4
0.381 μm [15μ"]	2497271-3
0.762 μm [30μ"]	2497271-2
1.27 μm [50μ"]	2497271-1
GOLD PLATING THICKNESS	PART NUMBER



NOTE :

- 1.HOUSING:GLASS FILLED PA6T(BLACK) UL94V-0(RoHS)
- 2.CONTACT: PHOSPHOR BRONZE,50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH (REFER PART NUMBER TABLE) GOLD AT MATING INTERFACE
- 3.SHIELD:0.2mm THICKNESS WITH BRASS.
- 4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- 5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
- 6.OPERATING TEMPERATURE: -40°C TO +85°C.
STORAGE TEMPERATURE:-40°C TO +85°C.



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN 21NOV2024 TARUN, R CHK 22NOV2024 RAMESH, KIVADER APVD 22NOV2024 RAGHAVAN, CG		 TE Connectivity	
DIMENSIONS: mm [INCHES]		TOLERANCES, UNLESS OTHERWISE SPECIFIED:		NAME	
		0 PLC ± - 1 PLC ± 0.35 2 PLC ± 0.25 3 PLC ± 0.15 4 PLC ± - ANGLES ± 2		RJ45 2X6/Through Hole/UPoE+(60W) 2.5GBase-T W/LED -	
MATERIAL		FINISH		SIZE	
-		-		A2	
		WEIGHT		CAGE CODE	
		-		00779	
		CUSTOMER DRAWING		DRAWING NO	
				C-2497271	
				RESTRICTED TO	
				-	
				SCALE	
				1:1	
				SHEET	
				1 of 3	
				REV	
				A	

REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

D

C

B

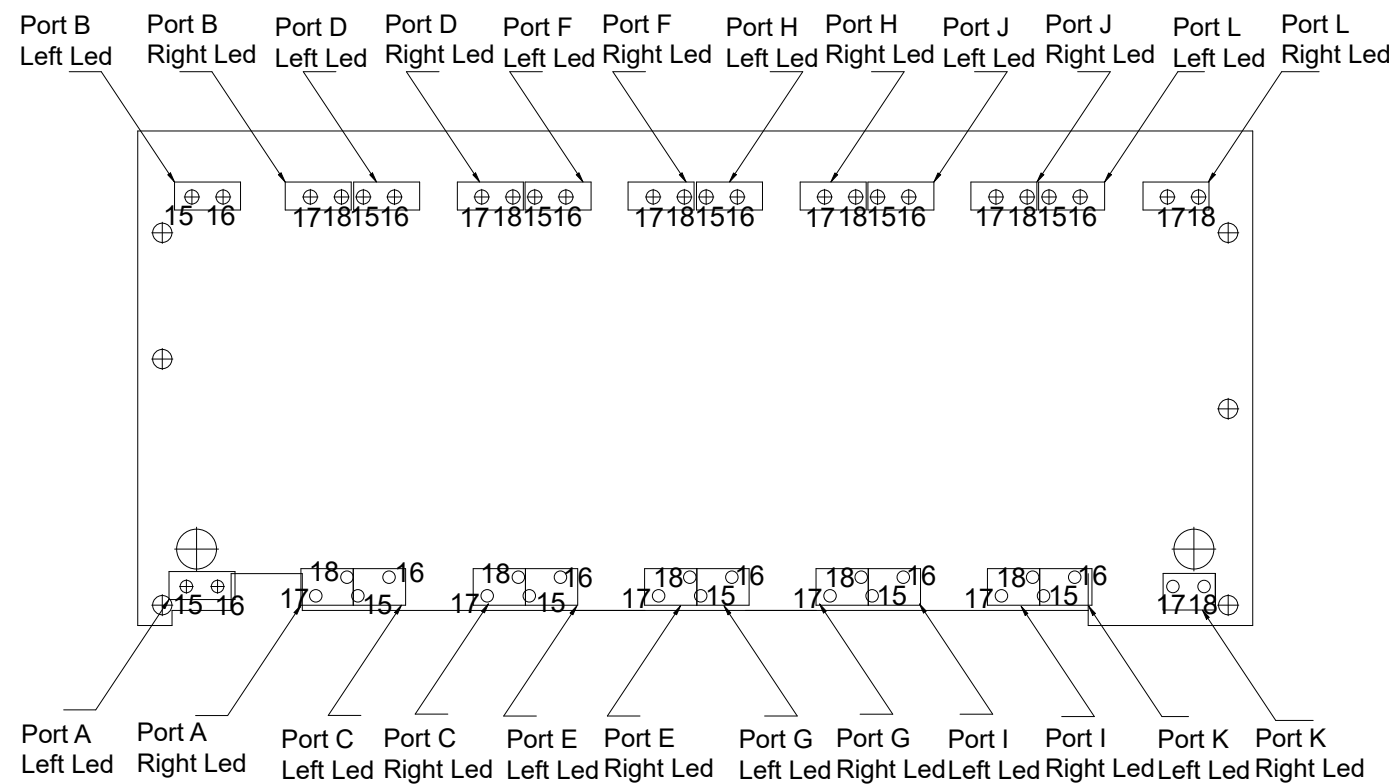
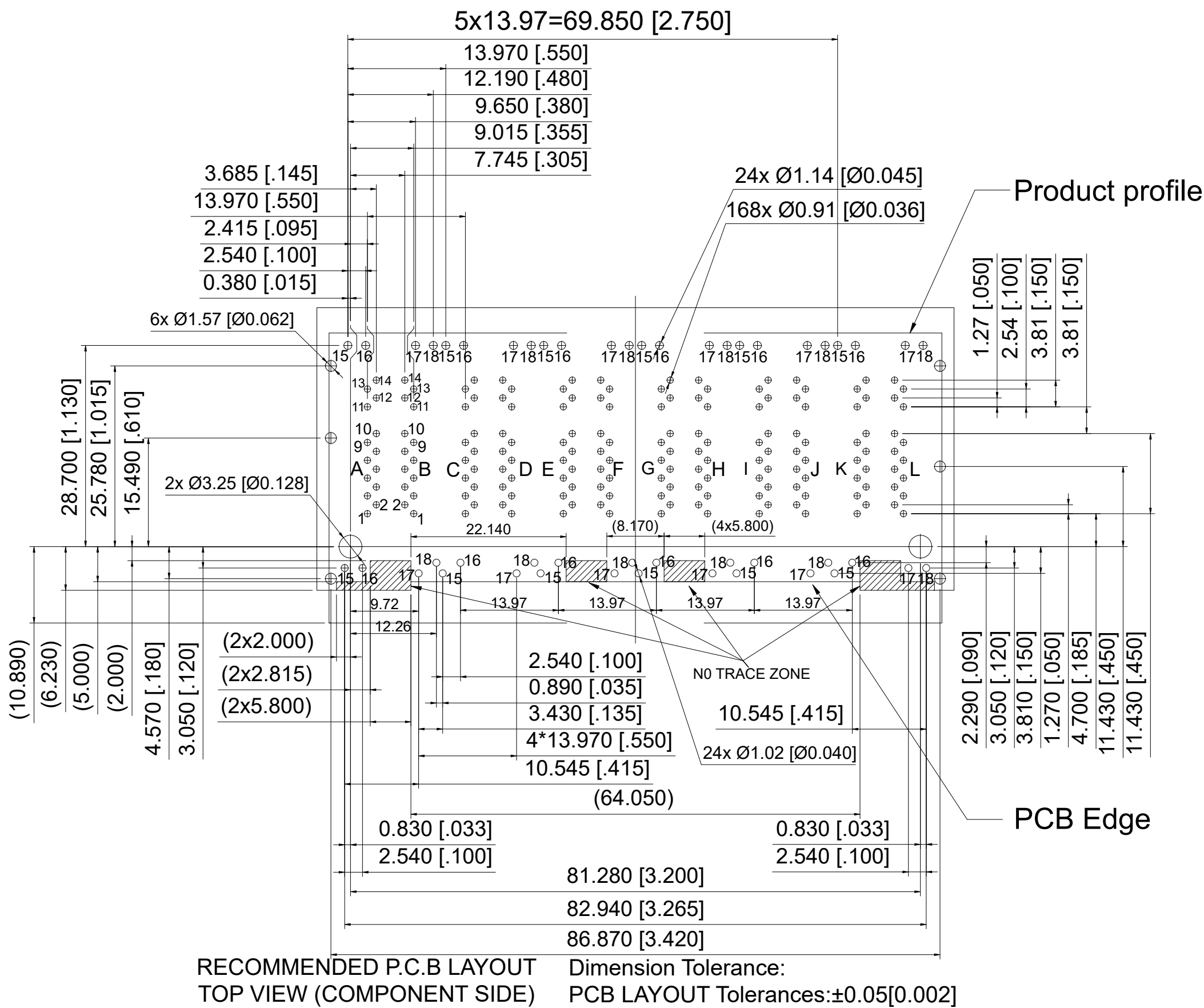
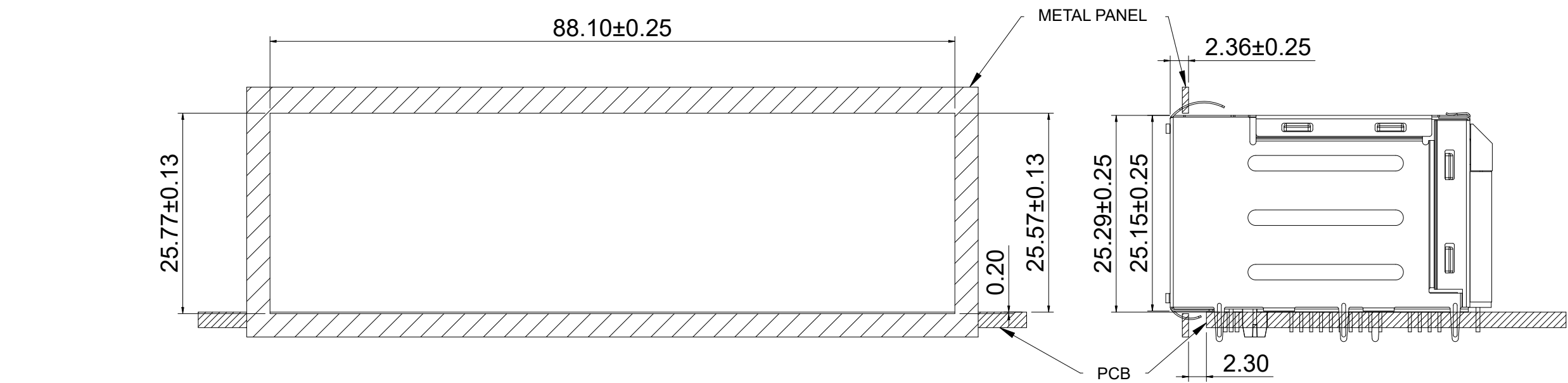
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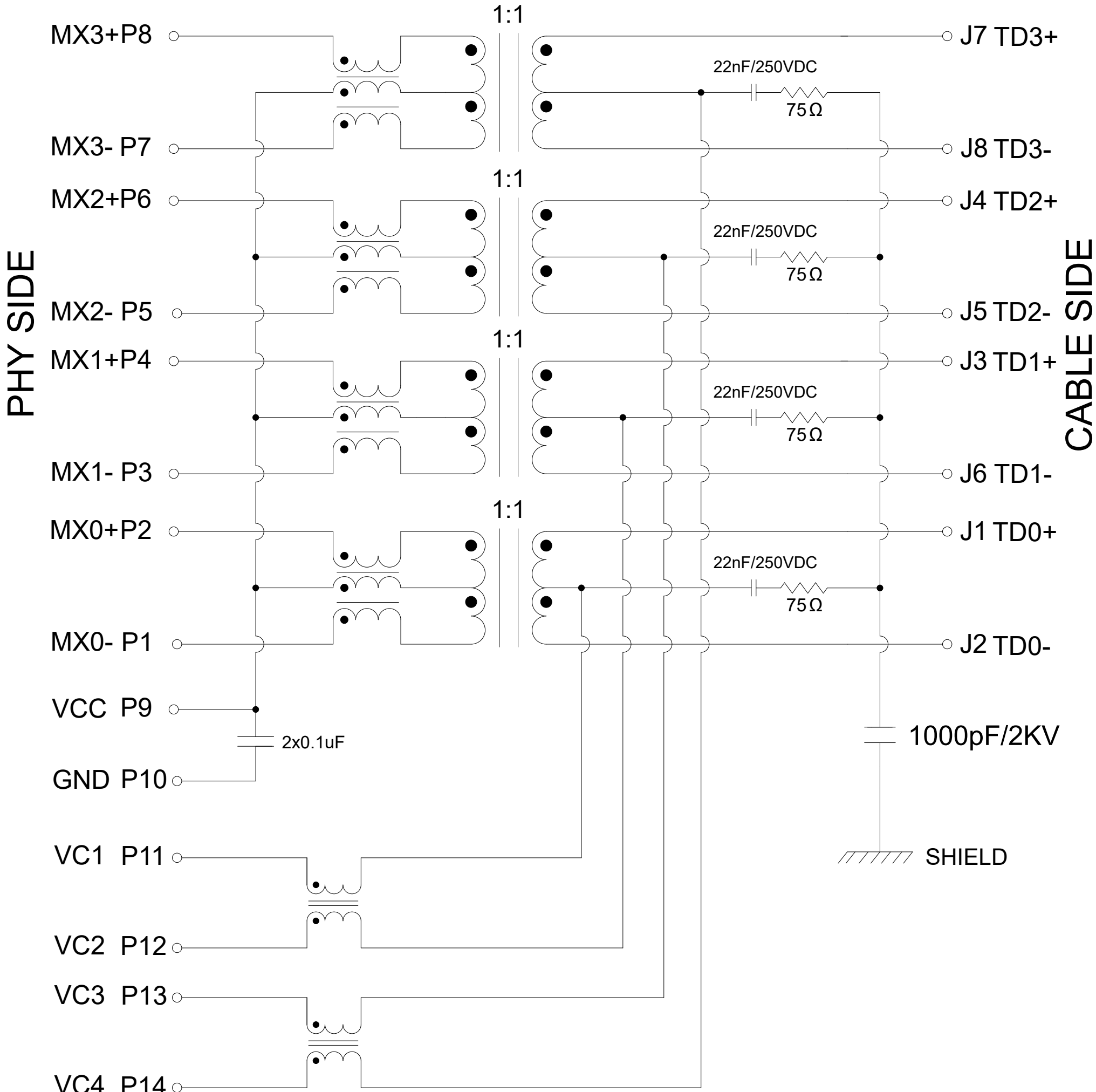
2497271

RECOMMENDED P.C.B LAYOUT TOP VIEW (COMPONENT SIDE) Dimension Tolerance: PCB LAYOUT Tolerances:±0.05[0.002]

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN TARUN, R	21NOV2024	TE Connectivity		
DIMENSIONS: mm [INCHES]		CHK RAMESH, KIVADER	22NOV2024			
		APVD RAGHAVAN, CG	22NOV2024	NAME RJ45 2X6/Through Hole/UPoE+(60W) 2.5GBase-T W/LED		
		PRODUCT SPEC 108-161622		SIZE A2		
MATERIAL -		FINISH -		CAGE CODE 00779		RESTRICTED TO -
		WEIGHT -		DRAWING NO C=2497271		
		CUSTOMER DRAWING		SCALE 1:1	SHEET 2 OF 3	REV A

P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
URNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	120uH MIN@100KHz/100mV With 19mA DC Bias For(CHANNEL1,2,3,4) 180uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL1,2,3,4)
INSERTION LOSS(IL)	-1.0dB MAX@1MHz-100MHz; -1.5dB MAX@100MHz-125MHz;
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-40MHz; -18+15LOG (f/40MHz)dB MIN@40MHz-200MHz;
CROSSTALK (ADJACENT CHANNELS)	-35dB MIN@1MHz-40MHz; -35+15LOG (f/40MHz)dB MIN@40MHz-125MHz;
COMMON MODE REJECTION RATIO(CMRR)	-30dB MIN@1MHz-200MHz;
COMMON TO DIFFERENTIAL MODE REJECTION(REF)	-35dB MIN@1MHz-125MHz;
DC CURRENT/VOLTAGE RATING-PSE PINS	720mA MAXIMUM@57VDC(CONTINUOUS)
HI-POT	2250VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Farward V(max)	TYP
GREEN	570nm	2.6V	2.2V
YELLOW	590nm	2.6V	2.0V

*WITH A FORWARD CURRENT OF 20mA

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN TARUN, R 21NOV2024	TE Connectivity		
DIMENSIONS: mm [INCHES]		CHK RAMESH, KIVADER 22NOV2024			
		APVD RAGHAVAN, CG 22NOV2024	NAME RJ45 2X6/Through Hole/UPoE+(60W) 2.5GBase-T W/LED		
		PRODUCT SPEC 108-161622	SIZE A2		
MATERIAL -		FINISH -	WEIGHT -	CAGE CODE 00779	DRAWING NO C=2497271
CUSTOMER DRAWING			SCALE 1:1	SHEET 3 OF 3	REV A