

LOC		DIST		REVISIONS			
P	LTR	DESCRIPTION		DATE	DWN	APVD	
A		EC 0S12-0341-04		29JUL2004	BW	GG	

- 1

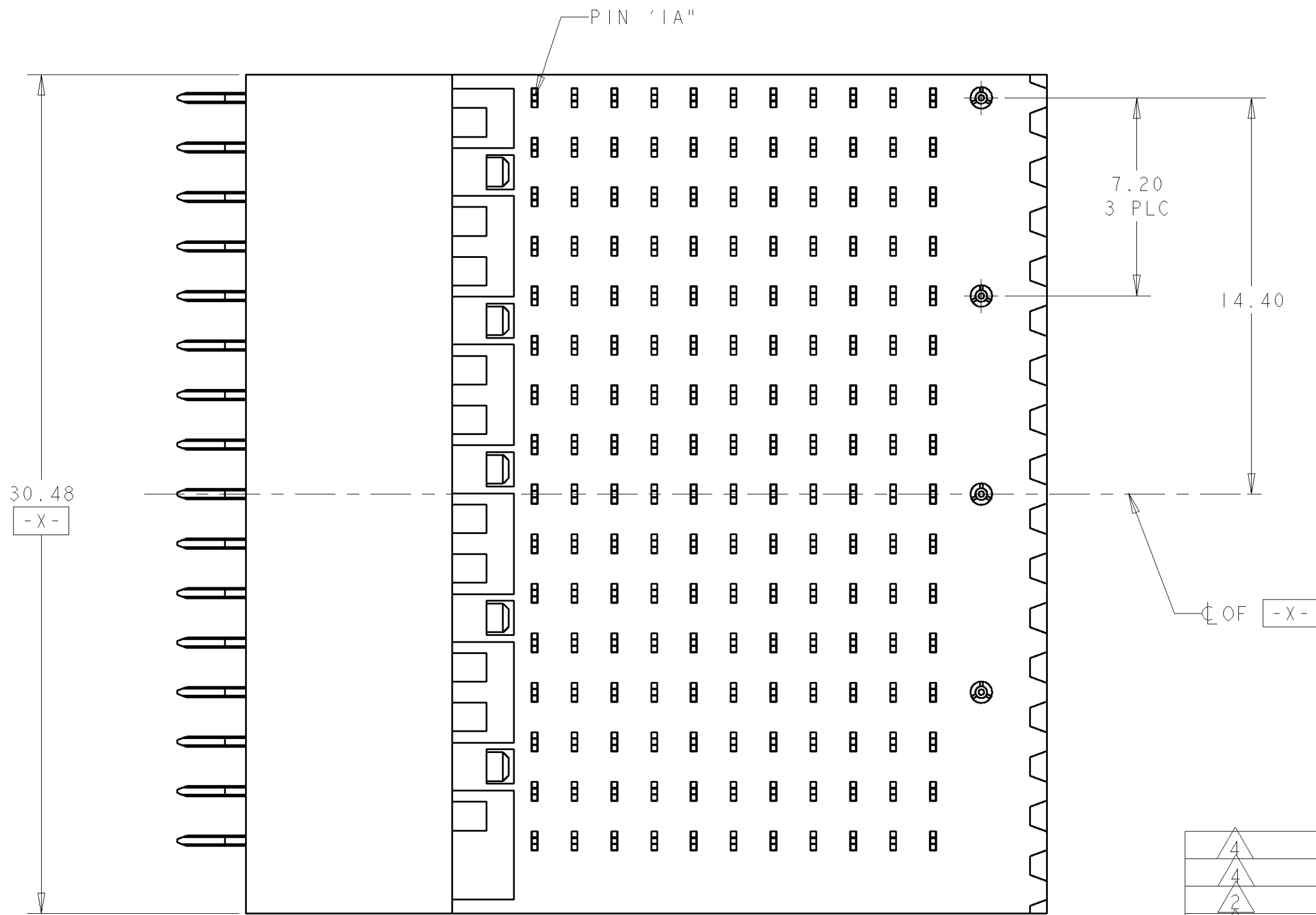
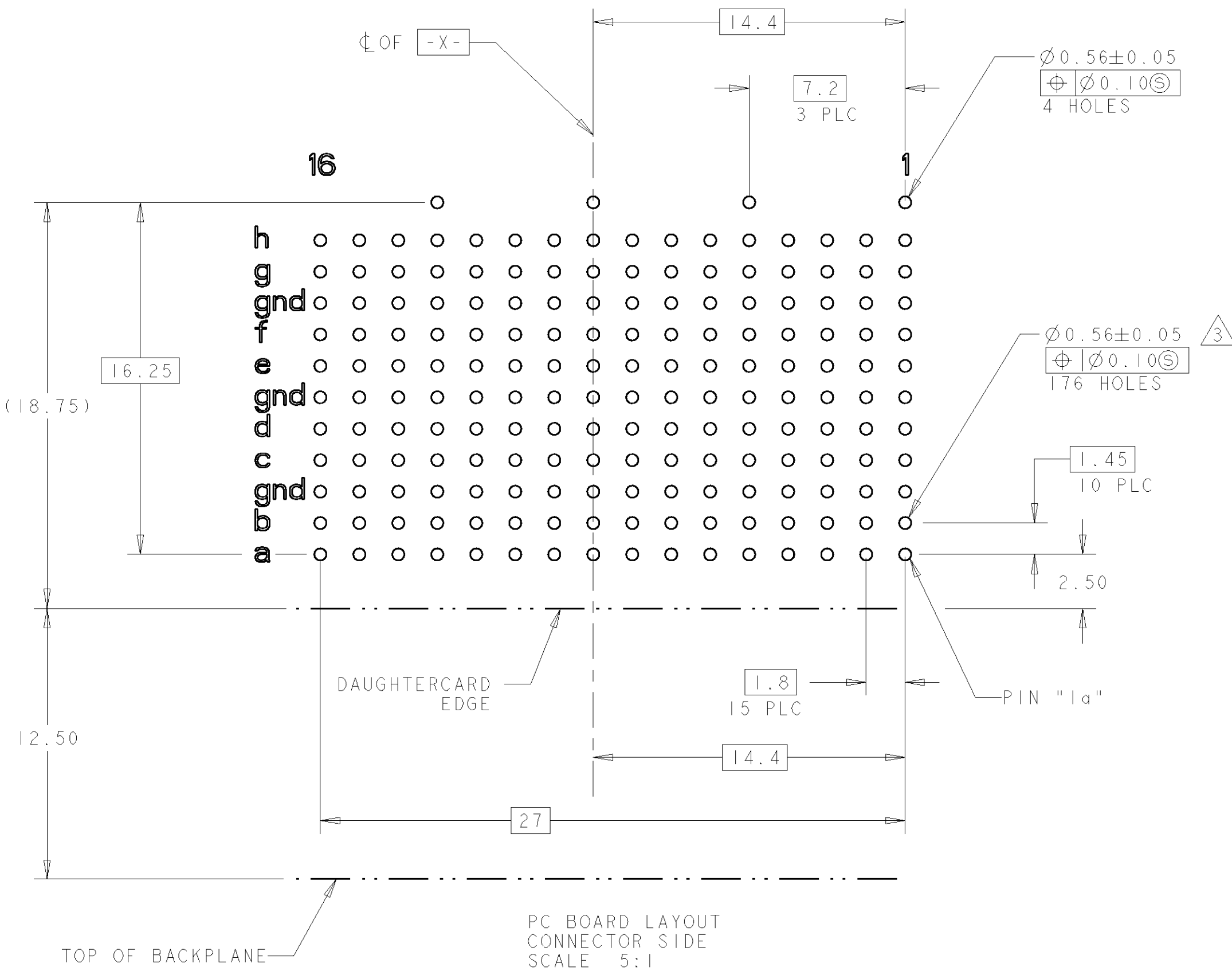
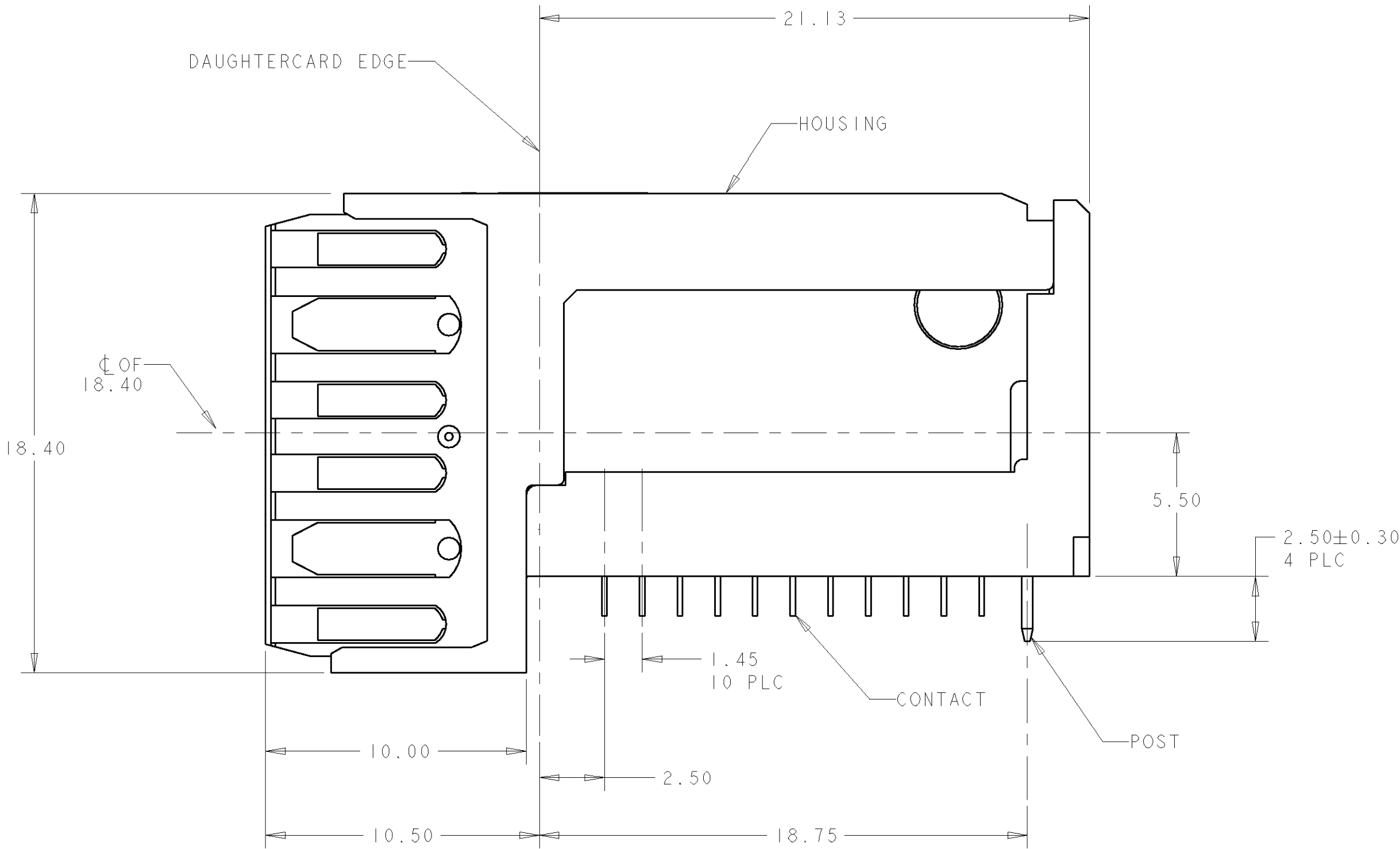
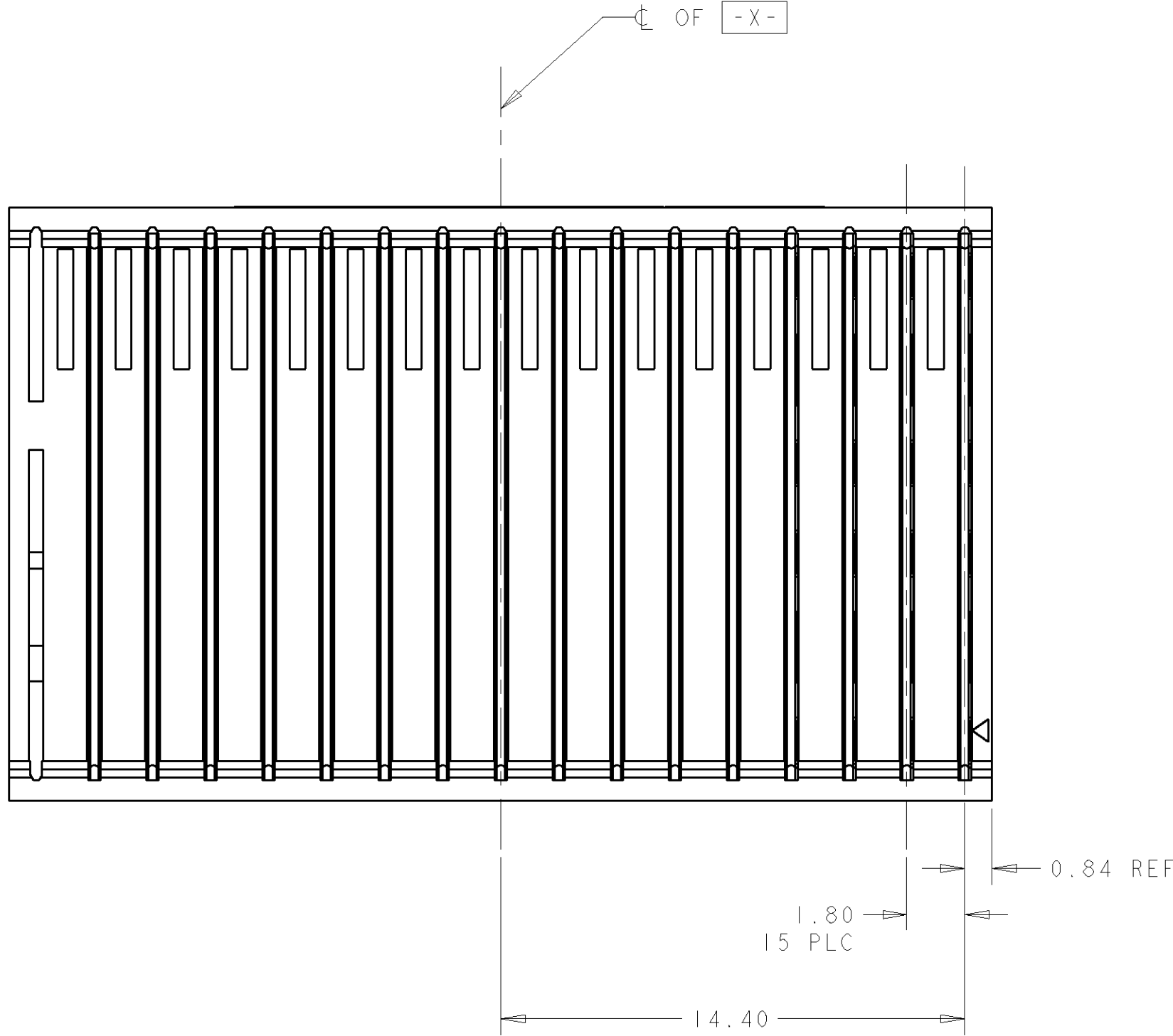
HOUSING: LCP, UL94V-0, BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE.
- 2

CONTACT: 0.76 μ m GOLD PER ASTM B 488 IN CONTACT AREA,
1.27 μ m MIN TIN-LEAD
ON PCB TAIL,
OVER 1.27 μ m MIN NICKEL PER
SAE AMS-QQ-N-290 OVER ALL.
POST: 1.27 μ m MIN NICKEL.
- 3

MANUFACTURING TOLERANCE
FOR A $\varnothing$ 0.56 $\pm$ 0.05 FINISHED HOLE.
DRILLED HOLE: $\varnothing$ 0.65 $\pm$ 0.02
COPPER PLATE: 0.050-0.025
TIN/LEAD PLATE: 0.0124-0.0038
- 4

CONTACT: 0.76 μ m GOLD PER ASTM B 488 IN CONTACT AREA,
1.27 μ m MIN MATTE TIN PER ASTM B 545
ON PCB TAIL,
OVER 1.27 μ m MIN NICKEL PER
SAE AMS-QQ-N-290 OVER ALL.
POST: 1.27 μ m MIN NICKEL.
- 5

MANUFACTURING TOLERANCE FOR A
 $\varnothing$ 0.575 $\pm$ 0.045 FINISHED HOLE WITHOUT
TIN LEAD PLATING:
DRILLED HOLE: $\varnothing$ 0.65 $\pm$ 0.02
COPPER PLATING: 0.025 - 0.050



4	5	DIFFERENTIAL, NONSEQUENTIAL	1410207-4
4	5	SINGLE END, NONSEQUENTIAL	1410207-3
2	3	DIFFERENTIAL, NONSEQUENTIAL	1410207-2
2	3	SINGLE END, NONSEQUENTIAL	1410207-1
CONTACT TAIL/ HOLE FINISH		DESCRIPTION	PART NO

DIMENSIONS:		TOLERANCES UNLESS OTHERWISE SPECIFIED:		DWN R TREA 21FEB2003		NAME RIGHT ANGLE PLUG ASSEMBLY, RIGHT END, FULL, 20.3mm, MultiGig RT 1, DAUGHTERCARD CONNECTOR	
mm		0 PLC $\pm$ 2 PLC $\pm$ 0.13 3 PLC $\pm$ 0.013 4 PLC $\pm$ ANGLES $\pm$ 1°		CHK D.DEFIBAUGH 21FEB2003		Tyco Electronics Harrisburg, PA 17105-3608	
MATERIAL		FINISH		APVD D.DEFIBAUGH 21FEB2003		RESTRICTED TO	
				PRODUCT SPEC 108-2072		SIZE CASE CODE DRAWING NO	
				APPLICATION SPEC 114-13056		A 00779 C=1410207	
				WEIGHT -		SCALE 8:1 SHEET 1 OF 1 REV A	
				CUSTOMER DRAWING			