

AgPd Termination Conductive Glue Mounting Chip Multilayer Ceramic Capacitors for Automotive Powertrain/Safety Equipment**■ Scope**

This specification is applied to Chip Multilayer Ceramic Capacitors limited to AgPd Termination Conductive Glue Mounting.

1. Specific applications:

- Automotive Powertrain/Safety Equipment: Products that can be used for automotive equipment related to running, turning, stopping, safety devices, etc., or equipment whose structure, equipment, and performance are legally required to meet technical standards for safety assurance or environmental protection.
- Automotive Infotainment/Comfort Equipment: Products that can be used for automotive equipment such as car navigation systems and car audio systems that do not directly relate to human life and whose structure, equipment, and performance are not specifically required by law to meet technical standards for safety assurance or environmental protection.
- Medial Equipment [GHTF A/B/C] except for Implant Equipment: Products suitable for use in medical devices designated under the GHTF international classifications as Class A or Class B (the functions of which are not directly involved in protection of human life or property) or in medical devices other than implants designated under the GHTF international classifications as Class C (the malfunctioning of which is considered to pose a comparatively high risk to the human body).

2. Unsuitable Application: Applications listed in "Limitation of applications" in this specification.

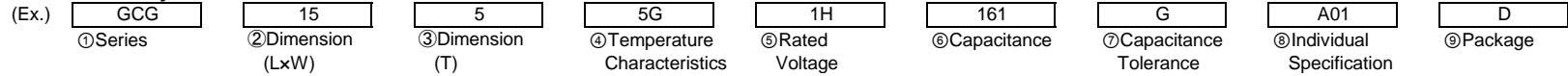
WE DISCLAIM ANY LOSS AND DAMAGES ARISING FROM OR IN CONNECTION WITH THE PRODUCTS INCLUDING BUT NOT LIMITED TO THE CASE SUCH LOSS AND DAMAGES CAUSED BY THE UNEXPECTED ACCIDENT, IN EVENT THAT THE PRODUCT IS APPLIED FOR THE PURPOSE WHICH IS SPECIFIED ABOVE AS THE UNSUITABLE APPLICATION FOR THE PRODUCT.

:Package

Product specifications in this catalog are as of Apr.11,2025, and are subject to change or obsolescence without notice.
Please consult the approval sheet before ordering. Please read rating and !Cautions first.

<Reference> Please kindly use our website.
Please refer to the product information page for more information on ceramic capacitors. [→ Ceramic capacitor product information](#)
Various data can be obtained directly from the product search. [→ Product search \(SMD\)](#)

■ MURATA Part No. System



■ Type & Dimension



image:Dimension

Size Code : 1005M(0402) (in mm)

② L	② W	③ T	e	g
1.0+/-0.1	0.5+/-0.05	0.5+/-0.05	0.15 to 0.35	0.3 min.

■ Rated Value

④Temperature Characteristics [5G] (Public STD Code : [X8G(MURATA)])			⑤Rated Voltage	⑥ Capacitance	⑦ Capacitance Tolerance	Operating Temp. Range	Mounting Method
Temp. coeff. or Cap. Change	Temp. Range	Ref.Temp.					
0+/-30 ppm/°C	25 to 150°C	25°C	DC 50V	160pF	+/-2%	-55 to 150°C	Conductive glue

⑧Individual Specification : This denotes Murata control code.

■ Package

⑨Package	Packaging	Standard Packing Quantity
D	φ180mm Reel PAPER Tape W8P2	10000 pcs./Reel
W	φ180mm Reel PAPER Tape W8P1	20000 pcs./Reel
J	φ330mm Reel PAPER Tape W8P2	50000 pcs./Reel

■ Specifications and Test Methods

No	Item	Specification	Test Method (Compliant Standard:AEC-Q200)
1	AEC-Q200 Rev.	-	Rev.D
2	Pre-and Post-Stress Electrical Test	-	-
3	High Temperature Exposure (Storage)	Appearance Capacitance Change Q or D.F. I.R.(Room Temp.) No defects or abnormalities. Within +/-2.5% Q ≥ 1000 More than 10000MΩ	Mounting method Test Temperature Test Time Post-treatment Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min 150+/-3°C 1000+/-12h Non treatment:Let sit for 24+/-2hours at room temperature, then measure.
4	Temperature Cycling	Appearance Capacitance Change Q or D.F. I.R.(Room Temp.) No defects or abnormalities. Within +/-2.5% Q ≥ 1000 More than 10000MΩ	Mounting method Cycles Temperature Cycling Post-treatment Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min 1000cycles Non treatment:Let sit for 24+/-2hours at room temperature, then measure.
5	Destructive Physical Analysis	Appearance No defects or abnormalities.	Per EIA-469
6	Moisture Resistance	Appearance Capacitance Change Q or D.F. I.R.(Room Temp.) No defects or abnormalities. Within +/-3% Q ≥ 350 More than 10000MΩ	Mounting method Test Temperature Test Humidity Test Time Temperature and Humidity cycle Post-treatment Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min +25°C to +65°C 80%RH to 98%RH Apply the 24h treatment shown below, 10 consecutive times. Non treatment:Let sit for 24+/-2hours at room temperature, then measure.

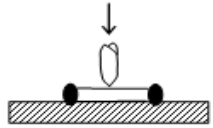
Step	Temp.(°C)	Time(min)
1	Min. Operating Temp.+0/-3	30+/-3
2	Room Temp.	1
3	Max. Operating Temp.+3/-0	30+/-3
4	Room Temp.	1



■ Specifications and Test Methods

No	Item	Specification		Test Method (Compliant Standard:AEC-Q200)	
7	Biased Humidity	Appearance Capacitance Change Q or D.F. I.R.(Room Temp.)	No defects or abnormalities. Within +/-3% Q ≥ 200 More than 1000MΩ	Mounting method Test Temperature Test Humidity Test Time Test Voltage Charge/discharge current Post-treatment	Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min 85+/-3°C 80%RH to 85%RH 1000+/-12h The rated voltage and 1.3+0.2/-0Vdc (add 6.8kΩ resistor) 50mA max. Non treatment:Let sit for 24+/-2hours at room temperature, then measure.
8	Operational Life	Appearance Capacitance Change Q or D.F. I.R.(Room Temp.)	No defects or abnormalities. Within +/-3% Q ≥ 350 More than 1000MΩ	Mounting method Test Temperature Test Time Test Voltage Charge/discharge current Post-treatment	Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min Maximum Operating Temperature +/-3°C 1000+/-12h 200% of the rated voltage 50mA max. Non treatment:Let sit for 24+/-2hours at room temperature, then measure.
9	Appearance	No defects or abnormalities.		Visual inspection	
10	Dimension	Shown in Dimension.		Using Measuring instrument of dimension.	
11	Resistance to Solvents	Appearance Capacitance Q or D.F. I.R.(Room Temp.)	No defects or abnormalities. Within the specified initial value. Within the specified initial value. More than 10000MΩ	Per MIL-STD-202 Method 215	
12	Mechanical Shock	Appearance Capacitance Q or D.F. I.R.(Room Temp.)	No defects or abnormalities. Within the specified initial value. Within the specified initial value. More than 10000MΩ	Mounting method Waveform Peak value Holding Time Velocity change Shocks directions and times	Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min Half-sine 1500g 0.5ms 4.7m/s Three shocks in each direction should be applied along 3 mutually perpendicular axes of the test specimen (18 shocks).
13	Vibration	Appearance Capacitance Q or D.F. I.R.(Room Temp.)	No defects or abnormalities. Within the specified initial value. Within the specified initial value. More than 10000MΩ	Mounting method Kind of Vibration Vibration Time Total amplitude Vibration directions and time	Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min A 10Hz to 2000Hz to 10Hz 20min 1.5mm This motion should be applied for 12 times in each 3 mutually perpendicular directions (total of 36 times).
14	ESD	Appearance Capacitance Q or D.F. I.R.(Room Temp.)	No defects or abnormalities. Within the specified initial value. Within the specified initial value. More than 10000MΩ	Per AEC-Q200-002	

■ Specifications and Test Methods

No	Item	Specification	Test Method (Compliant Standard:AEC-Q200)
15	Capacitance	Shown in Rated value.	Measurement Temperature 25°C Measurement Frequency 1.0+/-0.1MHz Measurement Voltage 0.5 to 5.0Vrms
16	Q or Dissipation Factor (D.F.)	$Q \geq 1000$	Measurement Temperature 25°C Measurement Frequency 1.0+/-0.1MHz Measurement Voltage 0.5 to 5.0Vrms
17	Insulation Resistance(I.R.) (Room Temperature)	More than 100000MΩ	Measurement Temperature 25°C Measurement Voltage Rated Voltage Charging Time 2min Charge/discharge current 50mA max.
18	Insulation Resistance(I.R.) (High Temperature)	More than 10000MΩ	Measurement Temperature 150°C Measurement Voltage Rated Voltage Charging Time 2min Charge/discharge current 50mA max.
19	Voltage proof	No defects or abnormalities.	Test Voltage 250% of the rated voltage Applied Time 1s to 5s Charge/discharge current 50mA max.
20	Terminal Strength	Appearance No defects or abnormalities. Capacitance Within the specified initial value. Q or D.F. Within the specified initial value. I.R.(Room Temp.) More than 10000MΩ	Mounting method Mount the capacitor on the test substrate using a conductive glue*. *Conductive glue:HEREAUS"PC3000", Curing conditions:140°C/30min Applied Force 2N Holding Time 60s
21	Beam Load Test	Destruction Value: More than 8N	Speed supplied the Stress Load 0.5mm/s Placement diagram < Chip Length : 2.5mm max. > 

■ Specifications and Test Methods

No	Item	Specification	Test Method (Compliant Standard:AEC-Q200)												
22	Temperature Characteristics of Capacitance	<p>No bias</p> <p>Nominal values of the temperature coefficient is shown in Rated value. But, the Capacitance Change under Reference Temperature is shown in Table A.</p> <p>Capacitance Drift: Within +/-0.2% or +/-0.05pF (Whichever is larger.)</p>	<p>The capacitance change should be measured after 5 min at each specified temp. stage.</p> <p>Capacitance value as a reference is the value in "*" marked step.</p> <p>Capacitance Drift The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in the step 1,3 and 5 by the cap. value in step 3.</p> <p>Measurement Voltage Less than 1.0Vrms (Refer to the individual data sheet)</p> <p>Temperature Step</p> <p style="text-align: center;">< No bias ></p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Step</th> <th>Temperature(°C)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Reference Temp. +/-2</td> </tr> <tr> <td>2</td> <td>Min. Operating Temp. +/-3</td> </tr> <tr> <td>3 *</td> <td>Reference Temp. +/-2</td> </tr> <tr> <td>4</td> <td>Max. Operating Temp. +/-3</td> </tr> <tr> <td>5</td> <td>Reference Temp. +/-2</td> </tr> </tbody> </table>	Step	Temperature(°C)	1	Reference Temp. +/-2	2	Min. Operating Temp. +/-3	3 *	Reference Temp. +/-2	4	Max. Operating Temp. +/-3	5	Reference Temp. +/-2
Step	Temperature(°C)														
1	Reference Temp. +/-2														
2	Min. Operating Temp. +/-3														
3 *	Reference Temp. +/-2														
4	Max. Operating Temp. +/-3														
5	Reference Temp. +/-2														

Table A Capacitance Change between at Reference Temp. and at each Temp. (%)

Char.	-55°C		-30°C		-10°C	
	Max.	Min.	Max.	Min.	Max.	Min.
5G	0.58	-0.24	0.4	-0.17	0.25	-0.11

Mounting method

- Test substrate
Material Alumina Substrate
- Land Dimension



Fig.1

Type	Dimension(mm)		
	a	b	c
GCG15	0.4	1.5	0.5

■ Package (Tape Carrier Packaging)

1. Minimum Quantity(pcs./reel)

Type		φ180mm Reel(W8P2)	φ180mm Reel(W8P1)	φ330mm Reel(W8P2)
		PAPER Tape	PAPER Tape	PAPER Tape
		CODE : D	CODE : W	CODE : J
GCG15	5	10000	20000	50000

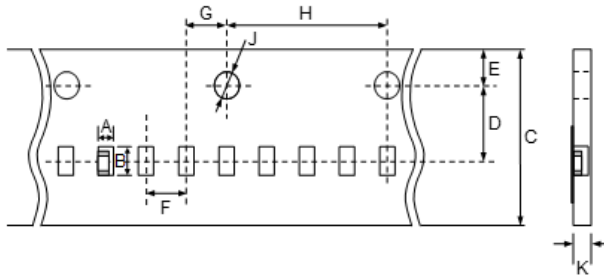
2. Dimensions of Tape (in mm)

(1)GCG15(W8P2 CODE : D/J)



Type		Dimensions(Chip)			A	B	C	D	E	F	G	H	J	K	M
		L	W	T											
GCG15	5	1.0+/-0.1	0.5+/-0.05	0.5+/-0.05	0.65(Typ.)	1.15(Typ.)	8.0+/-0.3	3.5+/-0.05	1.75+/-0.1	2.0+/-0.05	2.0+/-0.05	4.0+/-0.1	φ1.5+0.1/-0	0.8 max.	0.05 max.

(2)GCG15(W8P1 CODE : W)



Type		Dimensions(Chip)			A	B	C	D	E	F	G	H	J	K
		L	W	T										
GCG15	5	1.0+/-0.1	0.5+/-0.05	0.5+/-0.05	0.65(Typ.)	1.15(Typ.)	8.0+/-0.3	3.5+/-0.05	1.75+/-0.1	1.0+/-0.05	1.0+/-0.05	4.0+/-0.1	φ1.5+0.1/-0	0.8 max.

■ Package (Tape Carrier Packaging)

3. Dimensions of Reel (in mm)



Reel	A	B	C	D	E	W	W1
$\phi 180$ mm Reel	$\phi 180+0/-3.0$	$\phi 50$ min.	$\phi 13+/-0.2$	$\phi 21+/-0.8$	$2.0+/-0.5$	14.4 max.	$8.4+1.5$
$\phi 330$ mm Reel	$\phi 330+/-2.0$	$\phi 50$ min.	$\phi 13+/-0.2$	$\phi 21+/-0.8$	$2.0+/-0.5$	14.4 max.	$8.4+1.5$

■ Package (Tape Carrier Packaging)

- Part of the leader and part of the vacant section are attached as follows.
The sprocket holes are to the right as the tape is pulled toward the user.



- Accumulate tolerance of sprocket holes pitch = +/-0.3mm/10 pitch
- Chip in the tape is enclosed by top tape and bottom tape as shown in 2.Dimensions of Tape.
- The top tape and carrier tape are not attached at the end of the tape for a minimum of 5 pitches.
- There are no jointing for top tape and bottom tape.
- There are no fuzz in the cavity.
- Break down force of top tape : 5N min.
Break down force of bottom tape : 5N min. (Only a bottom tape existence)
- Reel is made by resin and appeaser and dimension is shown in 3.Dimensions of Reel.
There are possibly to change the material and dimension due to some impairment.
- Peeling off force : 0.1N to 0.7N in the direction as shown below.

Speed of Peeling off : 300 mm / min



- Label that show the customer parts number, our parts number, our company name, inspection number and quantity, will be put in outside of reel.

**Caution****■ Limitation of Applications**

The products listed in the Specification (hereinafter the product(s) is called as the "Product(s)") are designed and manufactured for applications specified in the Specification. (hereinafter called as the "Specific Application"). We shall not warrant anything in connection with the Products including fitness, performance, adequateness, safety, or quality, in the case of applications listed in from (1) to (11) written at the end of this precautions, which may generally require high performance, function, quality, management of production or safety. Therefore, the Product shall be applied in compliance with the specific application.

WE DISCLAIM ANY LOSS AND DAMAGES ARISING FROM OR IN CONNECTION WITH THE PRODUCTS INCLUDING BUT NOT LIMITED TO THE CASE SUCH LOSS AND DAMAGES CAUSED BY THE UNEXPECTED ACCIDENT, IN EVENT THAT (i) THE PRODUCT IS APPLIED FOR THE PURPOSE WHICH IS NOT SPECIFIED AS THE SPECIFIC APPLICATION FOR THE PRODUCT, AND/OR (ii) THE PRODUCT IS APPLIED FOR ANY FOLLOWING APPLICATION PURPOSES FROM (1) TO (11) (EXCEPT THAT SUCH APPLICATION PURPOSE IS UNAMBIGUOUSLY SPECIFIED AS SPECIFIC APPLICATION FOR THE PRODUCT IN THE SPECIFICATION*).

(1)Aircraft equipment (2)Aerospace equipment (3)Undersea equipment (4)Power plant control equipment (5)Medical equipment (6)Transportation equipment (7)Traffic control equipment (8)Disaster prevention/security equipment (9)Industrial data-processing equipment (10)Combustion/explosion control equipment (11)Equipment with complexity and/or required reliability equivalent to the applications listed in the above.

For exploring information of the Products which will be compatible with the particular purpose other than those specified in the Specification, please contact our sales offices, distribution agents, or trading companies with which you make a deal, or via our web contact form. Contact form: <https://www.murata.com/contactform>

*We may design and manufacture particular Products for applications listed in (1) to (11). Provided that, in such case we shall unambiguously specify such Specific Application in the Specification without any exception. Therefore, any other documents and/or performances, whether exist or non-exist, shall not be deemed as the evidence to imply that we accept the applications listed in (1) to (11).

■ Storage and Operation condition

1. If store the chip multilayer ceramic capacitors (henceforth just "capacitors") in an atmosphere consisting of high temperature or humidity, sulfur or chlorine gases, contaminants attach to the surface of external electrode, and bondability with conductive glue may deteriorate. Do not store the capacitors in an atmosphere consisting of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas, etc.).
Storage environment must be at room temperature of +5°C to +40°C and a relative humidity of 20% to 70%, and use the product within six months after receipt.
In case of packaging, do not open the last wrap, polyethylene bag, till just before using. After unpacking, immediately reseal, or store in a desiccator containing a desiccant.
2. Due to moisture condensation caused by rapid humidity changes, or the photochemical change caused by direct sunlight on the terminal electrodes, the bondability with conductive glue and electrical performance may deteriorate. Do not store capacitors under direct sunlight or in high humidity conditions.
3. This product is limited to conductive glue mounting. Do not apply mounting method other than conductive glue.
Flow or reflow soldering can result in a lack of adhesive strength on the outer electrode by poor wettability, which may result in chips breaking loose from the PCB.

■ Rating

1. Temperature Dependent Characteristics

1. The electrical characteristics of the capacitor can change with temperature.

1-1. For capacitors having larger temperature dependency, the capacitance may change with temperature changes. The following actions are recommended in order to ensure suitable capacitance values.

- (1) Select a suitable capacitance for the operating temperature range.
- (2) The capacitance may change within the rated temperature. When you use a high dielectric constant type capacitor in a circuit that needs a tight (narrow) capacitance tolerance (e.g., a time-constant circuit), please carefully consider the temperature characteristics, and carefully confirm the various characteristics in actual use conditions and the actual system.



2. Measurement of Capacitance

1. Measure capacitance with the voltage and frequency specified in the product specifications.

- 1-1. The output voltage of the measuring equipment may decrease occasionally when capacitance is high. Please confirm whether a prescribed measured voltage is impressed to the capacitor.
- 1-2. The capacitance values of high dielectric constant type capacitors change depending on the AC voltage applied. Please consider the AC voltage characteristics when selecting a capacitor to be used in a AC circuit.

3. Applied Voltage

1. Do not apply a voltage to the capacitor that exceeds the rated voltage as called out in the specifications.

- 1-1. Applied voltage between the terminals of a capacitor shall be less than or equal to the rated voltage.
 - (1) When AC voltage is superimposed on DC voltage, the zero-to-peak voltage shall not exceed the rated DC voltage. When AC voltage or pulse voltage is applied, the peak-to-peak voltage shall not exceed the rated DC voltage.
 - (2) Abnormal voltages (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated DC voltage.



1-2. Influence of over voltage

Over voltage that is applied to the capacitor may result in an electrical short circuit caused by the breakdown of the internal dielectric layers. The time duration until breakdown depends on the applied voltage and the ambient temperature.

4. Type of Applied Voltage and Self-heating Temperature

1. Confirm the operating conditions to make sure that no large current is flowing into the capacitor due to the continuous application of an AC voltage or pulse voltage. When a DC rated voltage product is used in an AC voltage circuit or a pulse voltage circuit, the AC current or pulse current will flow into the capacitor; therefore check the self-heating condition. Please confirm the surface temperature of the capacitor so that the temperature remains within the upper limits of the operating temperature, including the rise in temperature due to self-heating. When the capacitor is used with a high-frequency voltage or pulse voltage, heat may be generated by dielectric loss.

< Applicable to Rated Voltage of less than 100VDC >

The load should be contained so that the self-heating of the capacitor body remains below 20°C, when measuring at an ambient temperature of 25°C.



5. DC Voltage and AC Voltage Characteristic

1. The capacitance value of a high dielectric constant type capacitor changes depending on the DC voltage applied. Please consider the DC voltage characteristics when a capacitor is selected for use in a DC circuit.

1-1. The capacitance of ceramic capacitors may change sharply depending on the applied voltage. (See figure)
 Please confirm the following in order to secure the capacitance.

- (1) Determine whether the capacitance change caused by the applied voltage is within the allowed range.
- (2) In the DC voltage characteristics, the rate of capacitance change becomes larger as voltage increases, even if the applied voltage is below the rated voltage. When a high dielectric constant type capacitor is used in a circuit that requires a tight (narrow) capacitance tolerance (e.g., a time constant circuit), please carefully consider the voltage characteristics, and confirm the various characteristics in the actual operating conditions of the system.



2. The capacitance values of high dielectric constant type capacitors changes depending on the AC voltage applied. Please consider the AC voltage characteristics when selecting a capacitor to be used in a AC circuit.



6. Capacitance Aging

1. The high dielectric constant type capacitors have an Aging characteristic in which the capacitance value decreases with the passage of time. When you use a high dielectric constant type capacitors in a circuit that needs a tight (narrow) capacitance tolerance (e.g., a time-constant circuit), please carefully consider the characteristics of these capacitors, such as their aging, voltage, and temperature characteristics. In addition, check capacitors using your actual appliances at the intended environment and operating conditions.



7. Vibration and Shock

1. Please confirm the kind of vibration and/or shock, its condition, and any generation of resonance. Please mount the capacitor so as not to generate resonance, and do not allow any impact on the terminals.
2. Mechanical shock due to being dropped may cause damage or a crack in the dielectric material of the capacitor. Do not use a dropped capacitor because the quality and reliability may be deteriorated.
3. When printed circuit boards are piled up or handled, the corner of another printed circuit board should not be allowed to hit the capacitor in order to avoid a crack or other damage to the capacitor.



■ **Mounting**

1. Selection of Conductive Adhesive, Mounting Process, and Bonding Strength

1. The acquired bonding strength may change greatly depending on the conductive adhesive to be used.
 Be sure to confirming the desired performance can be acquired in the assumed monting process with the conductive adhesive to be used.

2. Maintenance of the Mounting (pick and place) Machine

1. Make sure that the following excessive forces are not applied to the capacitors. Check the mounting in the actual device under actual use conditions ahead of time.
 - 1-1. In mounting the capacitors on the printed circuit board, any bending force against them shall be kept to a minimum to prevent them from any damage or cracking.
 Please take into account the following precautions and recommendations for use in your process.
 - (1) Adjust the lowest position of the pickup nozzle so as not to bend the printed circuit board.



2. Dirt particles and dust accumulated in the suction nozzle and suction mechanism prevent the nozzle from moving smoothly. This creates excessive force on the capacitor during mounting, causing cracked chips.
 Also, the locating claw, when worn out, imposes uneven forces on the chip when positioning, causing cracked chips. The suction nozzle and the locating claw must be maintained, checked and replaced periodically.

3. Moisture proof

1. To prevent the silver electrode migration, keep parts under low moisture condition with resin coating and the equivalent.

4. Coating

1. A crack may be caused in the capacitor due to the stress of the thermal contraction of the resin during curing process.
 The stress is affected by the amount of resin and curing contraction. Select a resin with low curing contraction.
 The difference in the thermal expansion coefficient between a coating resin or a molding resin and the capacitor may cause the destruction and deterioration of the capacitor such as a crack or peeling, and lead to the deterioration of insulation resistance or dielectric breakdown.
 Select a resin for which the thermal expansion coefficient is as close to that of the capacitor as possible. A silicone resin can be used as an under-coating to buffer against the stress.
2. Select a resin that is less hygroscopic.
 Using hygroscopic resins under high humidity conditions may cause the deterioration of the insulation resistance of a capacitor. An epoxy resin can be used as a less hygroscopic resin.
3. The halogen system substance and organic acid are included in coating material, and a chip corrodes by the kind of Coating material. Do not use strong acid type.

■ Others**1. Under Operation of Equipment**

- 1-1. Do not touch a capacitor directly with bare hands during operation in order to avoid the danger of an electric shock.
- 1-2. Do not allow the terminals of a capacitor to come in contact with any conductive objects (short-circuit).
Do not expose a capacitor to a conductive liquid, inducing any acid or alkali solutions.
- 1-3. Confirm the environment in which the equipment will operate is under the specified conditions.
Do not use the equipment under the following environments.
- (1) Being spattered with water or oil.
 - (2) Being exposed to direct sunlight.
 - (3) Being exposed to ozone, ultraviolet rays, or radiation.
 - (4) Being exposed to toxic gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
 - (5) Any vibrations or mechanical shocks exceeding the specified limits.
 - (6) Moisture condensing environments.
- 1-4. Use damp proof countermeasures if using under any conditions that can cause condensation.

2. Others

- 2-1. In an Emergency
- (1) If the equipment should generate smoke, fire, or smell, immediately turn off or unplug the equipment. If the equipment is not turned off or unplugged, the hazards may be worsened by supplying continuous power.
 - (2) In this type of situation, do not allow face and hands to come in contact with the capacitor or burns may be caused by the capacitor's high temperature.
- 2-2. Disposal of waste
When capacitors are disposed of, they must be burned or buried by an industrial waste vendor with the appropriate licenses.
- 2-3. Circuit Design
- (1) Addition of Fail Safe Function
Capacitors that are cracked by dropping or bending of the board may cause deterioration of the insulation resistance, and result in a short.
If the circuit being used may cause an electrical shock, smoke or fire when a capacitor is shorted, be sure to install fail-safe functions, such as a fuse, to prevent secondary accidents.
 - (2) This series are not safety standard certified products.
- 2-4. Remarks
Failure to follow the cautions may result, worst case, in a short circuit and smoking when the product is used.
The above notices are for standard applications and conditions. Contact us when the products are used in special mounting conditions.
Select optimum conditions for operation as they determine the reliability of the product after assembly.
The data herein are given in typical values, not guaranteed ratings.

Notice

■ Rating

1. Operating Temperature

1. The operating temperature limit depends on the capacitor.
 - 1-1. Do not apply temperatures exceeding the maximum operating temperature.
It is necessary to select a capacitor with a suitable rated temperature that will cover the operating temperature range.
It is also necessary to consider the temperature distribution in equipment and the seasonal temperature variable factor.
 - 1-2. Consider the self-heating factor of the capacitor
The surface temperature of the capacitor shall not exceed the maximum operating temperature including self-heating.

2. Atmosphere Surroundings (gaseous and liquid)

1. Restriction on the operating environment of capacitors.
 - 1-1. The capacitor will short-circuit by water or brine. It may shorten the lifetime and may have the failure by the corrosion of terminals and the permeation of moisture into capacitor.
 - 1-2. The same phenomenon as the above may occur when the electrodes or terminals of the capacitor are subject to moisture condensation.
 - 1-3. The deterioration of characteristics and insulation resistance due to the oxidization or corrosion of terminal electrodes may result in breakdown when the capacitor is exposed to corrosive or volatile gases or solvents for long periods of time.

3. Piezo-electric Phenomenon

1. When using high dielectric constant type capacitors in AC or pulse circuits, the capacitor itself vibrates at specific frequencies and noise may be generated. Moreover, when the mechanical vibration or shock is added to capacitor, noise may occur.

■ Others**1. Transportation**

1. The performance of a capacitor may be affected by the conditions during transportation.

1-1. The capacitors shall be protected against excessive temperature, humidity and mechanical force during transportation.

- Mechanical condition

Transportation shall be done in such a way that the boxes are not deformed and forces are not directly passed on to the inner packaging.

1-2. Do not apply excessive vibration, shock, or pressure to the capacitor.

(1) When excessive mechanical shock or pressure is applied to a capacitor, chipping or cracking may occur in the ceramic body of the capacitor.

(2) When the sharp edge of an air driver, tweezers, a chassis, etc. impacts strongly on the surface of the capacitor, the capacitor may crack and short-circuit.

1-3. Do not use a capacitor to which excessive shock was applied by dropping etc. A capacitor dropped accidentally during processing may be damaged.

2. Characteristics Evaluation in the Actual System

1. Evaluate the capacitor in the actual system, to confirm that there is no problem with the performance and specification values in a finished product before using.

2. Since a voltage dependency and temperature dependency exists in the capacitance of high dielectric type ceramic capacitors, the capacitance may change depending on the operating conditions in the actual system.

Therefore, be sure to evaluate the various characteristics, such as the leakage current and noise absorptivity, which will affect the capacitance value of the capacitor.

3. In addition, voltages exceeding the predetermined surge may be applied to the capacitor by the inductance in the actual system.

Evaluate the surge resistance in the actual system as required.

**NOTE**

1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
2. You are requested not to use our product deviating from this product specification.
3. We consider it not appropriate to include any terms and conditions with regard to the business transaction in the product specifications, drawings or other technical documents.
Therefore, if your technical documents as above include such terms and conditions such as warranty clause, product liability clause, or intellectual property infringement liability clause, they will be deemed to be invalid.