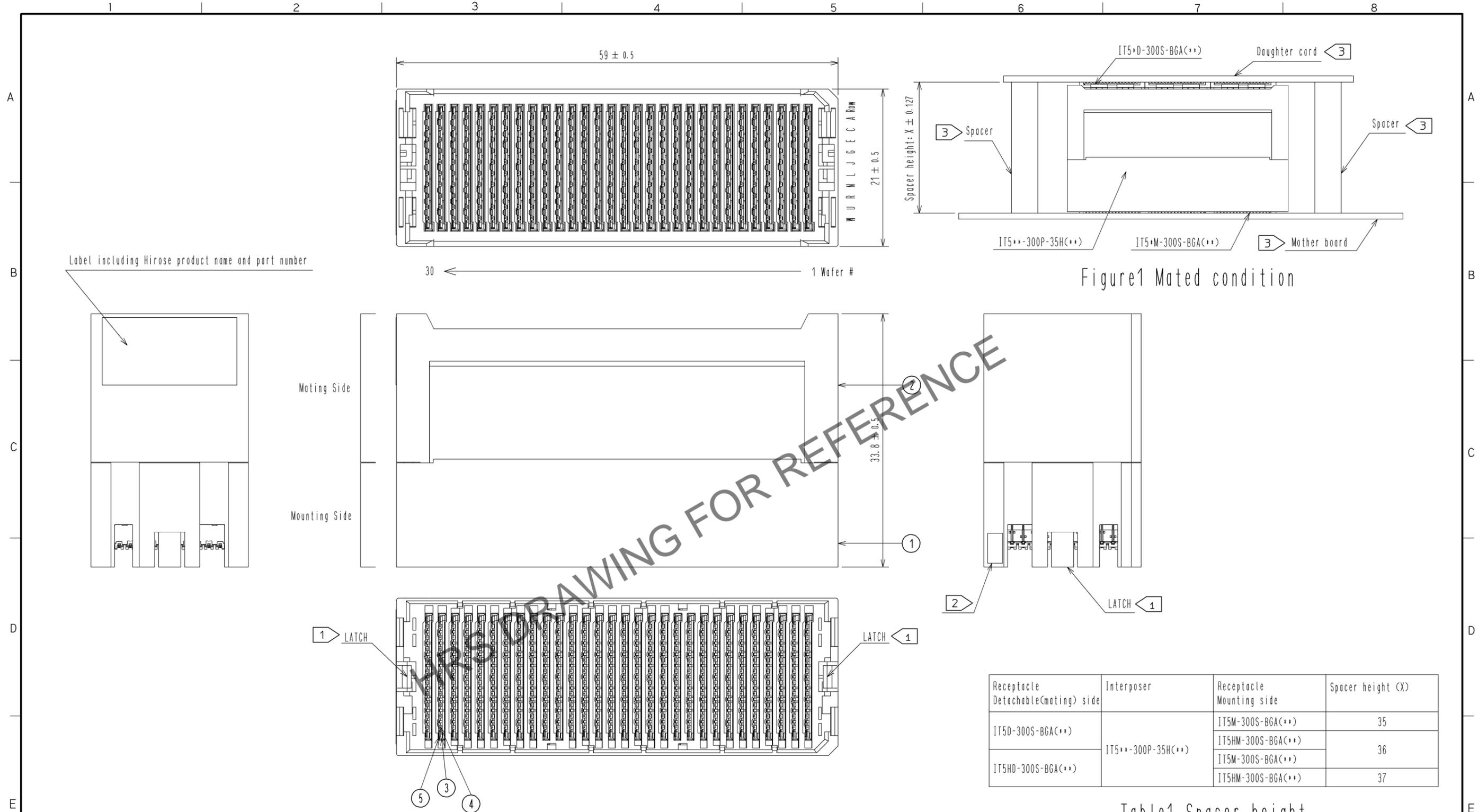


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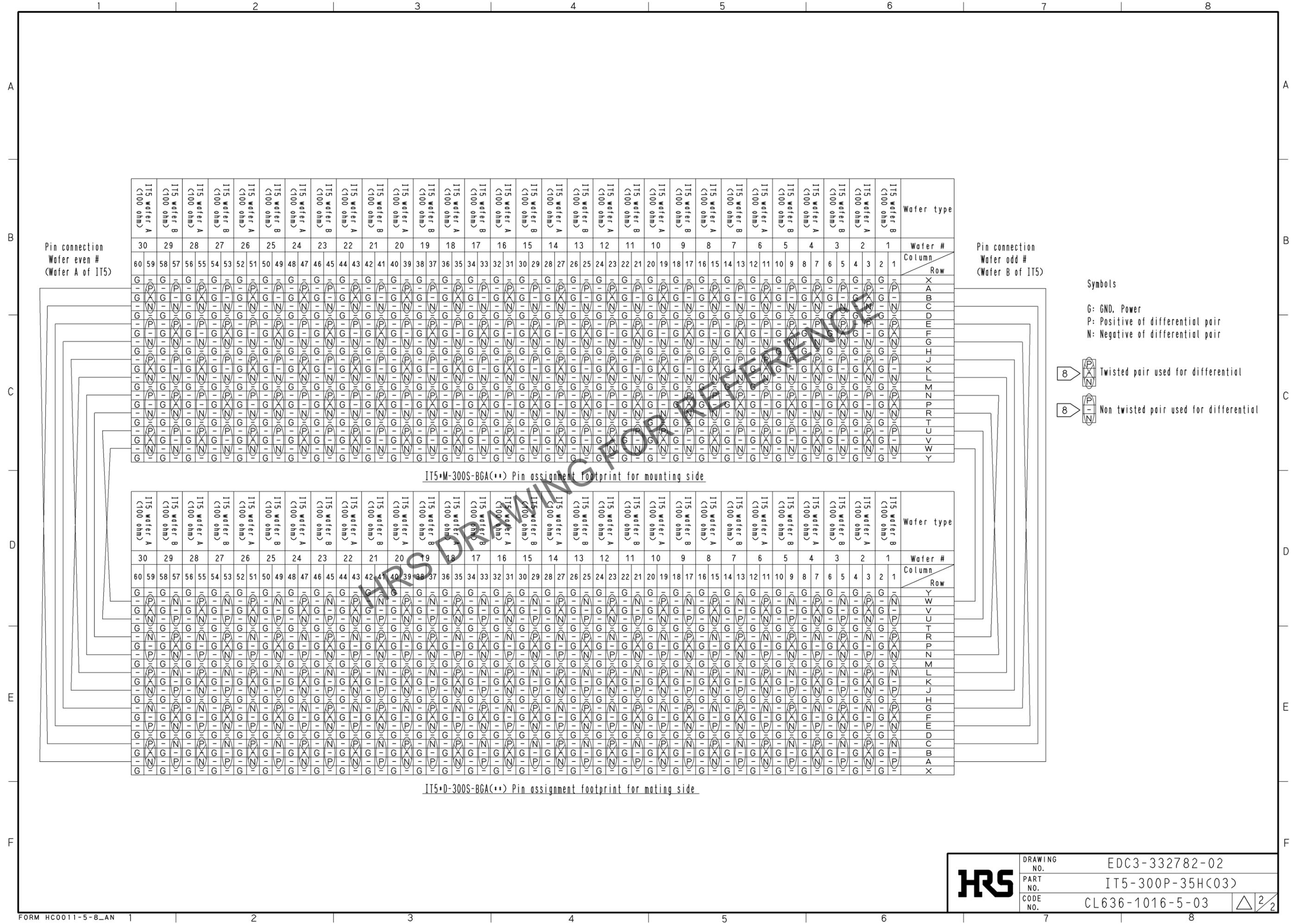


- NOTE.
- ① Mounting side is mated with the receptacle:IT5*M-300S-BGA(**) and fixed with the latch.
 - ② Lot No. shall be marked on the indicated position.
 - ③ The indicated mother board and daughter card shall be fastened to the additional reinforcements to prevent the stress on IT5**-300S-BGA(**) solder joints.
 4. Please refer to HIROSE document: ETAD-F0584 'IT5 design guideline'.
 5. This product consists of 300 signal lines.
 6. 15pcs of this products are packed in a soft tray.
 7. Standard package of 30pcs are packaged in a carton box.
 - ⑧ Please contact Hirose regarding pin assignment for single-ended signal transmission at the pairs used for differential signal transmission.

NO.	MATERIAL	FINISH . REMARKS	NO.	MATERIAL	FINISH . REMARKS
3	LCP	BLACK, UL94V-0	5	Copper Alloy	Contact area:Nickel(1.5μm)+Gold(0.76μm) Other area :Nickel(1.5μm)
2	LCP	GRAY, UL94V-0	4	Copper Alloy	Contact area:Nickel(1.5μm)+Gold(0.76μm) Other area :Nickel(1.5μm)
1	PBT	BLACK, UL94V-0			

UNITS	SCALE	COUNT	DESCRIPTION OF REVISIONS	DESIGNED	CHECKED	DATE
mm	2 : 1	△	APPROVED : MK. EZAKI 13.10.18			
HIROSE ELECTRIC CO., LTD.			CHECED : MK. NAGATA 13.10.18	DRAWING NO. EDC3-332782-02		
			DESIGNED : TF. SUGAWARA 13.10.18	PART NO. IT5-300P-35H(03)		
			DRAWN : TF. SUGAWARA 13.10.18	CODE NO. CL636-1016-5-03		
						1/2

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Pin connection
 Wafer even #
 (Wafer A of IT5)

Pin connection
 Wafer odd #
 (Wafer B of IT5)

- Symbols
- G: GND, Power
 - P: Positive of differential pair
 - N: Negative of differential pair
- Twisted pair used for differential
 - Non twisted pair used for differential

IT5-M-300S-BGA(**) Pin assignment footprint for mounting side

IT5-D-300S-BGA(**) Pin assignment footprint for mating side

HRS	DRAWING NO.	EDC3-332782-02
	PART NO.	IT5-300P-35H(C03)
	CODE NO.	CL636-1016-5-03
		2/2