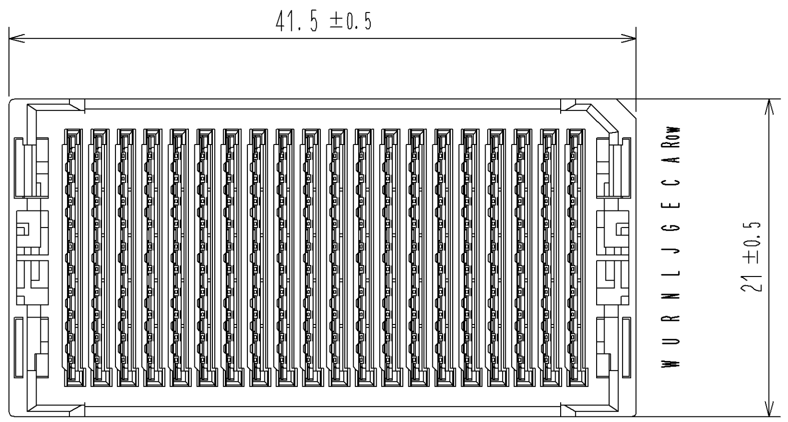


20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	Column
5A	5B	5A	5B	5A	5B	5A	5B	5A	5B	5A	5B	5A	5B	5A	5B	5A	5B	5A	5B	Blade type

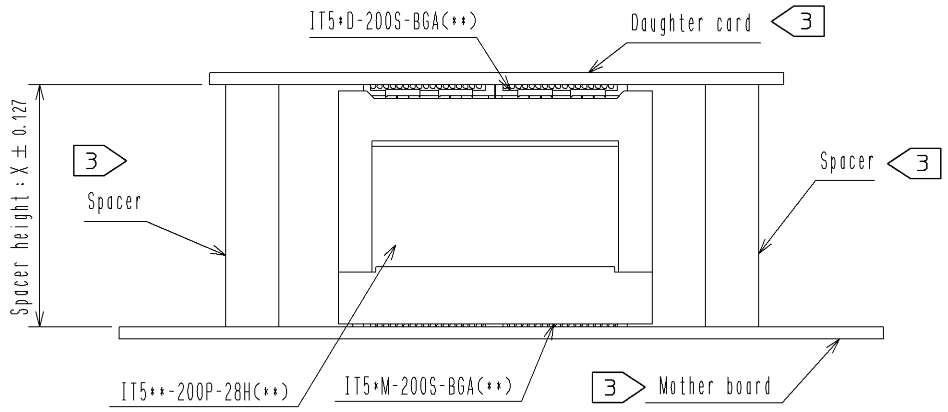
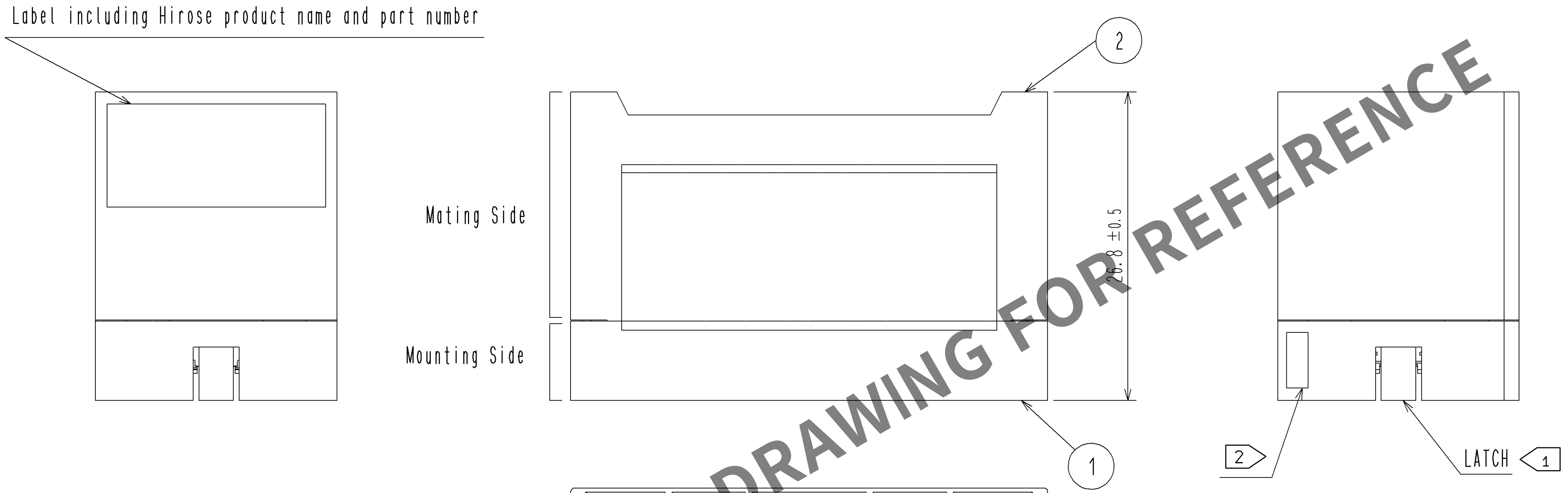
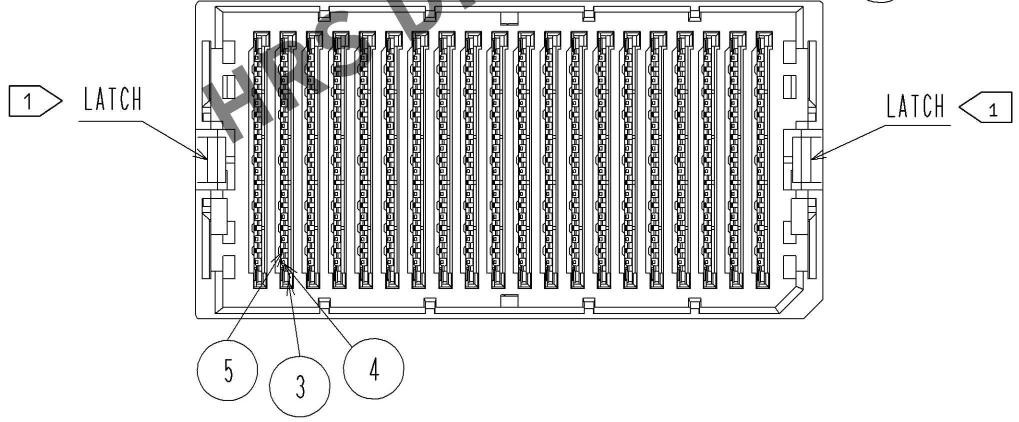


Fig.1 (FREE)





Receptacle Detachable(mating) side	Interposer	Receptacle Mounting side	Spacer height (X)
IT5D-200S-BGA(**)	IT5**-200P-28H(**)	IT5M-200S-BGA(**)	28
		IT5HM-200S-BGA(**)	29
IT5HD-200S-BGA(**)		IT5M-200S-BGA(**)	
		IT5HM-200S-BGA(**)	30

Table1 Spacer height

NOTE. 1 Mounting side is mated with the receptacle:IT5*M-200S-BGA(**) and fixed with the latch.
2 Lot No. shall be marked on the indicated position.
3 The indicated mother board and daughter card shall be fastened to the additional reinforcements to prevent the stress on IT5**-200S-BGA(**) solder joints.
4. Please refer to HIROSE document: ETAD-F0584 ‘IT5 design guideline’.
5. This product consists of 200 signal lines.
6. 40pcs of this products are packed in a soft tray.
7. Standard package of 40pcs are packaged in a carton box.
8 Please contact Hirose regarding pin assignment for single-ended signal transmission at the pairs used for differencial signal transmission.

NO.	MATERIAL	FINISH , REMARKS
3	LCP	BLACK, UL94V-0
2	LCP	GRAY, UL94V-0
1	PBT	BLACK, UL94V-0

NO.	MATERIAL	FINISH , REMARKS
5	Copper Alloy	Contact area:Nickel(1.5μm)+Gold(0.76μm) Other area :Nickel(1.5μm)
4	Copper Alloy	Contact area:Nickel(1.5μm)+Gold(0.76μm) Other area :Nickel(1.5μm)

UNITS	SCALE		COUNT	DESCRIPTION OF REVISIONS	DESIGNED	CHECKED	DATE
mm	2 : 1	△					

HIROSE
ELECTRIC
CO., LTD.

APPROVED	:MK. EZAKI	14. 07. 04
CHECED	:MK. NAGATA	14. 07. 04
DESIGNED	:TF. SUGAWARA	14. 07. 04
DRAWN	:TF. SUGAWARA	14. 07. 04

DRAWING NO.	EDC3-340334-01
PART NO.	IT5-200P-28H(03)
CODE NO.	CL636-1042-5-03 △ 1/2

FORM HC0011-5-7_AN

IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		Wafer type
20		19		18		17		16		15		14		13		12		11		10		9		8		7		6		5		4		3		2		1		Wafer #
40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	Column Row
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	X
-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	A
G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	B
-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	C
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	D
-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	E
G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	F
-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	G
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	H
-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	J
G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	K
-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	L
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	M
-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	N
G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	P
-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	R
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	T
-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	-	P	U
G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	V
-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	-	N	W
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	Y

IT5*M-200S-BGA(**) Pin assignment footprint for mounting side

IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		IT5 wafer A (100 ohm)		IT5 wafer B (100 ohm)		Wafer type
20		19		18		17		16		15		14		13		12		11		10		9		8		7		6		5		4		3		2		1		Wafer #
40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	Column Row
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	Y
-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	W
G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	V
-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	U
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	T
-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	R
G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	P
-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	N
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	M
-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	L
G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	K
-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	J
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	H
-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	G
G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	F
-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	E
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	D
-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	C
G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	G	×	G	-	B
-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	-	N	-	P	A
G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	G	-	X

IT5*D-200S-BGA(**) Pin assignment footprint for mating side

Symbols

G: GND, Power
P: Positive of differential pair
N: Negative of differential pair

8 P × N Twisted pair used for differential

8 P - N Non twisted pair used for differential

HRS
DRAWING NO. EDC3-340334-01
PART NO. IT5-200P-28H(03)
CODE NO. CL636-1042-5-03 △ 2/2

FORM HC0011-5-8