

Click [here](#) for the 3D model.

General Information

Series	TF
Dielectric	Polymer Tantalum
Style	SMD Chip
Description	SMD, Polymer, Molded, Face Down Terminals
Features	Face Down, Low Profile, Low ESR
RoHS	Yes
Termination	Nickel Palladium Gold
AEC-Q200	No
Shelf Life	52 Weeks
MSL	3

Specifications

Capacitance	47 uF
Tolerance	20%
Voltage DC	6.3 VDC (105C)
Temperature Range	-55/+105°C
Rated Temperature	105°C
Life	1000 Hrs (105C)
Humidity	60C, 90% RH, 500 Hours
Failure Rate	N/A
ESR	150 mOhms (100kHz 25C)
Ripple Current	632 mA (rms, 100kHz 45C), 442.4 mA (rms, 85C), 158 mA (rms, 105C)
Leakage Current	29.6 uA (5min 25°C)

Dimensions

L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
H	0.8mm MAX
S	0.8mm +/-0.2mm
F	1.2mm +/-0.1mm

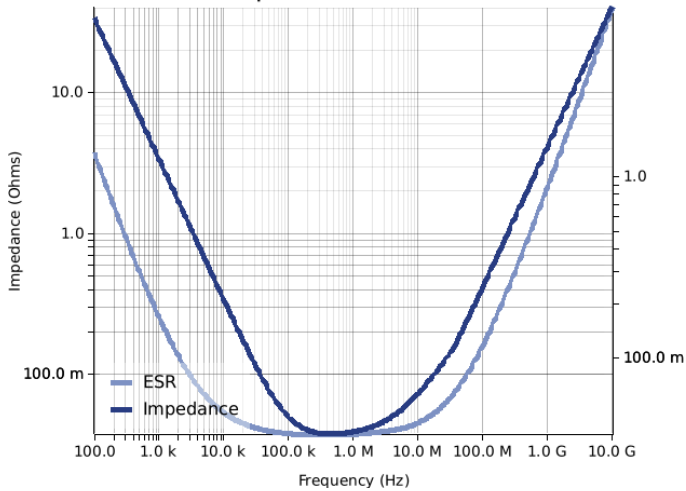
Packaging Specifications

Packaging	T&R, 178mm
Packaging Quantity	3000

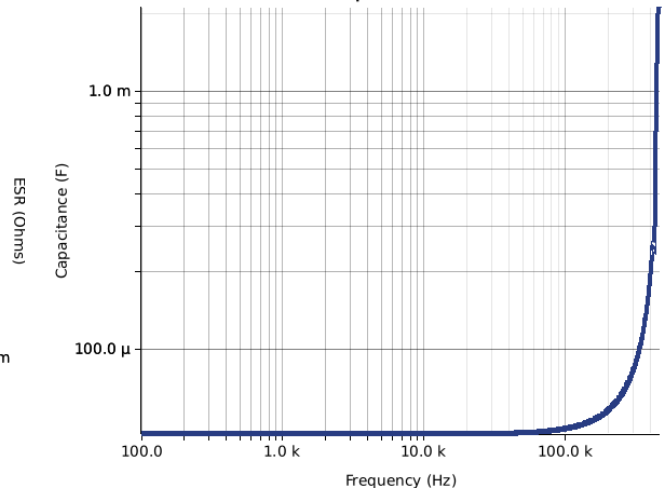
Simulations

For the complete simulation environment please visit [Y-SIM](#).

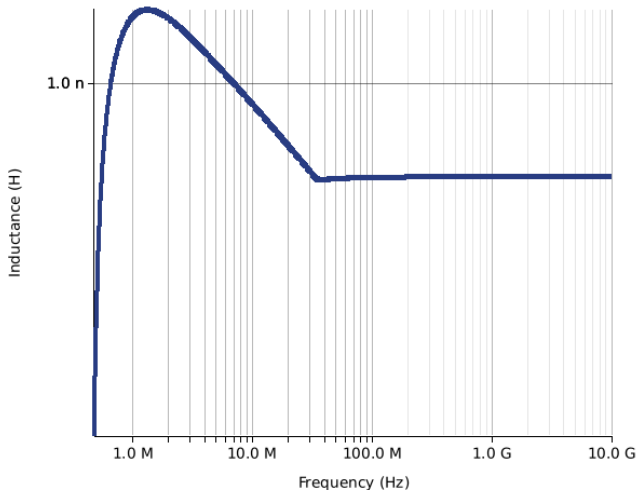
Impedance and ESR



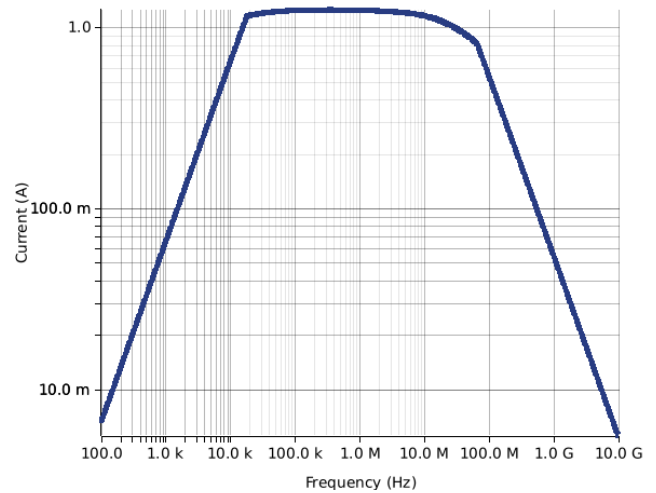
Capacitance



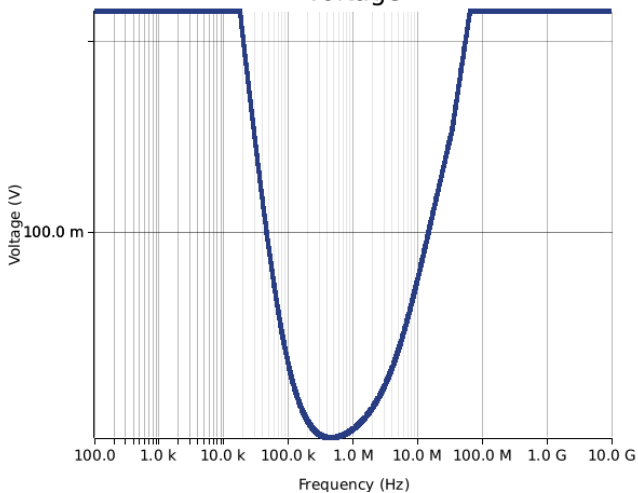
Inductance



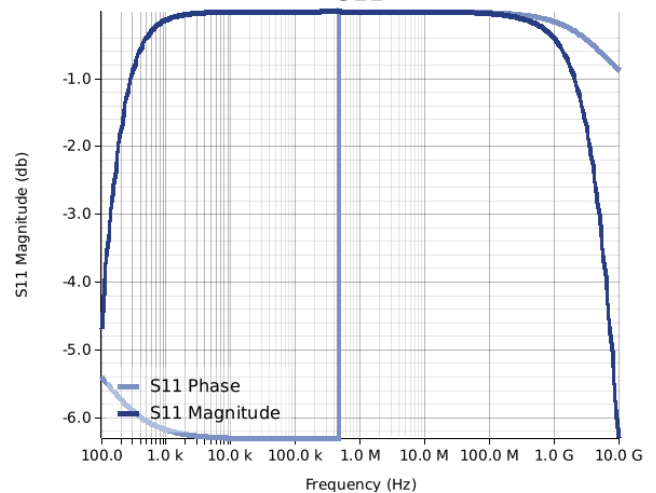
Current

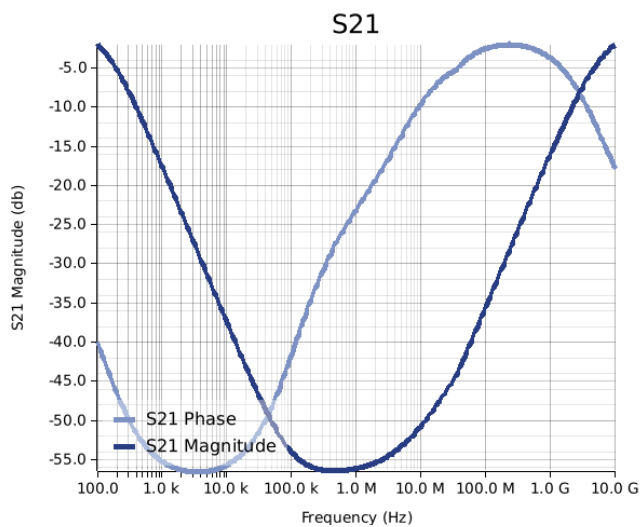


Voltage



S11





These are simulations.
This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance).
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.