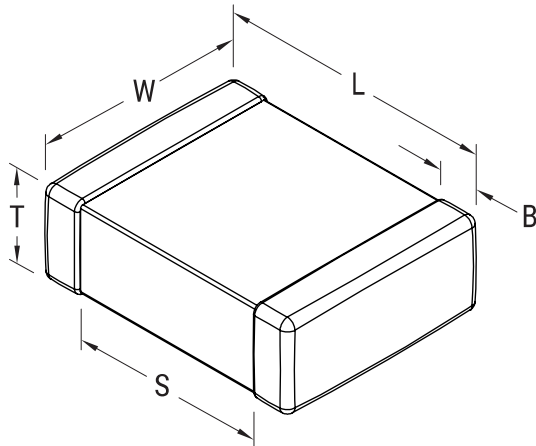


C1812T475K5RALTU

Aliases (C1812T475K5RAL7800)

SMD COTS X7R, Ceramic, 4.7 uF, 10%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 2.3 mm, 1812 / 4532



Click [here](#) for the 3D model.

General Information

Series	SMD COTS X7R
Style	SMD Chip
Description	SMD, MLCC, COTS, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	No
Prop 65	WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov/
SCIP Number	2d771165-5336-48a3-96fa-3663929fd828
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%
AEC-Q200	No
Typical Component Weight	185 mg
Shelf Life	78 Weeks
MSL	1

Dimensions

L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
T	1.6mm +/-0.20mm
S	2.3mm MIN
B	0.6mm +/-0.35mm
Case Code (EIA / mm)	1812 / 4532

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

Specifications

Capacitance	4.7 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	106.4 MOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.