

# NC2650ZA330A-EV

## Ultra-low Quiescent Current ( $I_{QVIN} = 85\text{nA}$ ) 1A Step-down Switching Regulator

### FEATURES

- Input Voltage Range (Maximum Rating):  
1.8 V to 5.5 V (6.5 V)
- Output Voltage Range:  
0.8 V to 3.3 V (internally fixed)
- Output Voltage Accuracy:  $\pm 1\%$  ( $V_{SET} \geq 1.2\text{ V}$ )  
 $\pm 1.5\%$  ( $V_{SET} < 1.2\text{ V}$ )
- Quiescent Current: Typ. 85 nA
- Efficiency: Typ. 80 %  
( $V_{IN}=3.6\text{V}$ ,  $V_{OUT}=1.8\text{V}$ ,  $I_{OUT}=1\mu\text{A}$ )
- Ripple Voltage: Typ. 10mVpp ( $I_{OUT}=500\text{mA}$ )
- UVLO Detection Voltage: Typ. 1.45 V
- Soft-Start Time: Typ. 170  $\mu\text{s}$
- Thermal Shutdown Function:  
Detection Temperature: Typ. 150 °C  
Release Temperature: Typ. 120 °C
- Auto Discharge Function

### APPLICATIONS

- IoT Edge Terminals
- Devices Powered by Coin/Button/Dry Batteries.
- Alarms, Smart Watches etc.

### GENERAL DESCRIPTION

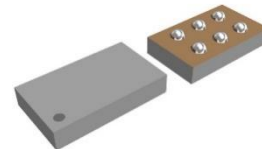
The NC2650 is a synchronous rectification step-down switching regulator using CMOS-based with PWM/PFM automatic alternating function.

This product achieves ultra-low power consumption of only 85 nA in quiescent current, so it is ideal for battery-powered portable devices such as wearable devices and IoT devices.

Because it exhibits high efficiency under light load conditions, it extends battery life in intermittent operation applications and allows the use of a small inductor (0.47 $\mu\text{H}$ ), contributing to space savings.

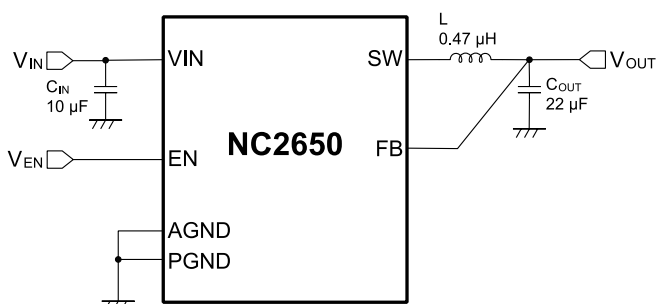
It also achieves low ripple voltage and fast load transient response, providing stable power quality. Shutdown function is possible by switching the EN pin and auto-discharge can be selected, so that enables flexible power supply design.

### PACKAGE

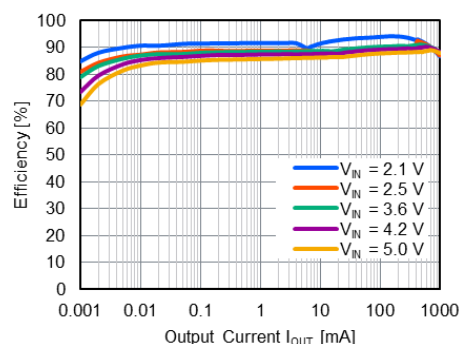


WLCSP-6-ZA2 1.47 x 0.87 x 0.4 (mm)

### TYPICAL APPLICATIONS



### EFFICIENCY TYPICAL CHARACTERISTICS



NC2650ZA  $V_{OUT} = 1.8\text{V}$

## ■PRODUCT NAME INFORMATION

| Product Name | Package     |
|--------------|-------------|
| NC2650ZA330A | WLCSP-6-ZA2 |

## Description of configuration

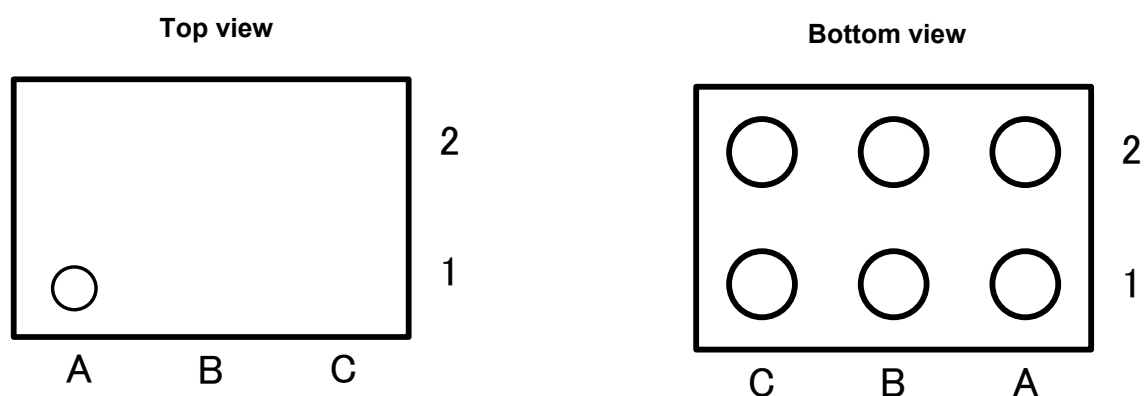
|     | Item           | Description                             |
|-----|----------------|-----------------------------------------|
| 330 | Output Voltage | Set Output Voltage ( $V_{SET}$ ): 3.3 V |

## Version

|   | Auto Discharge Function |
|---|-------------------------|
| A | Yes                     |

- For more details on NC2650 IC, please refer to <https://www.nisshinbo-microdevices.co.jp/en/products/dc-dc-switching-regulator/spec/?product=nc2650>

## ■PIN DESCRIPTIONS (NC2650ZA)



WLCSP-6-ZA2 Pin Configuration

## NC2650ZA (WLCSP-6-ZA2) Pin Descriptions

| Pin No. | Pin Name | I/O   | Description                                                                                                |
|---------|----------|-------|------------------------------------------------------------------------------------------------------------|
| A1      | FB       | I     | Feedback Pin<br>Connect it to the VOUT node as an output voltage feedback pin.                             |
| B1      | SW       | O     | Switching Output Pin<br>Internal MOSFET Drain<br>Connect an inductor between the VOUT node and the SW pin. |
| C1      | VIN      | Power | Power Supply Input Pin                                                                                     |
| A2      | AGND     | -     | Analog GND Pin of Internal Circuit<br>Be sure to connect to the GND pin on the board.                      |
| B2      | EN       | I     | Enable Pin<br>Can set the active state with the "High" input and the shutdown state with the "Low" input.  |
| C2      | PGND     | -     | Power Ground Pin of Internal Circuit<br>Be sure to connect to the GND pin on the board.                    |

## ■ABSOLUTE MAXIMUM RATINGS

|                                    | Symbol    | Ratings     | Unit |
|------------------------------------|-----------|-------------|------|
| VIN pin Voltage                    | $V_{IN}$  | -0.3 to 6.5 | V    |
| FB pin Voltage                     | $V_{FB}$  | -0.3 to 6.5 | V    |
| SW pin Voltage                     | $V_{SW}$  | -0.3 to 6.5 | V    |
| EN pin Voltage                     | $V_{EN}$  | -0.3 to 6.5 | V    |
| Storage Temperature Range          | $T_{stg}$ | -55 to 125  | °C   |
| Junction Temperature <sup>*1</sup> | $T_j$     | -40 to 125  | °C   |

## ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause permanent damage and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

<sup>\*1</sup> Calculate the power consumption of the IC from the operating conditions, and calculate the junction temperature with the thermal resistance.

Please refer to "THERMAL CHARACTERISTICS" for the thermal resistance under our measurement board conditions

## ■RECOMMENDED OPERATING CONDITIONS

|                             | Symbol   | Ratings    | unit |
|-----------------------------|----------|------------|------|
| VIN pin Voltage             | $V_{IN}$ | 1.8 to 5.5 | V    |
| Operating Temperature Range | $T_a$    | -40 to 85  | °C   |
| Junction Temperature Range  | $T_j$    | -40 to 125 | °C   |

## RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

## ■ELECTRICAL CHARACTERISTICS

$V_{IN} = 3.6\text{ V}$  unless otherwise specified.

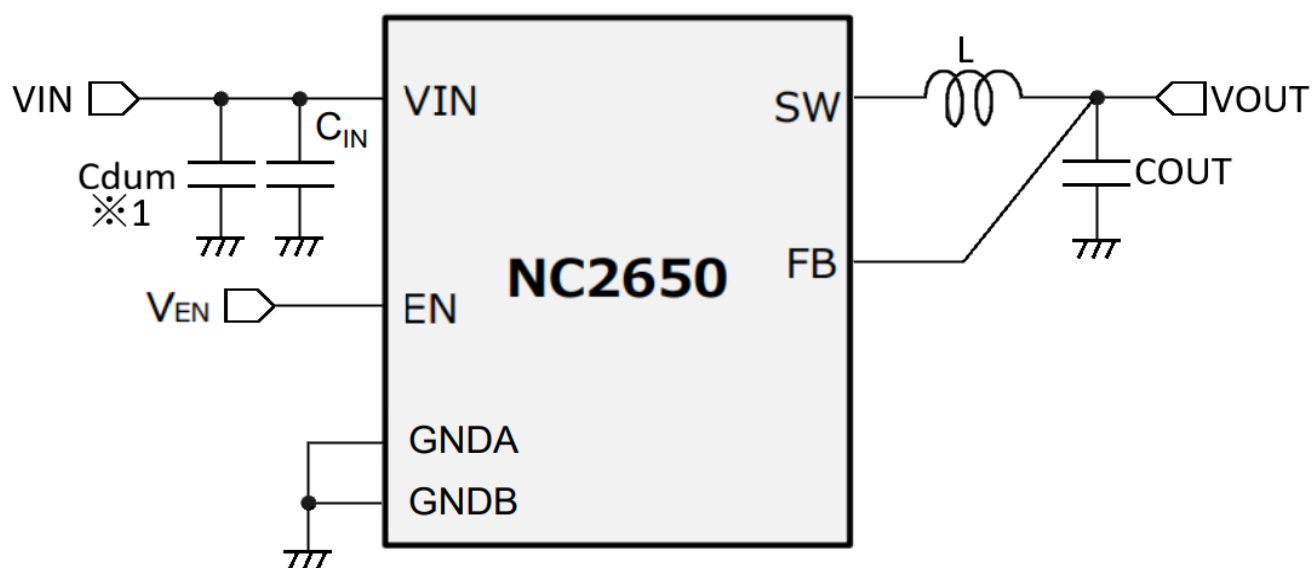
For parameter that do not describe the temperature condition, the MIN / MAX value under the condition of  $-40\text{ }^{\circ}\text{C} \leq T_j \leq 125\text{ }^{\circ}\text{C}$  is described.

| Parameter                              | Symbol          | Conditions                                                                                                                       | MIN            | TYP  | MAX            | Unit               |
|----------------------------------------|-----------------|----------------------------------------------------------------------------------------------------------------------------------|----------------|------|----------------|--------------------|
| Output Voltage                         | $V_{OUTCCM}$    | PWM, $V_{SET} \geq 1.2\text{ V}$ ,<br>$T_j = 25\text{ }^{\circ}\text{C}$                                                         | $\times 0.99$  | -    | $\times 1.01$  | V                  |
|                                        |                 | PWM, $V_{SET} \geq 1.2\text{ V}$ ,<br>$-40\text{ }^{\circ}\text{C} \leq T_j \leq 125\text{ }^{\circ}\text{C}$                    | $\times 0.98$  | -    | $\times 1.02$  | V                  |
|                                        |                 | PWM, $V_{SET} < 1.2\text{ V}$ ,<br>$T_j = 25\text{ }^{\circ}\text{C}$                                                            | $\times 0.985$ | -    | $\times 1.015$ | V                  |
|                                        |                 | PWM, $V_{SET} < 1.2\text{ V}$ ,<br>$-40\text{ }^{\circ}\text{C} \leq T_j \leq 125\text{ }^{\circ}\text{C}$                       | $\times 0.975$ | -    | $\times 1.025$ | V                  |
| VIN pin Quiescent Current              | $I_{QVIN}$      | $V_{OUT} = V_{SET}$ , no switching<br>$T_j = 25\text{ }^{\circ}\text{C}$                                                         | -              | 85   | 170            | nA                 |
| Shutdown Current                       | $I_{SD}$        | $V_{IN} = 1.8\sim 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$<br>$T_j = 25\text{ }^{\circ}\text{C}$                                    | -              | 4    | 100            | nA                 |
| EN "H" Input Current                   | $I_{ENH}$       | $V_{IN} = V_{EN} = 5.5\text{ V}$<br>$-40\text{ }^{\circ}\text{C} \leq T_j \leq 85\text{ }^{\circ}\text{C}$                       | -40            | 0    | 40             | nA                 |
| EN "L" Input Current                   | $I_{ENL}$       | $V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$<br>$-40\text{ }^{\circ}\text{C} \leq T_j \leq 85\text{ }^{\circ}\text{C}$        | -40            | 0    | 40             | nA                 |
| On-resistance for Discharger           | $R_{ONDIS}$     | NC2650xxxxxA                                                                                                                     | -              | 60   | -              | $\Omega$           |
| EN pin "H" Input Voltage               | $V_{ENH}$       | $V_{IN} = 5.5\text{ V}$                                                                                                          | 0.8            | -    | 5.5            | V                  |
| EN pin "L" Input Voltage               | $V_{ENL}$       | $V_{IN} = 1.8\text{ V}$                                                                                                          | 0              | -    | 0.3            | V                  |
| On-resistance of High Side MOSFET      | $R_{ONH}$       | $I_{SW} = 100\text{ mA}$                                                                                                         | -              | 150  | -              | m $\Omega$         |
| On-resistance of Low Side MOSFET       | $R_{ONL}$       | $I_{SW} = 100\text{ mA}$                                                                                                         | -              | 125  | -              | m $\Omega$         |
| Thermal Shutdown Detection Temperature | $T_{SDDET}$     |                                                                                                                                  | -              | 150  | -              | $^{\circ}\text{C}$ |
| Thermal Shutdown Release Temperature   | $T_{SDREL}$     |                                                                                                                                  | -              | 120  | -              | $^{\circ}\text{C}$ |
| High Side Current Limit                | $I_{LIMHS}$     |                                                                                                                                  | 1.4            | 1.8  | 2.2            | A                  |
| High Side Current Limit at Soft-Start  | $I_{LIMHS\_SS}$ | $V_{SET} = 1.8\text{ V}$                                                                                                         | 0.26           | 0.5  | 0.76           | A                  |
| Start-up Delay Time                    | $t_{STARTUP}$   | from EN = low to high until<br>device starts switching<br>$-40\text{ }^{\circ}\text{C} \leq T_j \leq 85\text{ }^{\circ}\text{C}$ | -              | 100  | 250            | $\mu\text{s}$      |
| Soft-Start Time                        | $t_{SS}$        | $I_{OUT} = 0\text{ mA}$ , $V_{set} = 1.8\text{ V}$                                                                               | -              | 170  | -              | $\mu\text{s}$      |
| UVLO Detection Voltage *1              | $V_{UVLODET}$   | $V_{IN} = \text{Falling}$                                                                                                        | 1.35           | 1.45 | 1.55           | V                  |
| UVLO Release Voltage *1                | $V_{UVLOREL}$   | $V_{IN} = \text{Rising}$                                                                                                         | 1.4            | 1.5  | 1.6            | V                  |

All electrical characteristic parameters that specify the minimum and maximum specifications are tested under the condition of  $T_j \approx T_a = 25\text{ }^{\circ}\text{C}$ .

\*1 Due to the circuit configuration,  $V_{UVLODET} \geq V_{UVLOREL}$  does not hold. The hysteresis is Typ.50 mV.

## ■ TYPICAL APPLICATION CIRCUIT



※1 Testing with this EV board, an external attachment might be necessary for evaluation of the correct performance of the NC2650 and already has been attached as Cdum.

For evaluation, wiring for power supply or GND will be used. Considering the voltage drop or noise by the wiring, Cdum has been mounted on the EV board to obtain the right performance of the NC2650 in the actual PCB layout or measurement unit's wire is very short, and Cdum will be unnecessary.

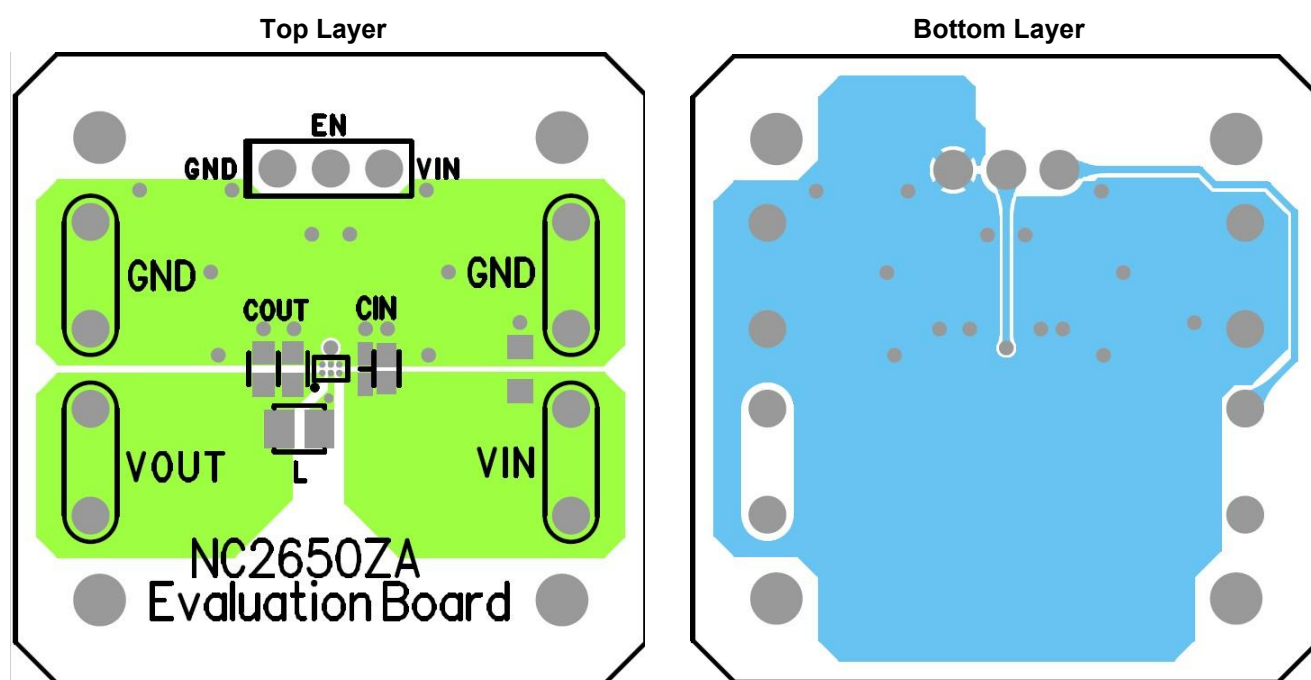
## Recommended external parts

| Symbol   | Capacitance  |
|----------|--------------|
| CIN      | 10 $\mu$ F   |
| COUT     | 22 $\mu$ F   |
| L        | 0.47 $\mu$ H |
| Cdum(※1) | 22 $\mu$ F   |

The bill of materials will be attached on the shipment of each purchased evaluation board.

## ■Evaluation Board / PCB Layout Pattern Example

NC2650ZA(WLCSP-6-ZA2)



When designing PCB layout patterns, pay close attention to the following items.

External components should be placed as close as possible to the IC and on the same side, and the wiring from the IC to the components should be short. In particular, the capacitor CIN connected between VIN and PGND and the capacitor COUT connected between FB and AGND should be placed at the shortest possible distance from the terminals.

Current due to switching flows through the power supply wiring, ground wiring and SW pins. If the impedance of the power and ground wiring is high, the potential inside the IC may fluctuate due to the switching current, causing the IC to operate unstable. Therefore, the power and ground wiring should be short and thick.

The wiring from the SW pin to the inductor is a noise source, so ensure that the current capacity is sufficient to prevent the noise from increasing and that the wiring is not thicker and longer than necessary.



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