



Mini-Circuits

FLAT GAIN, HIGH DYNAMIC RANGE

Monolithic Amplifier

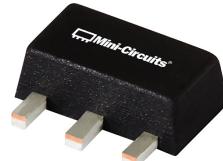
PGA-106R-75+

75Ω

5 to 250 MHz

THE BIG DEAL

- High IP3, +34 dBm Typ.
- Gain, 17.9 dB Typ.
- High P1dB +19.5 dBm Typ.

*Generic photo used for illustration purposes only*

CASE STYLE: DF782

+RoHS Compliant

The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

APPLICATIONS

- CATV Return Path

PRODUCT OVERVIEW

PGA-106R-75+ (RoHS compliant) is an advanced 75Ω amplifier fabricated using E-pHEMT technology and offers extremely high dynamic range over a broad frequency range with low noise figure and flat gain. Lead finish is SnAgNi. It has repeatable performance from lot to lot and is enclosed in a SOT-89 package for very good thermal performance.

KEY FEATURES

Feature	Advantages
Broad Band: 5 to 250 MHz	Broadband covering CATV return path applications
High IP3 Versus DC Power Consumption: +34 dBm Typical	The PGA-106R-75+ matches industry leading IP3 performance relative to device size and power consumption. The combination of the design and E-pHEMT structure provides enhanced linearity over a broad frequency range as evidenced in the IP3 being typically 15 dB above the P1dB point. This feature makes this amplifier ideal for use in CATV applications.
High IP2, +62 dBm	Suppresses second order product on wideband applications such as CATV
Low Noise Figure: 3.1 dB at 250 MHz 6.0 dB at 10 MHz	Low noise figure performance in combination with the high output IP3 results in high dynamic range.

REV. C
ECO-024506
PGA-106R-75+
MCL NY
250212

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Monolithic Amplifier **PGA-106R-75+**

75Ω 5 to 250 MHz

ELECTRICAL SPECIFICATIONS¹ AT +25°C, 75Ω AND +5.0 V, UNLESS NOTED OTHERWISE

Parameter	Condition (MHz)	Min.	Typ.	Max.	Units
Frequency Range		5		250	MHz
Gain	5 - 250	16.3	17.9	19.9	dB
Gain Flatness (±)	5 - 250		0.1		dB
Input Return Loss	5 - 250		15.3		dB
Output Return Loss	5 - 250		19.0		dB
Reverse Isolation	5 - 250		23.4		dB
Output Power @ 1 dB compression	5 - 250		+19.5		dBm
Output IP3	5 - 250		+34.4		dBm
Output IP2 ²	5 - 250		+62.0		dBm
	10		6.0		
Noise Figure	50		3.3		
	250		3.1		
Device Operating Voltage (V _D)		+4.8	+5.0	+5.2	V
Device Operating Current			116	132	mA
Device Current Variation vs. Temperature ³			97		µA/°C
Device Current Variation vs. Voltage			0.05		mA/mV
Thermal Resistance, Junction-to-Ground Lead			76		°C/W

1. Measured on Mini-Circuits Characterization Test board TB-587+. See Characterization Test Circuit (Fig. 1).

2. Output IP2 measured at sum frequency of the two tones (f_{meas} = f₁ + f₂).

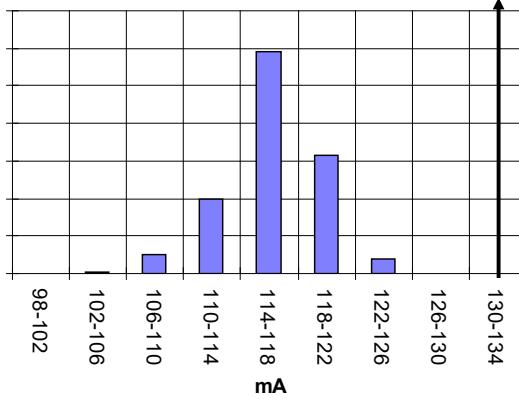
3. (Current at +85°C - Current at -45°C)/130

ABSOLUTE MAXIMUM RATINGS⁴

Parameter	Ratings
Operating Temperature (Ground Lead)	-40°C to +85°C
Storage Temperature	-65°C to +150°C
Operating Current at +5.0 V	170 mA
Power Dissipation	0.85 W
Input Power (CW)	+23 dBm (5 minutes) +14 dBm (continuous)
DC Voltage on Pin 3	+6 V

4. Permanent damage may occur if any of these limits are exceeded.

Electrical maximum ratings are not intended for continuous normal operation.

HISTOGRAM



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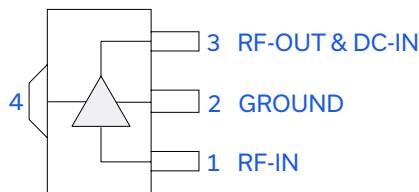
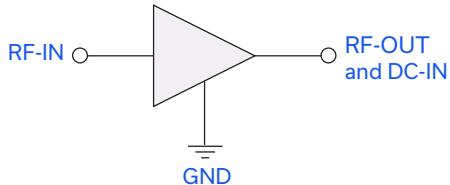
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PGA-106R-75+

75Ω

5 to 250 MHz

SIMPLIFIED SCHEMATIC AND PIN DESCRIPTION



Function	Pin Number	Description
RF-IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit", Fig. 2.
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.

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CHARACTERIZATION TEST CIRCUIT

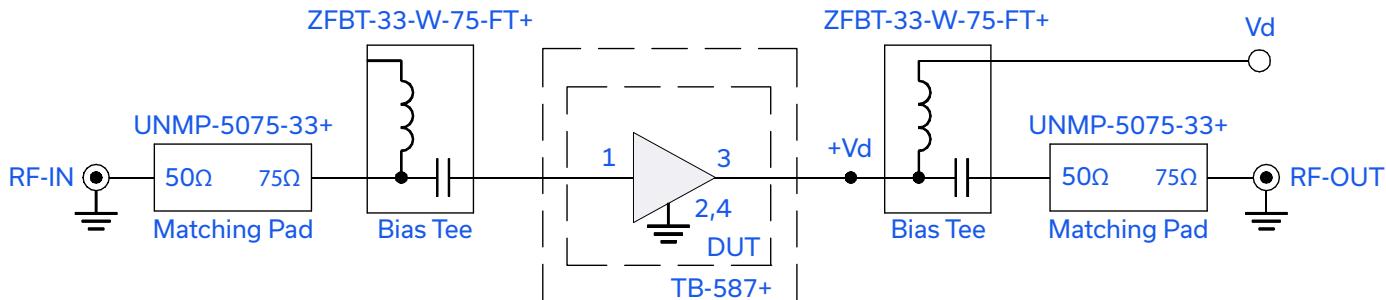


Fig 1. Block Diagram of Test Circuit used for characterization. (DUT tested on Mini-Circuits Characterization test board TB-587+)
 Gain, Return Loss, Output Power at 1 dB Compression (P1dB), Output IP3 (OIP3), Output IP2 (OIP2) and Noise Figure measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:

1. Gain and Return loss: $P_{IN} = -25$ dBm
2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, +5 dBm/tone at output.
3. Output IP2 (OIP2): Two tones, spaced 1 MHz apart, +5 dBm/tone at output.

RECOMMENDED APPLICATION CIRCUIT

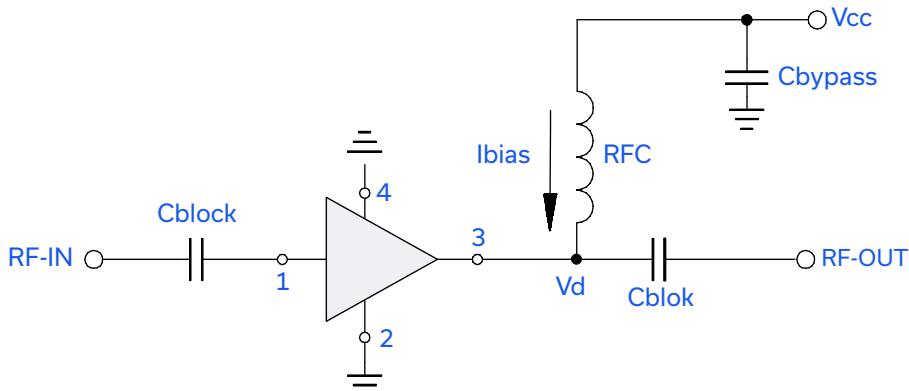
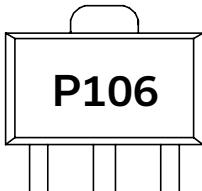


Fig 2. Evaluation board includes case, connectors, and components soldered to PCB

PRODUCT MARKING



Marking may contain other features or characters for internal lot control.

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ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASHBOARD. [CLICK HERE](#)

Performance Data	Data Table Swept Graphs S-Parameter (S2P Files) Data Set (.zip file)
Case Style	DF782 (SOT 89) Plastic package, exposed paddle lead finish: tin-silver over nickel
Tape & Reel Standard Quantities Available on Reel	F55 7" Reels with 20, 50, 100, 200, 500 or 1K devices
Suggested Layout for PCB Design	PL-384
Evaluation Board	TB-699+
Environmental Ratings	ENV08T1

ESD RATING

Human Body Model (HBM): Class 1B (500 <1000 V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class class M1 (50 V) in accordance with ANSI/ESD STM5.2-1999

MSL RATING

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

NOTES

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
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