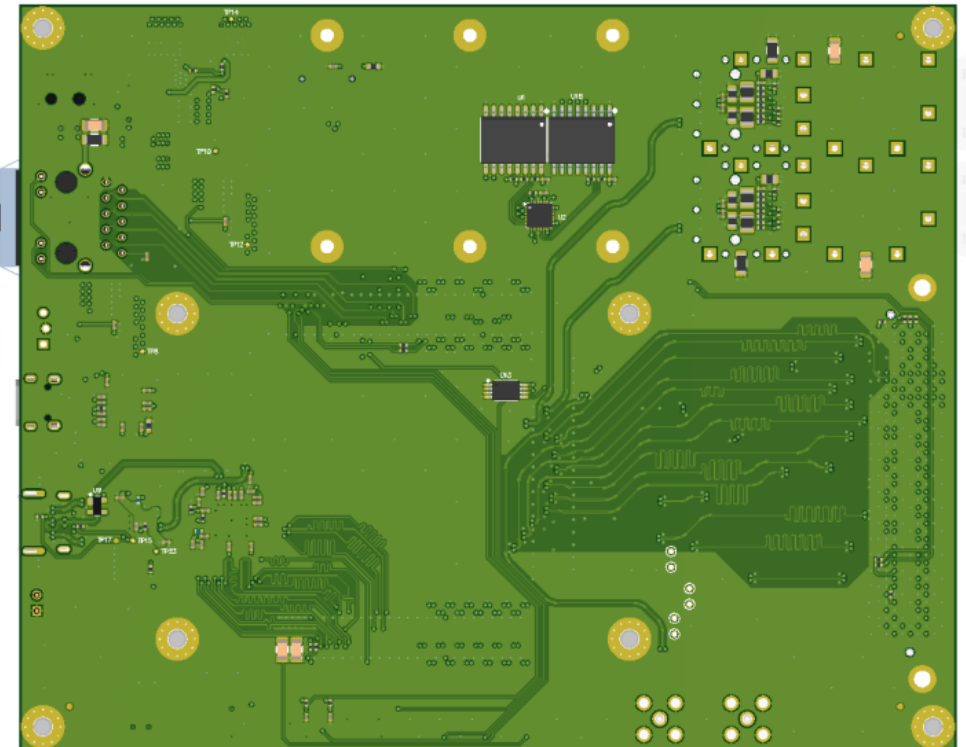
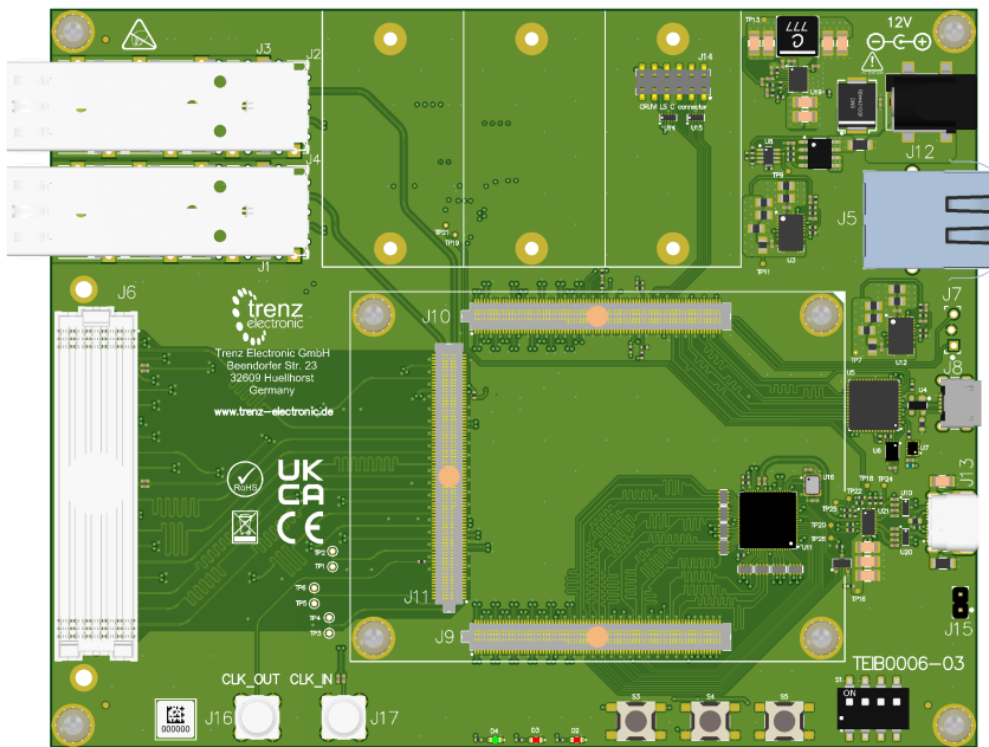


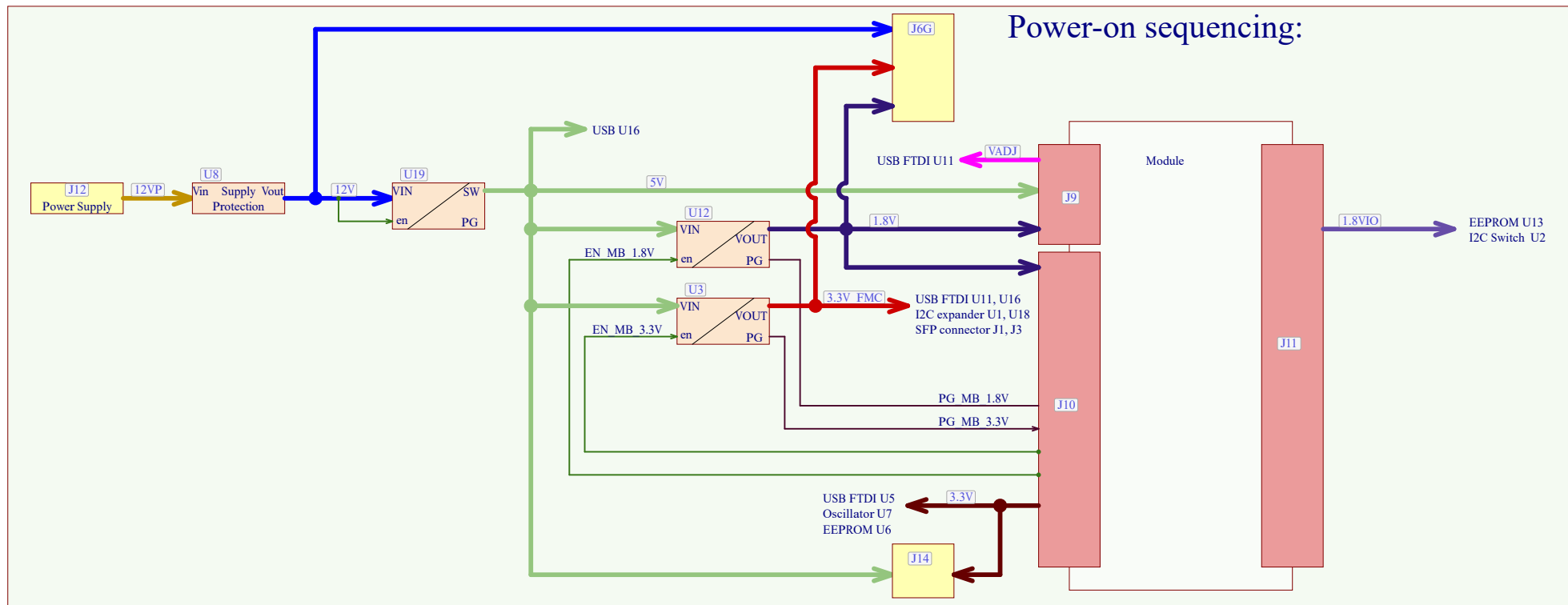


Regarding the usage of our schematics and alike documentation for Trenz baseboard TEIB0006 .
Baseboard is Open Source Hardware and therefore free to use, adapt and can be modified to suit your needs as described in the "The MIT License".
Permission is hereby granted, free of charge, to any person obtaining a copy of this hardware and associated documentation files (the "Product"), to deal in the Product without restriction, including without limitation the rights to use, copy, modify, merge, publish, distribute, sublicense, and/or sell copies of the Product, and to permit persons to whom the Product is furnished to do so.
Schematics and other handouts serve for informational purposes only!

Drawn by IG/VG Checked by VT			Title: TEIB0006 – Legal Notices			
Assembly variant A Created by VT Modified by IG Modified at 2025-02-07			A4	Number: TEIB0006 A		Rev. 03
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REV	Description	
-01	Initial revision	VT
-02	1. Remove R60,R61 (pull-up for DEVCLRN,NSTATUS -> set on micromodule) 2. Remove CLKUSR from J2, remove J15 3. Changed 1.8V -> VADJ (connector J2 pins 140,142; U11; R78;R79) 4. Changed 1.8V -> 1.8VIO (U2) 5. Removed Battery holder, added optional 2-pin header for Battery 6. Removed pin headers J7/J14, added optional CRUVI (J14) 7. Changed DCDC (LMZ23605TZ/NOPB -> LTM4638EY#PBF) 8. Full upd LIB	VT (10.2019)
-03	1. DCDC [U3], [U12] EN6340QI (EOL) regulators have been replaced by MPM3860GQW-Z. 2. [U19] LTM4638EY#PBF replaced by MPQ8633BGLE-Z. 3. Diode [U10], [U14], [U15] AOZ8808DI-03 (NRND) are replaced with AOZ8S311UD4-03. 4. FUSE [F1] is EOL and replaced. 5. Resistors [R21], [R24] are changed with 330 Ohms. 6. The USB Type C shield [J13] connected to GND in accordance with the USB-C standard. [R82] is changed with 0 Ohm. 7. Added USB Switch [U21]. 8. Added diode [U20]. 9. Clock generator [U16] is replaced with quartz. 10. Replaced power Jack [J12]. 11. Added series resistors R83, R84, R86-R90, R94 for impedance matching. 12. Oszillator [U7] SiT8008AI-73-XXS-12.000000E is EOL and replaced with SiT8008BI-73-XXS-12.000000E. 13. Added additional capacitors [C107], [C109] to [S3], [S4]. 14. Added additional capacitors [C111], [C112] and resistors [R110], [R111] to [J2], [J4]. 15. Added additional capacitors [C114], [C116] to CRUVI LS [J14]. 16. Added additional capacitors [C120], [C121]. 17. Induktoren [L1] - [L4] are changed with 4.7 µH. 18. Added capacitor [C106] to filter the power supply. 19. Added test points [TP7] - [TP26]. 20. Page "USB30" renamed to "USB30 to DATA". 21. Added page "USB30_CONN_SWITCH". 22. Track-it™ Traceability Pad is removed (EOL). 23. Added legal notices, power overview and system overview. 24. Updated company address on PCB. 25. Added UKCA logo, ESD protection logo.	IG/VG (02.2025)

		Title: TEIB0006 - Revision Changes	
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Supported Voltage Ranges:

Power Rail	Direction	Range	Tolerance	Description	Note
12VP	IN	12V	+/-5%	Micromodule Power	J12
12V	IN	12V	+/-5%	Power supply for external use	J6
5V	OUT	5V	+/-3%	Power supply for external use	J9, J14
3.3V_FMC	IN/OUT	3.3V	+/-3%	Power supply for external use	J6
3.3V	OUT	3.3V	+/-3%	Power supply for external use	J10, J14
1.8V	OUT	1.8V	+/-3%	Power supply for external use	J9, J10, J6
1.8VIO	OUT	1.8V	+/-3%	Micromodule Power	J11
VADJ	OUT	1.8V	+/-3%	Micromodule Power	J9

Legend:

3.3V	Net name	PL_DCIN	B2B Connector	0.9V	Connector
→	Power bus				
→	Control signal				
		PL_DCIN 0.9V	On-Board Power Supply Device		



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The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.

A

A

B

B

C

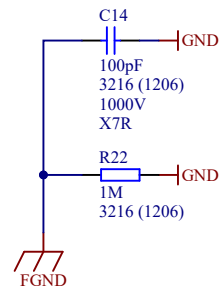
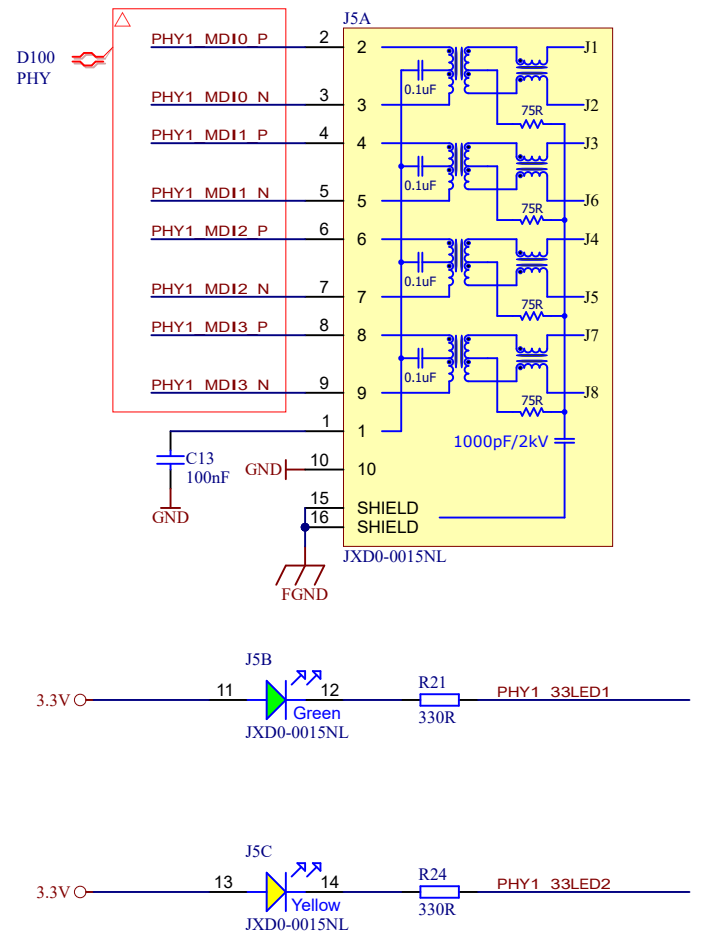
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Title: TEIB0006 - SFP		
A4	Number: TEIB0006 A	Rev. 03
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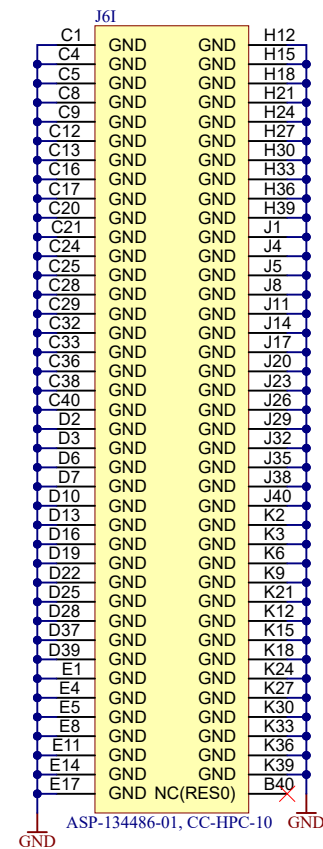
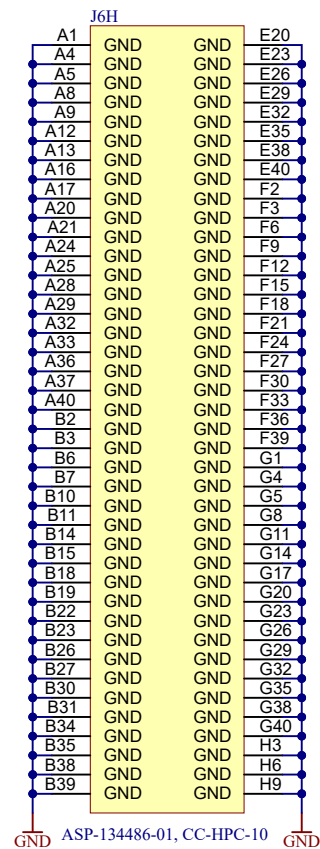
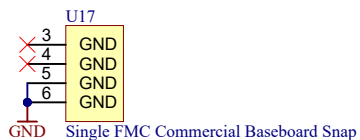
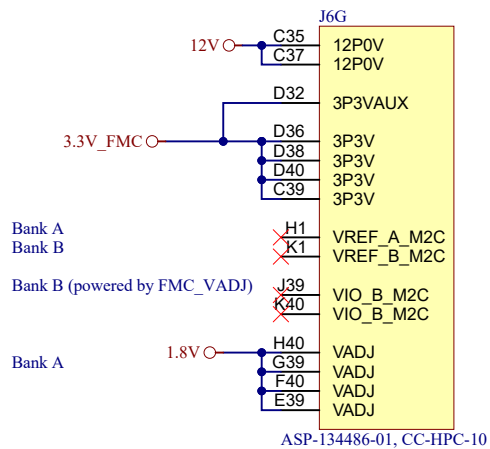
D

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Title: TEIB0006 - FMC PWR			
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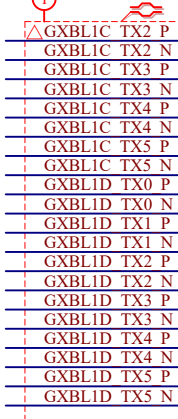
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FMC_DP



D100

J6A

C2

C3

A22

A23

A26

A27

A30

A31

A34

A35

A38

A39

B36

B37

B32

B33

B28

B29

B24

B25

C6

C7

A2

A3

A6

A7

A10

A11

A14

A15

A18

A19

B16

B17

B12

B13

B8

B9

B4

B5

DP0_C2M_P

DP0_C2M_N

DP1_C2M_P

DP1_C2M_N

DP2_C2M_P

DP2_C2M_N

DP3_C2M_P

DP3_C2M_N

DP4_C2M_P

DP4_C2M_N

DP5_C2M_P

DP5_C2M_N

DP6_C2M_P

DP6_C2M_N

DP7_C2M_P

DP7_C2M_N

DP8_C2M_P

DP8_C2M_N

DP9_C2M_P

DP9_C2M_N

DP0_M2C_P

DP0_M2C_N

DP1_M2C_P

DP1_M2C_N

DP2_M2C_P

DP2_M2C_N

DP3_M2C_P

DP3_M2C_N

DP4_M2C_P

DP4_M2C_N

DP5_M2C_P

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DP7_M2C_P

DP7_M2C_N

DP8_M2C_P

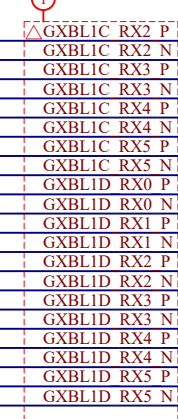
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DP9_M2C_P

DP9_M2C_N

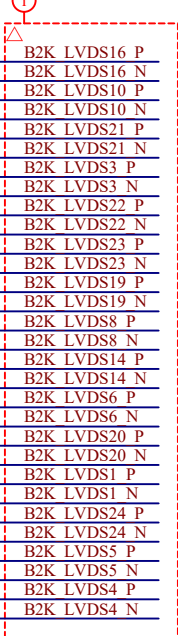
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FMC_DP



D100

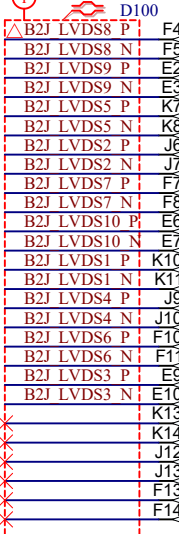
LA



D100

ASP-134486-01, CC-HPC-10

HA



J6C

Bank A

HA00_P_CC

HA00_N_CC

HA01_P_CC

HA01_N_CC

HA02_P

HA02_N

HA03_P

HA03_N

HA04_P

HA04_N

HA05_P

HA05_N

HA06_P

HA06_N

HA07_P

HA07_N

HA08_P

HA08_N

HA09_P

HA09_N

HA10_P

HA10_N

HA11_P

HA11_N

HA12_P

HA12_N

HA13_P

HA13_N

HA14_P

HA14_N

HA15_P

HA15_N

HA16_P

HA16_N

HA17_P_CC

HA17_N_CC

HA18_P

HA18_N

HA19_P

HA19_N

HA20_P

HA20_N

HA21_P

HA21_N

HA22_P

HA22_N

HA23_P

HA23_N

HA23_P

HA23_N

HA23_P

HA23_N

HA23_P

HA23_N

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HA23_P

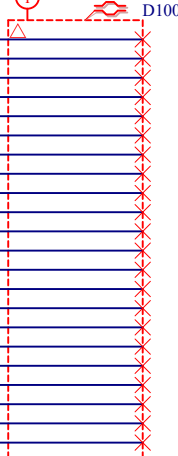
HA23_N

HA23_P

HA23_N

ASP-134486-01, CC-HPC-10

HA



J6C

Bank A

HA00_P_CC

HA00_N_CC

HA01_P_CC

HA01_N_CC

HA02_P

HA02_N

HA03_P

HA03_N

HA04_P

HA04_N

HA05_P

HA05_N

HA06_P

HA06_N

HA07_P

HA07_N

HA08_P

HA08_N

HA09_P

HA09_N

HA10_P

HA10_N

HA11_P

HA11_N

HA12_P

HA12_N

HA13_P

HA13_N

HA14_P

HA14_N

HA15_P

HA15_N

HA16_P

HA16_N

HA17_P_CC

HA17_N_CC

HA18_P

HA18_N

HA19_P

HA19_N

HA20_P

HA20_N

HA21_P

HA21_N

HA22_P

HA22_N

HA23_P

HA23_N

HA23_P

HA23_N

HA23_P

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HA23_P

HA23_N

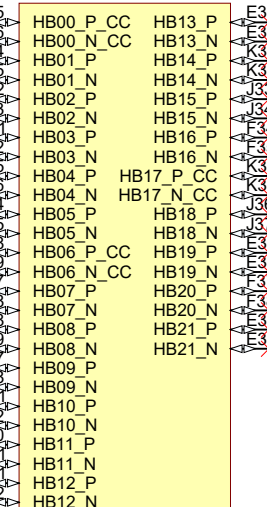
HA23_P

HA23_N

HA23_P

HA23_N

J6D Bank B



ASP-134486-01, CC-HPC-10



Title:

TEIB0006 - FMC

A4

Number:

TEIB0006

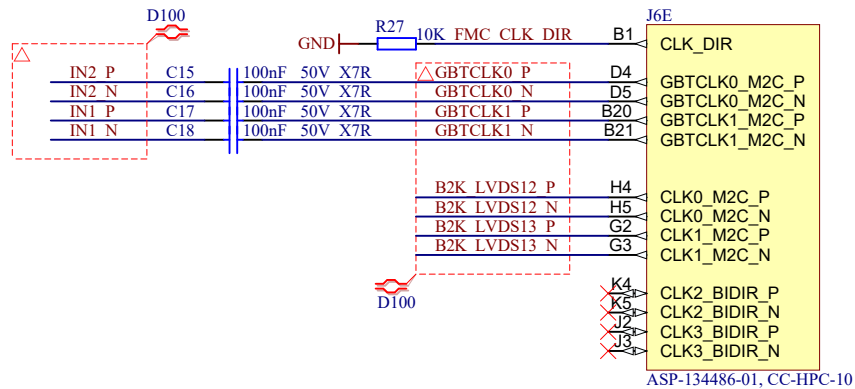
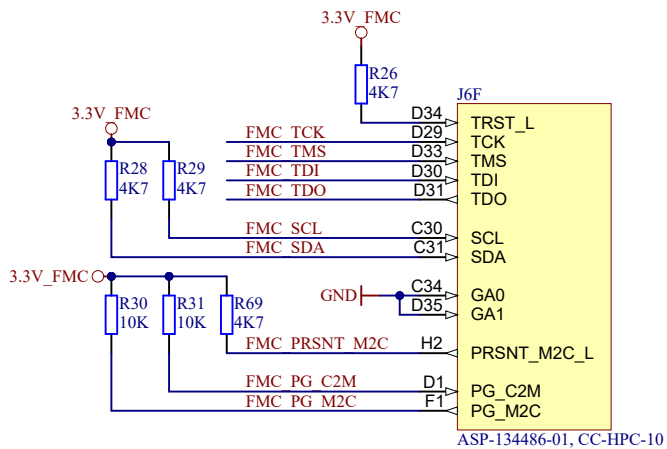
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Rev.

03

Date:

2025-03-05



	Title: TEIB0006 - FMC_MISC		
	A4	Number: TEIB0006 A	Rev. 03
	Date: 2025-03-05	Copyright: Trenz Electronic GmbH	Page 12 of 16
	Filename: FMC_MISC.SchDoc		

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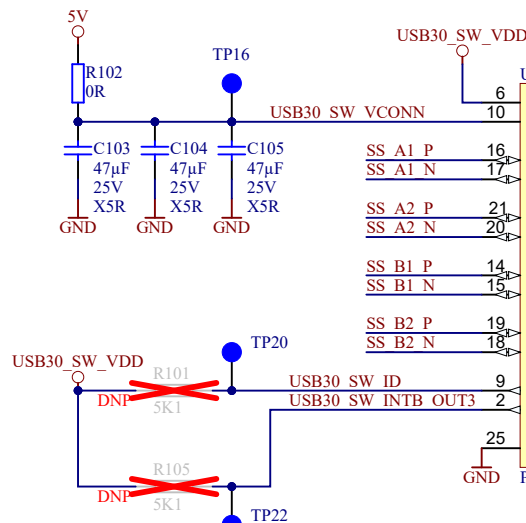
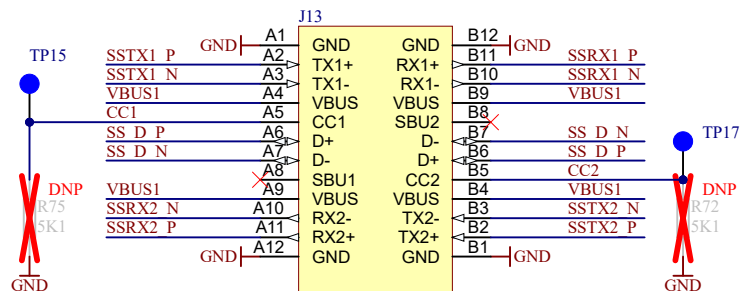
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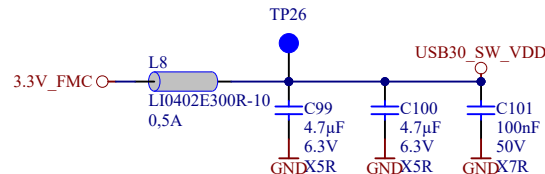
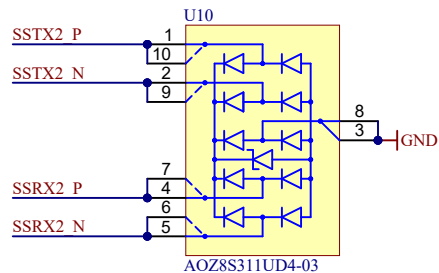
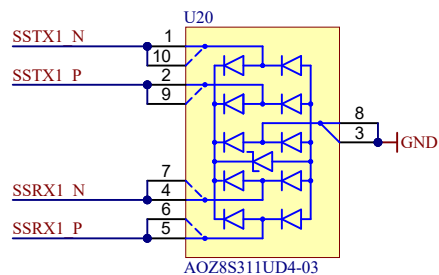
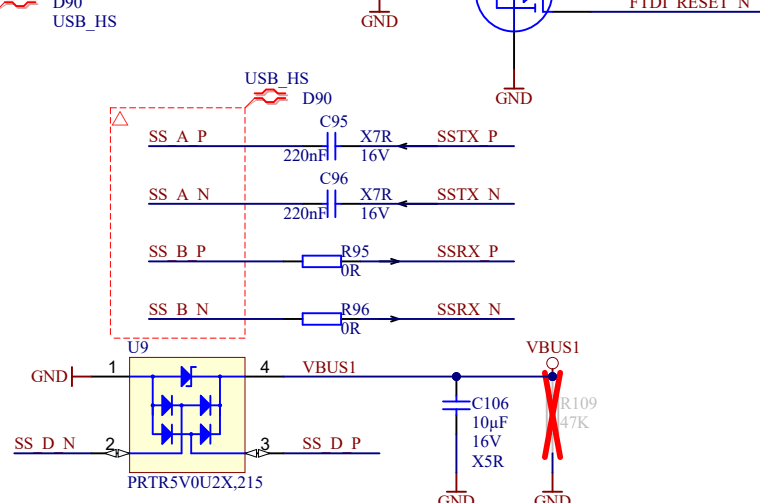
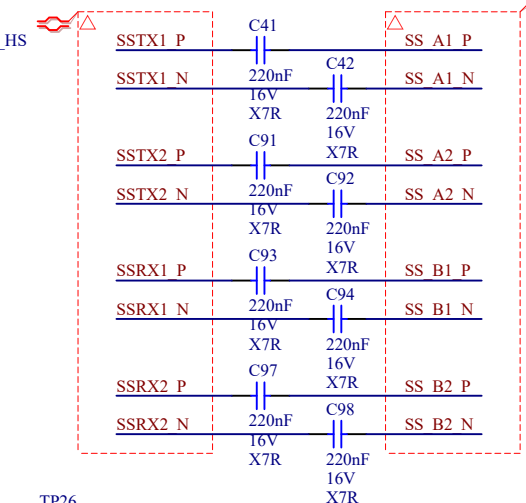
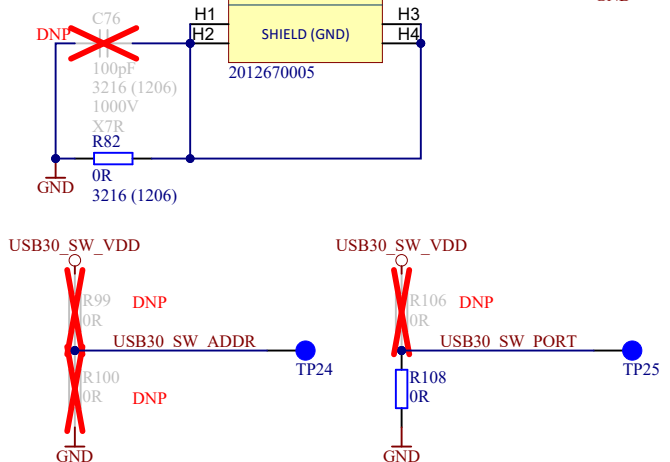
C

D

D



ADDR	I	Tri-level input pin to indicate I ² C address or pin control mode: ADDR is floating - Pin control mode; ADDR=VDD - I ² C enabled with ADDR bit 6 equal to 1; ADDR=GND - I ² C enabled with ADDR bit 6 equal to 0
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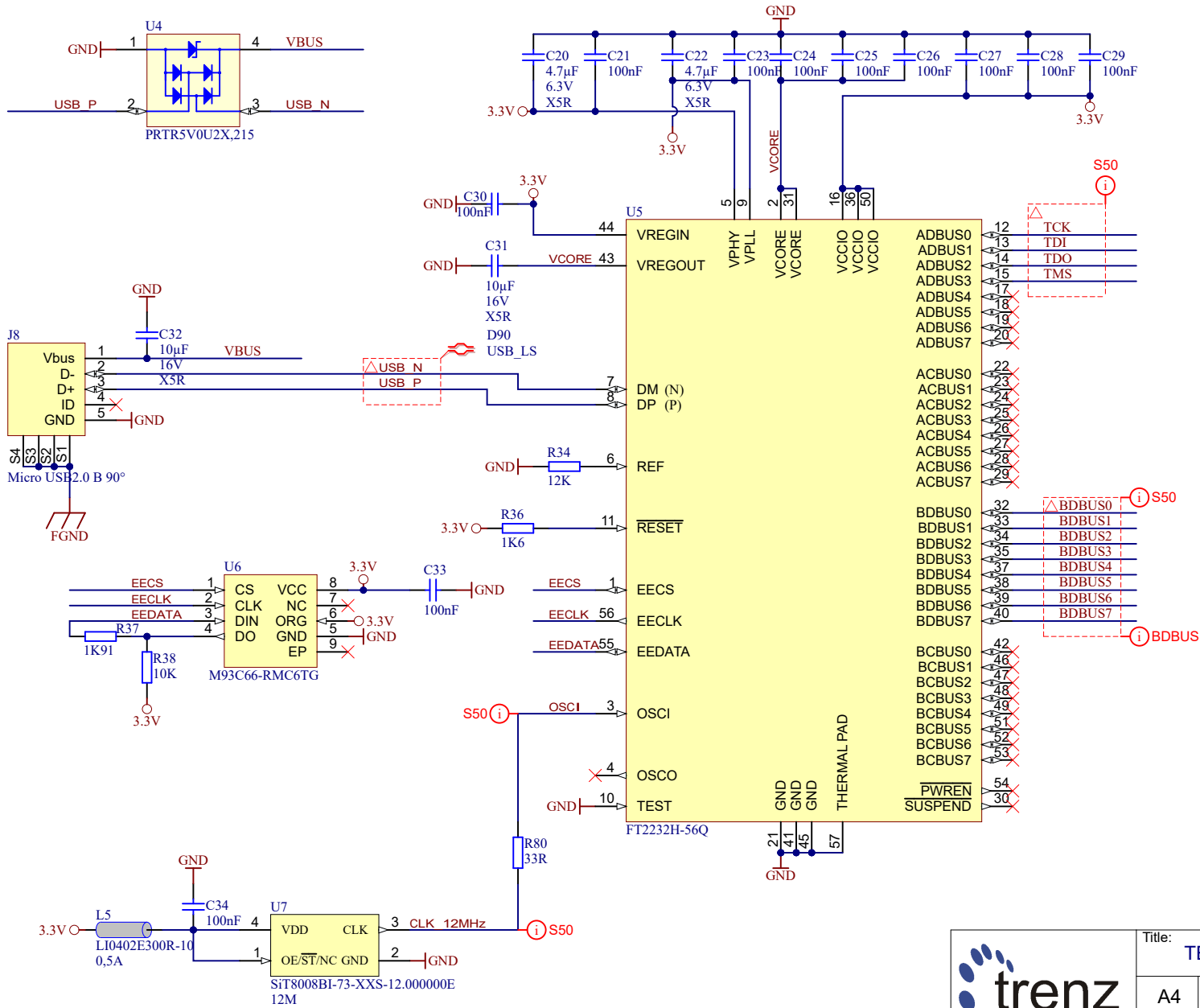
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Date: 2025-03-05	Copyright: Trenz Electronic GmbH	Page 13 of 16
Filename: USB30_CONN_SWITCH.SchDoc		

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A4	Number: TEIB0006 A	Rev. 03
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Filename: FTDI.SchDoc		Page 15 of 16

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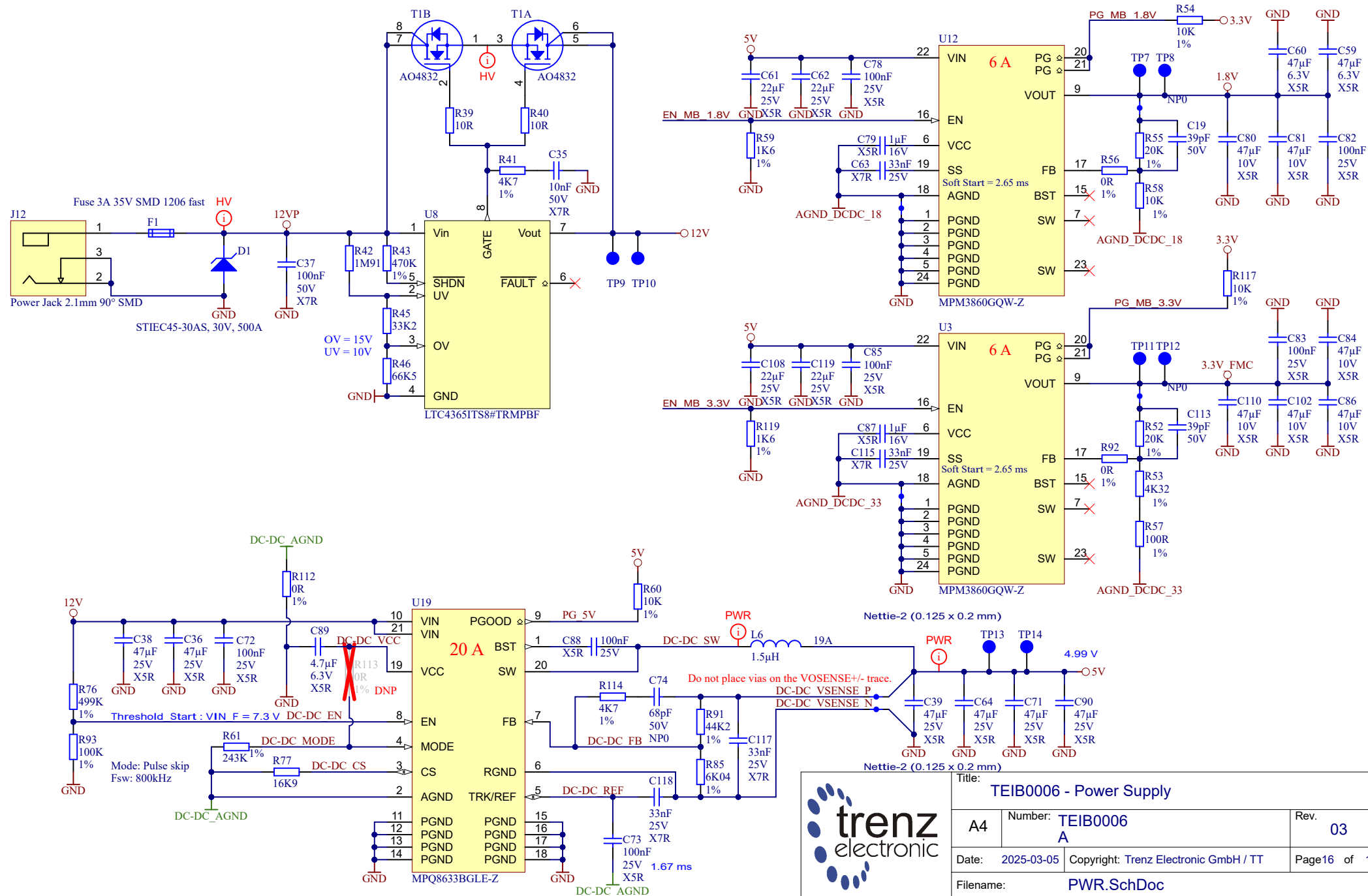
B

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C

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D



Title: TEIB0006 - Power Supply			
A4	Number: TEIB0006 A	Rev. 03	
Date: 2025-03-05	Copyright: Trenz Electronic GmbH / TT		Page 16 of 16
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