

1

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U_B2B B2B.schdoc	U_DP_PS DP_PS.schdoc
U_USB3-HUB USB3-HUB.SchDoc	U_SATA SATA.schdoc
U_PCl_e PCl_e.schdoc	U_USB-CONN USB-CONN.schdoc
U_SFP SFP.schdoc	U_USB USB.schdoc
U_SC SC.schdoc	U_USB-PHY USB-PHY.SchDoc
U_SC2 SC2.schdoc	U_ETH-PHY ETH-PHY.SchDoc
U_PMOD PMODE.schdoc	U_I2C_system I2C_system.schdoc
U_XMOD XMOD.schdoc	U_CLK CLK.schdoc
U_Power Power.SchDoc	U_CLK_MUX CLK_MUX.schdoc
U_SD SD.schdoc	U_FMC PWR FMC PWR.SchDoc
U_EMMC eMMC.SchDoc	U_FMC FMC.SchDoc
U_FMC MISC FMC_MISC.SchDoc	U_BD BD.SchDoc
U_RC Revision_Changes.SchDoc	U_Audio Audio.SchDoc
U_MISC MISC.schdoc	U_FIREFLY FireFly.SchDoc

U_PowerOverview
PowerOverview.SchDoc

LOGO1

TE Logo PRINT Layer

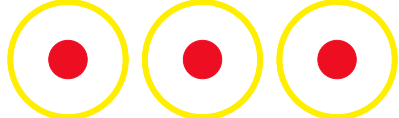
LOGO PRINT
MECH5

TE Address Overlay

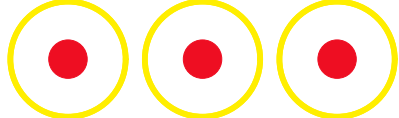
Serial
Serial
Serialnumber 6,3 x 6.3mm

LOGO ADDRESS

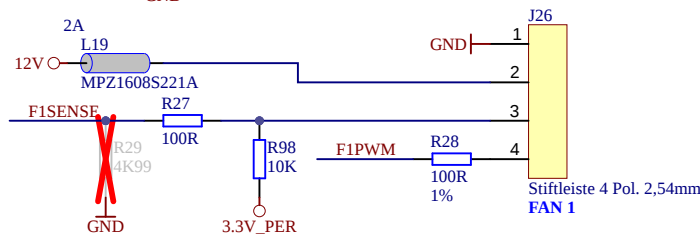
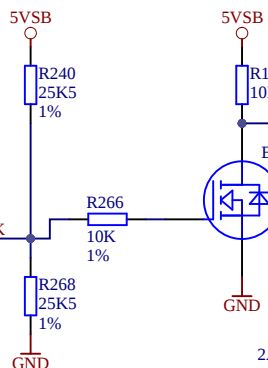
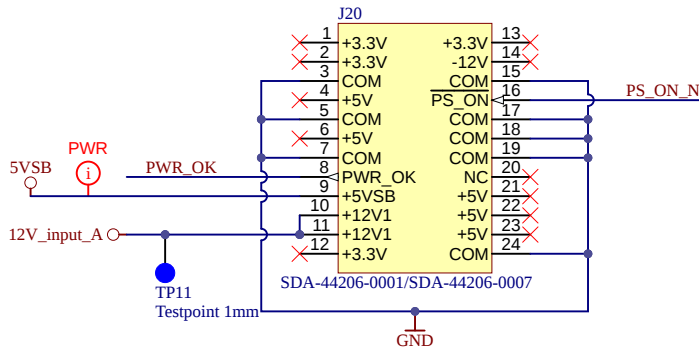
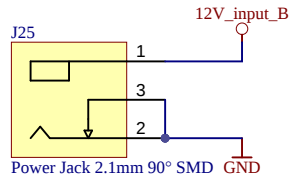
FIDU-DOT - mini FIDU-DOT - mini FIDU-DOT - mini



PM3 PM4 PM2
FIDU-DOT - mini FIDU-DOT - mini FIDU-DOT - mini

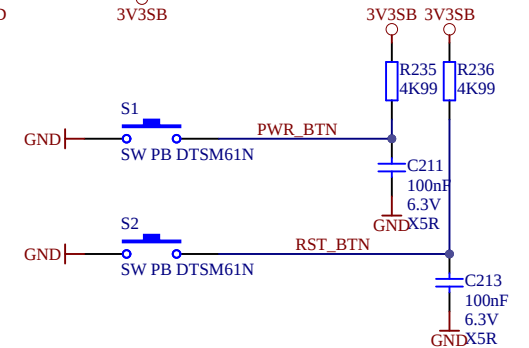
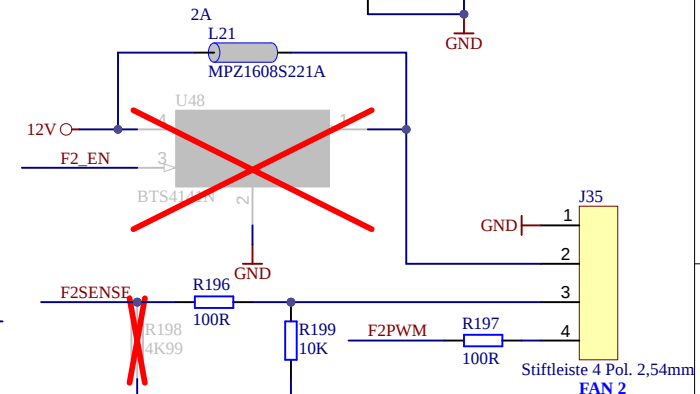
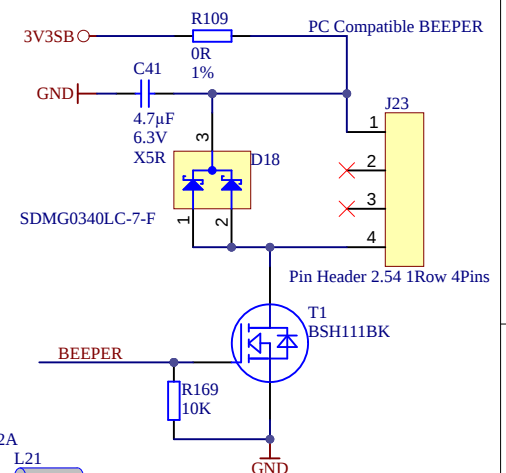
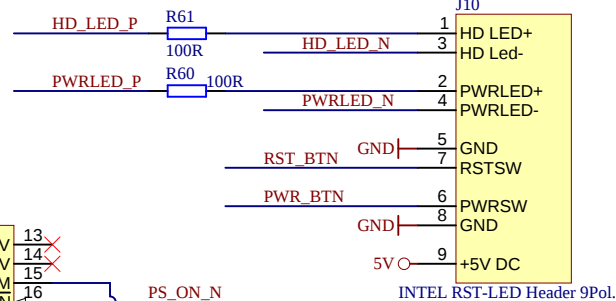


PM5 PM6 PM1



UKCA
UKCA Logo on Top Overlay
UKCA-TOPOVERLAY

INTEL Reset/LED Pinout



Assembly variant	Default
Created by	VT
Modified by	VT
Modified at	2021-08-04
SVN Revision	Unknown revision



Title:
TEBF0818 - Mini-ITX Top Sheet

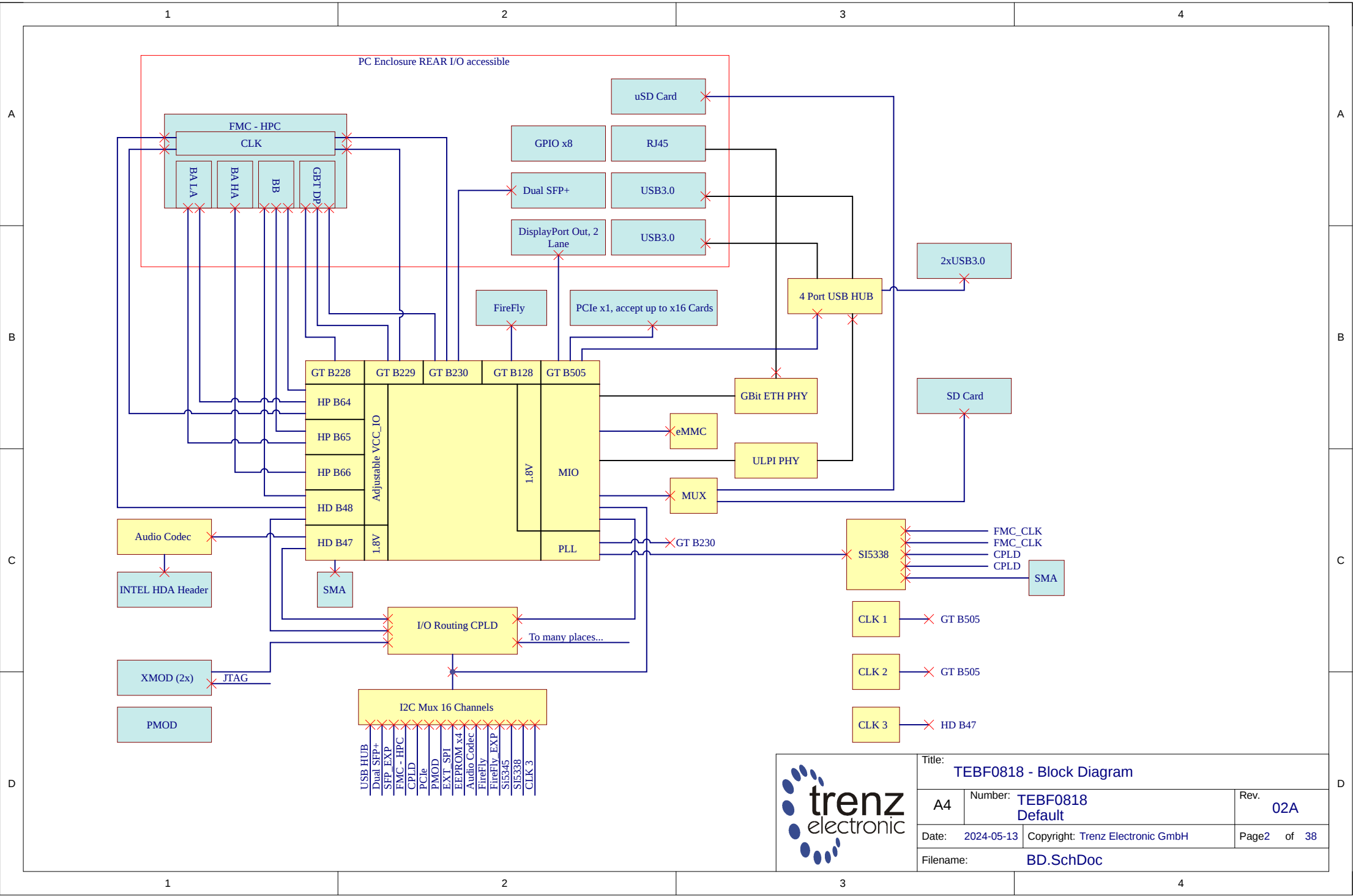
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page1 of 38
Filename:	TEBF0818.schdoc	

1

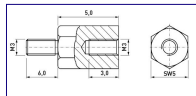
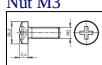
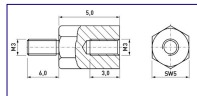
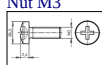
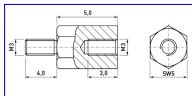
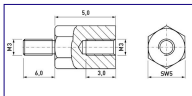
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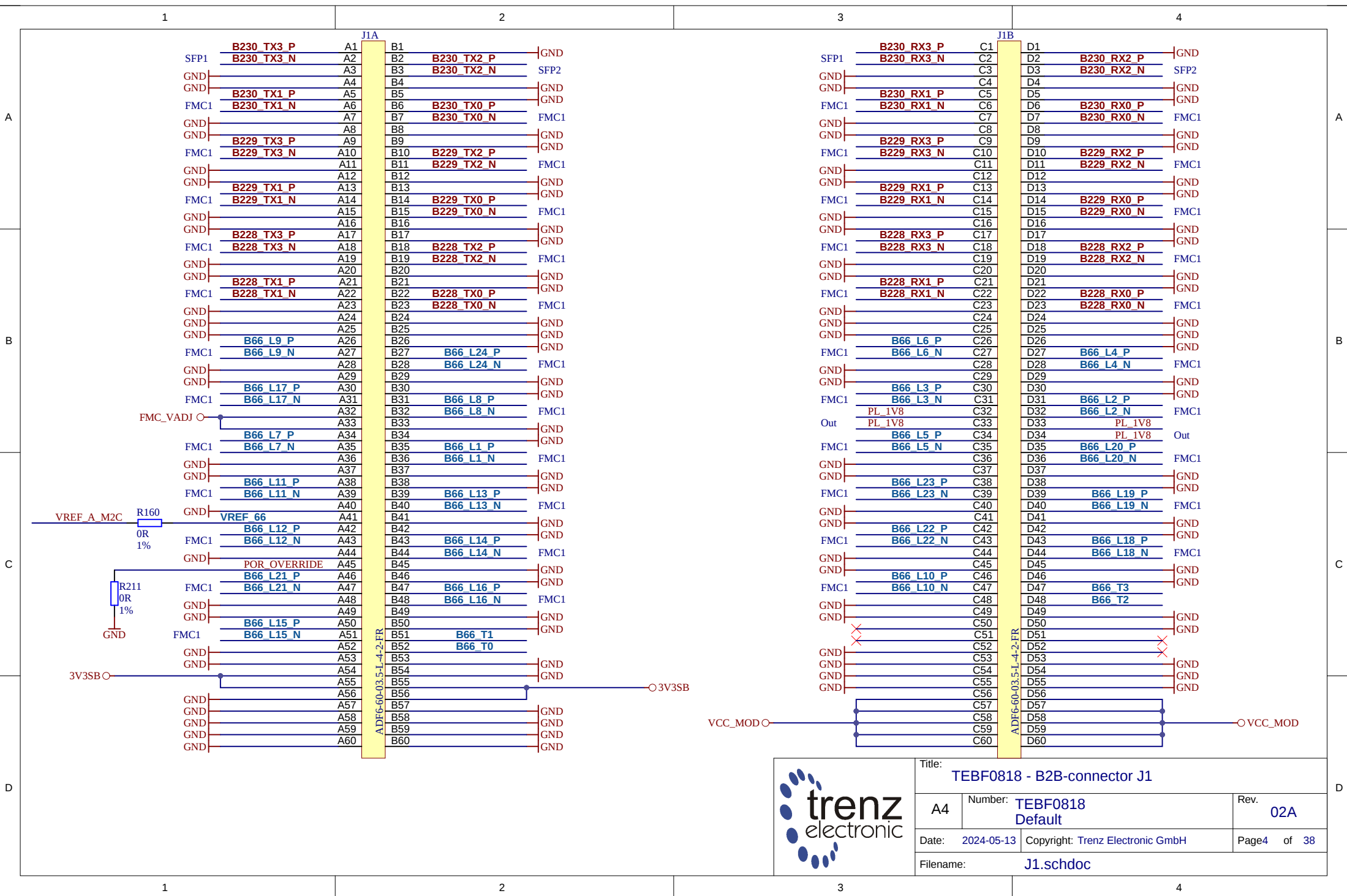
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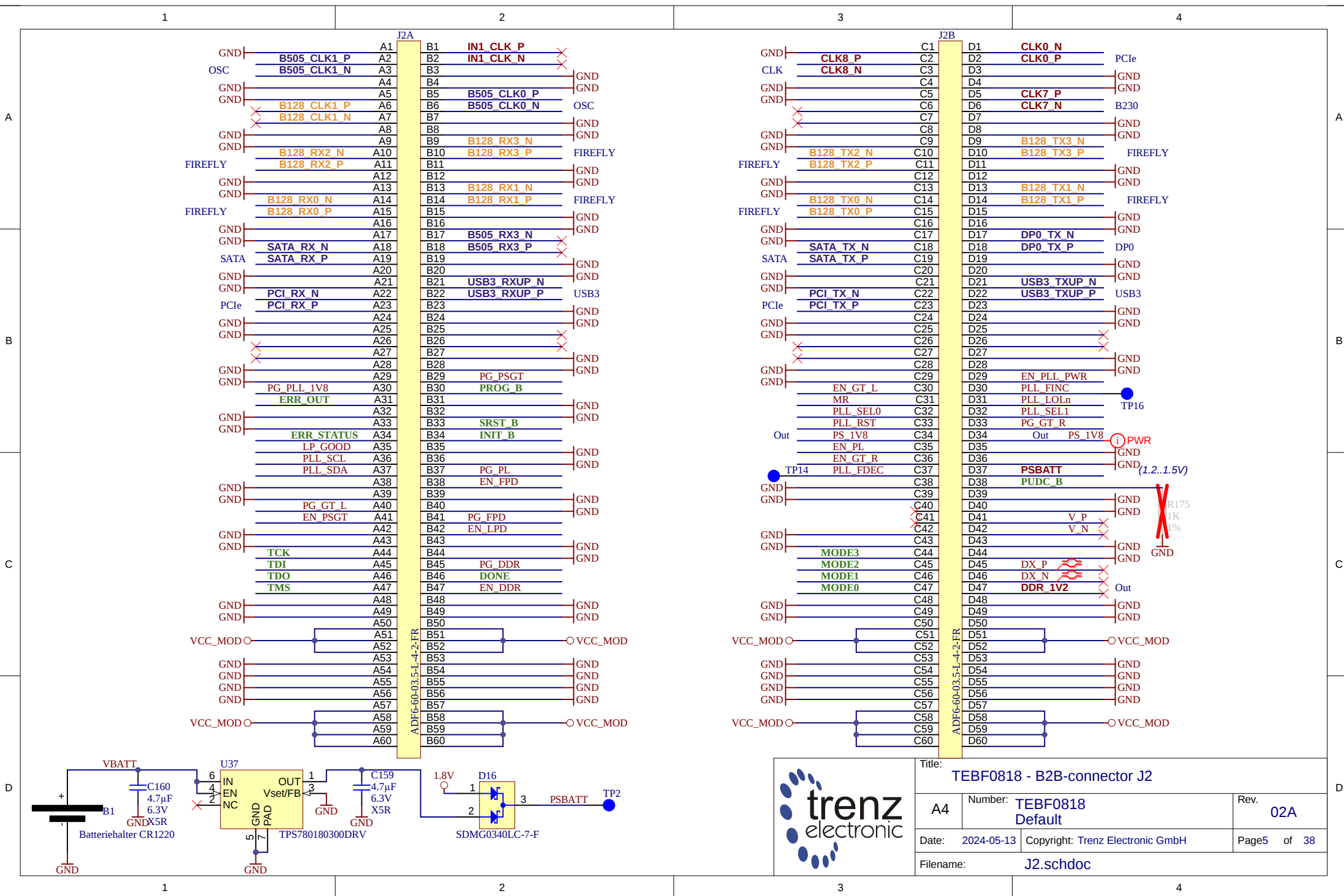
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Title: TEBF0818 - Block Diagram		
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	
Filename: BD.SchDoc		Page2 of 38

1	2	3	4	
A	U_J1 J1.schdoc U_J2 J2.schdoc U_J3 J3.schdoc U_J4 J4.schdoc SNAP1 1 PAD1 2 PAD2 3 PAD3 4 PAD4 TE_76x52mm_3N_Snap			A
B	 Distance Holder M3x5 Male/Female  Washer M3  Serrated Washer M3  Nut M3  Screw M3x4  Distance Holder M3x5 Male/Female  Washer M3  Serrated Washer M3  Nut M3  Screw M3x4  Distance Holder M3x5 Male/Female  Washer M3  Serrated Washer M3  Nut M3  Screw M3x4  Distance Holder M3x5 Male/Female  Washer M3  Serrated Washer M3  Nut M3  Screw M3x4			B
C				C
D	 Title: TEBF0818 - B2B-connectors A4 Number: TEBF0818 Default Date: 2024-05-13 Filename: B2B.schdoc Rev. 02A Page3 of 38			D
1	2	3	4	





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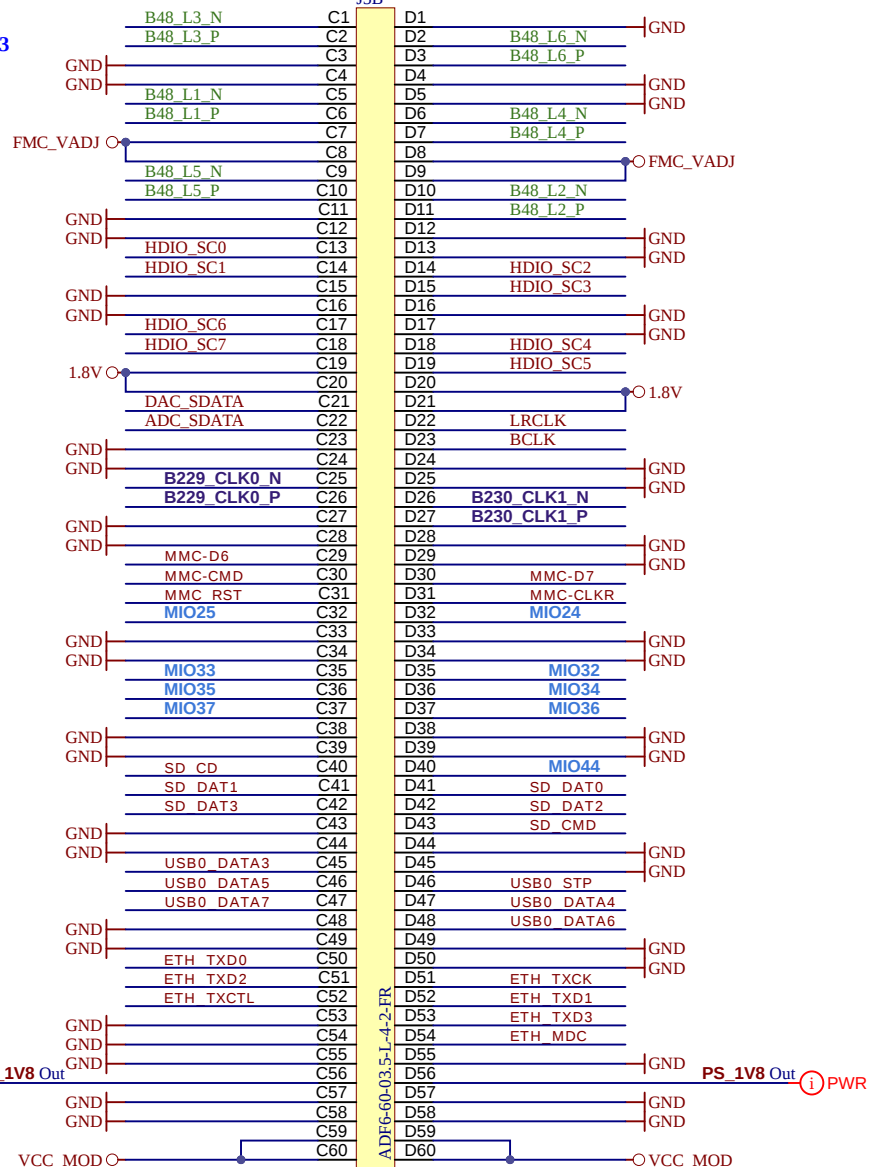
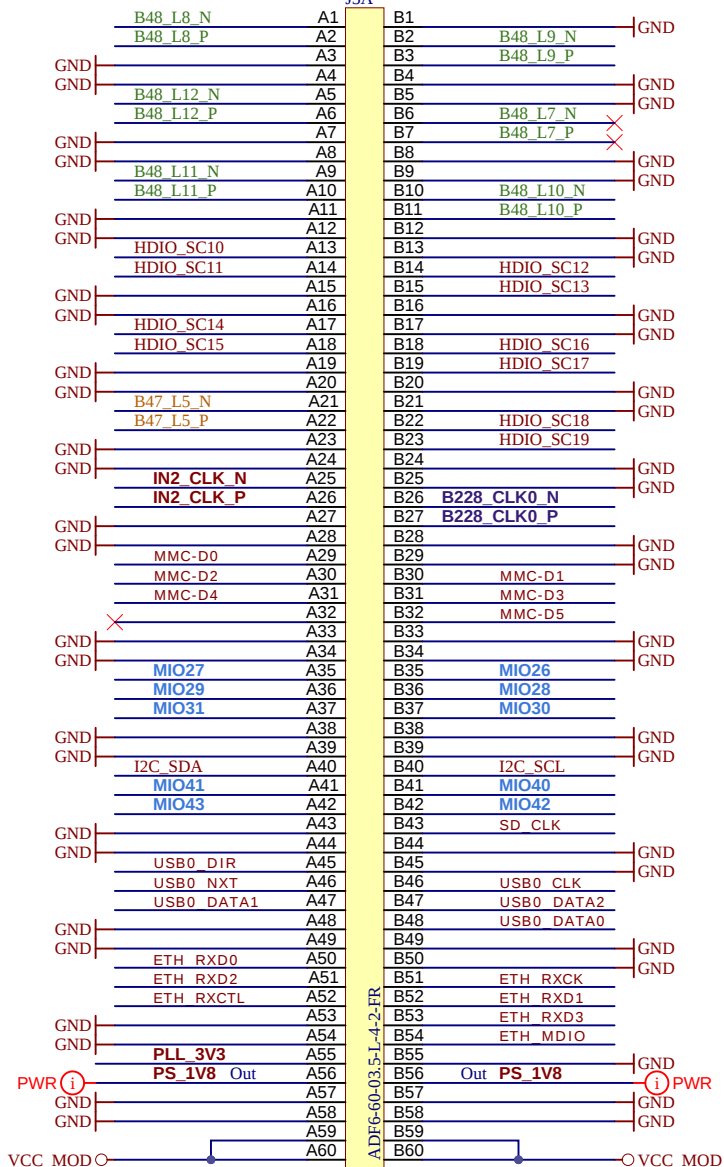
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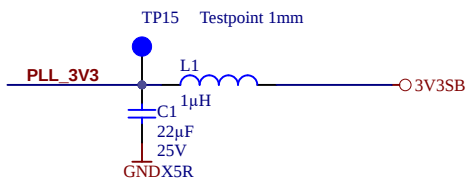
J3A

J3B

B48 --> B25 for TE0813



Testpoint 1mm
TP28
Testpoint 1mm
TP29
Testpoint 1mm
TP30
Testpoint 1mm
TP31
Testpoint 1mm
TP32



Title: TEBF0818 - B2B-connector J3		
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page6 of 38
Filename: J3.schdoc		

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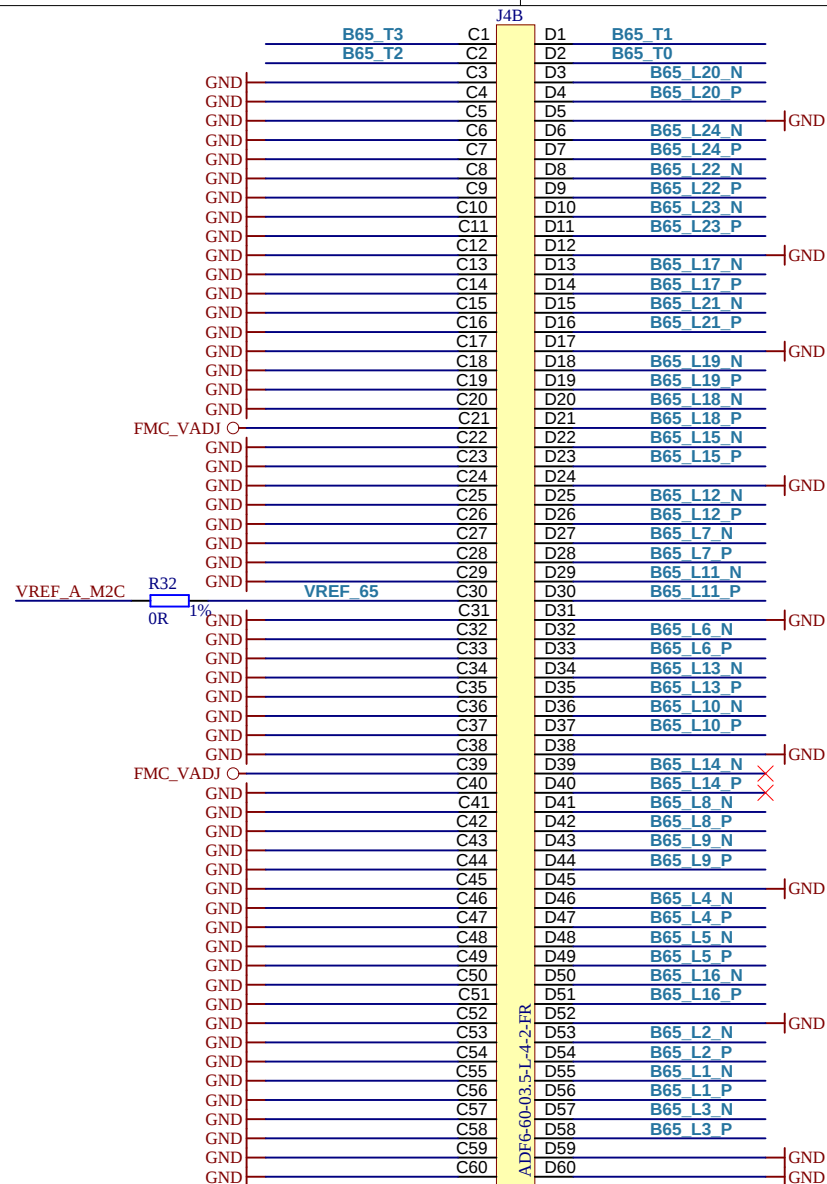
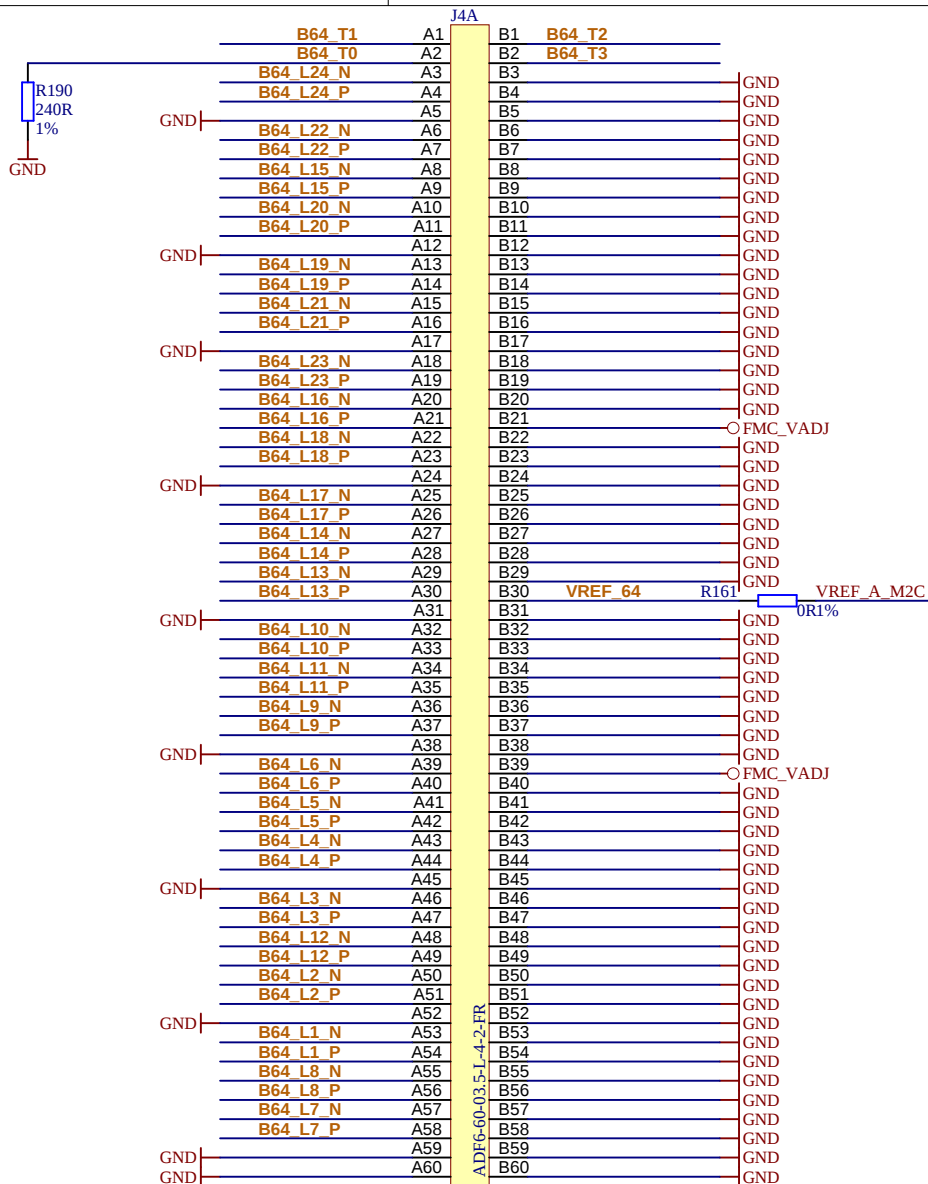
4

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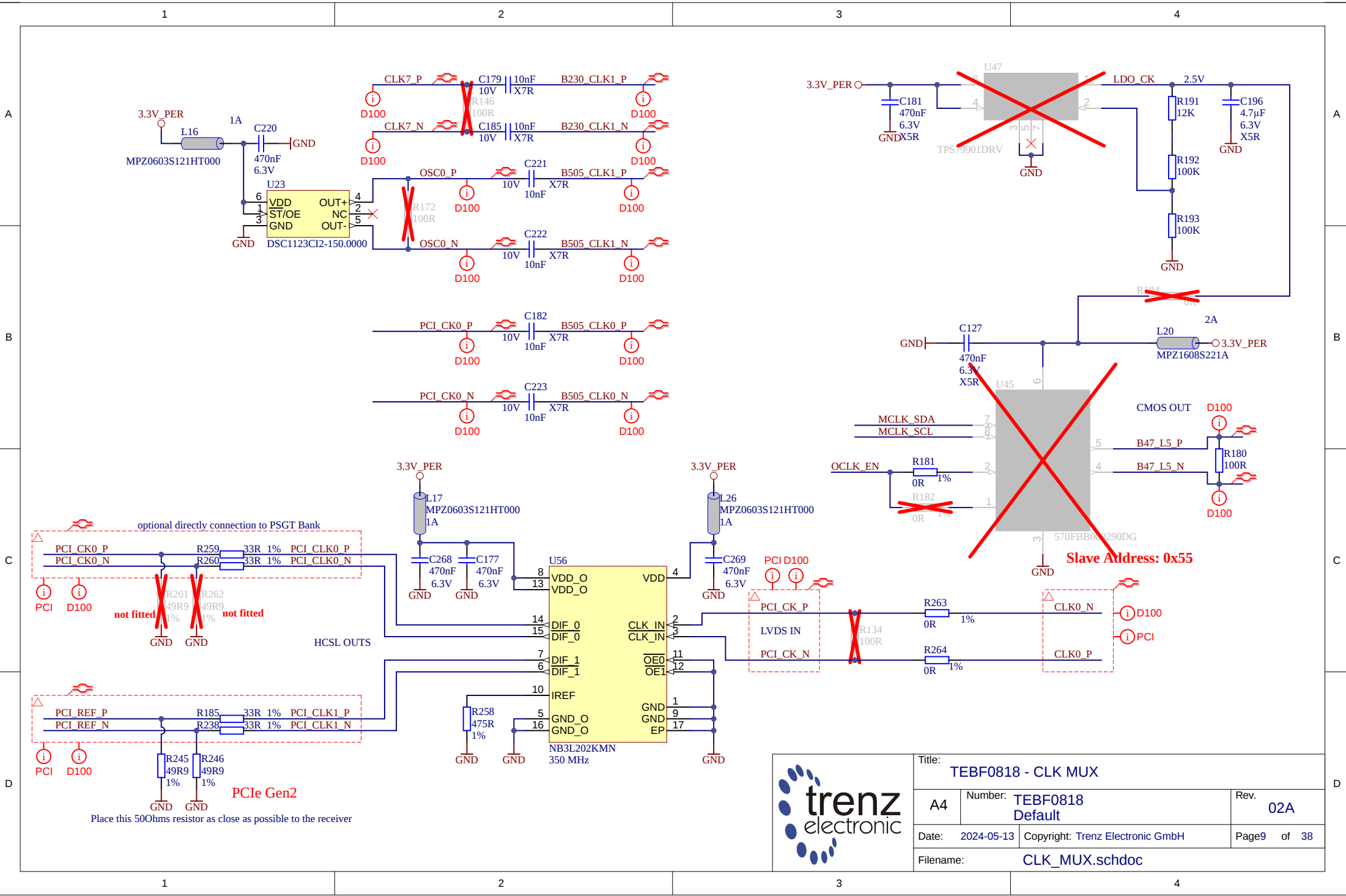
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Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page 7 of 38
Filename: J4.schdoc		

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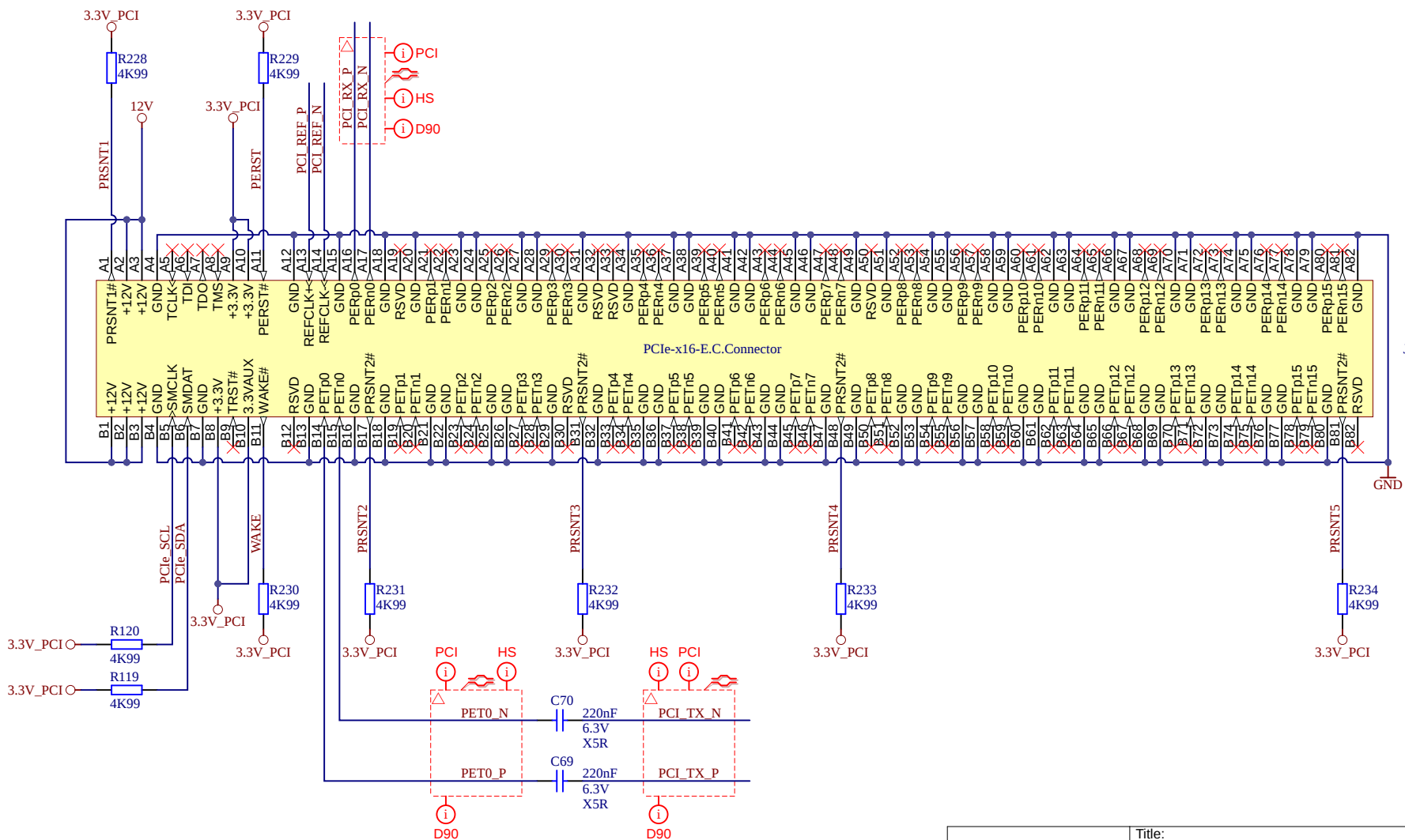
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Title: TEBF0818 - CLK MUX		
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	
Filename: CLK_MUX.schdoc		Page 9 of 38



Title: TEBF0818 - PCIe		
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	
Filename: PCIe.schdoc		Page10 of 38

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A

A

B

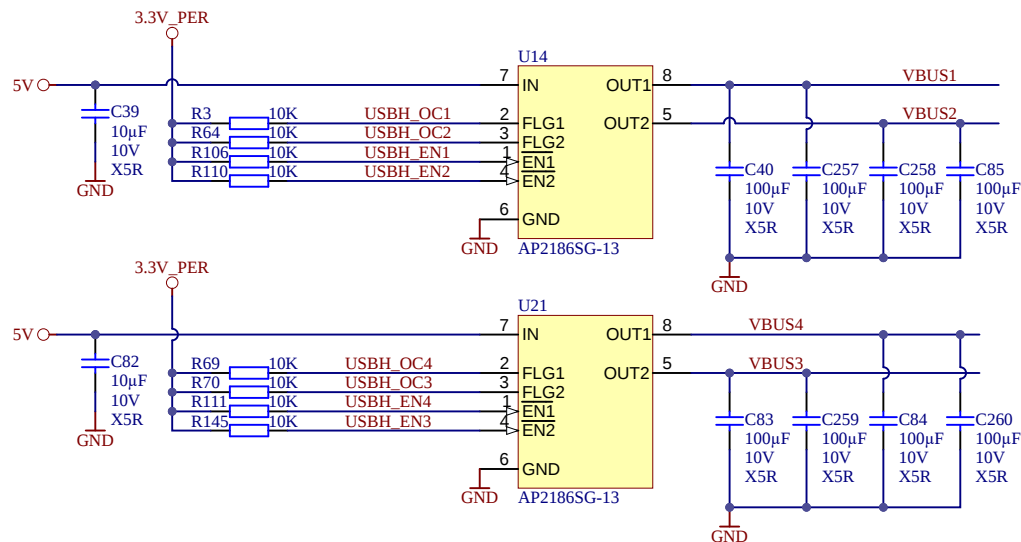
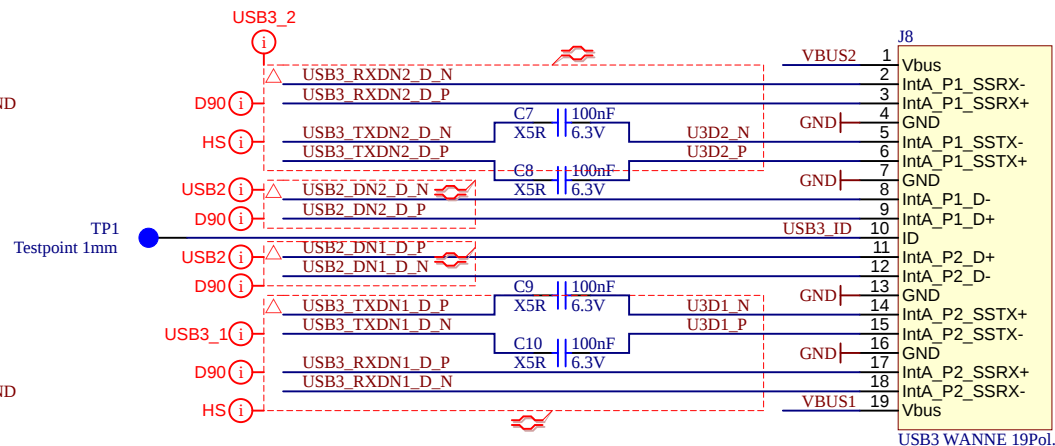
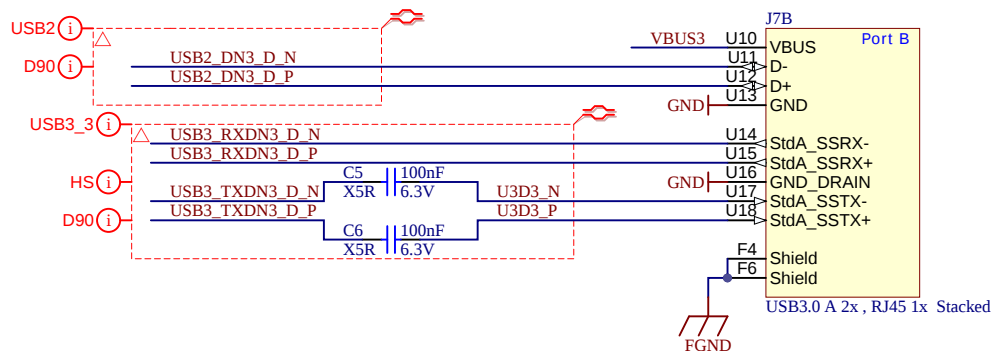
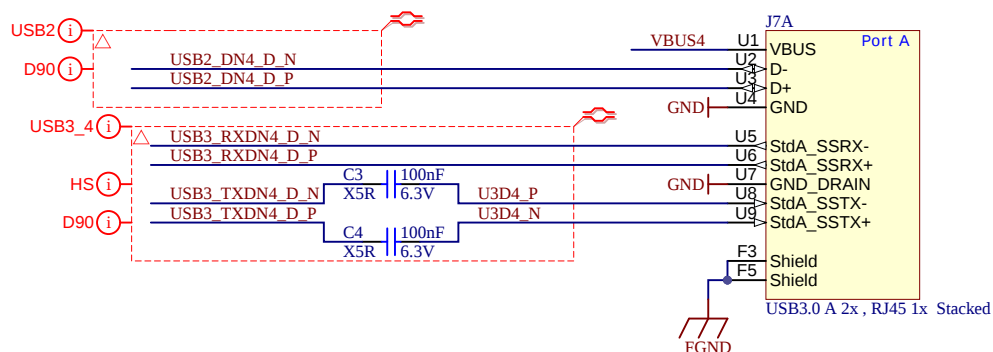
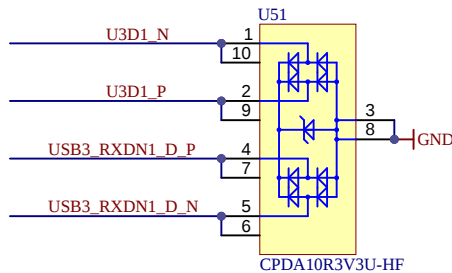
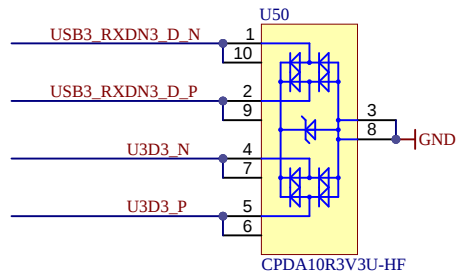
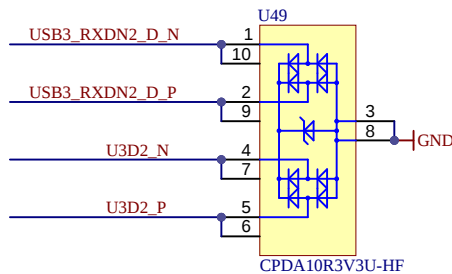
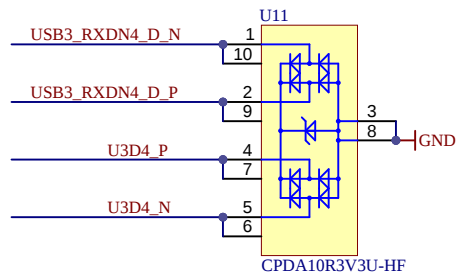
B

C

C

D

D



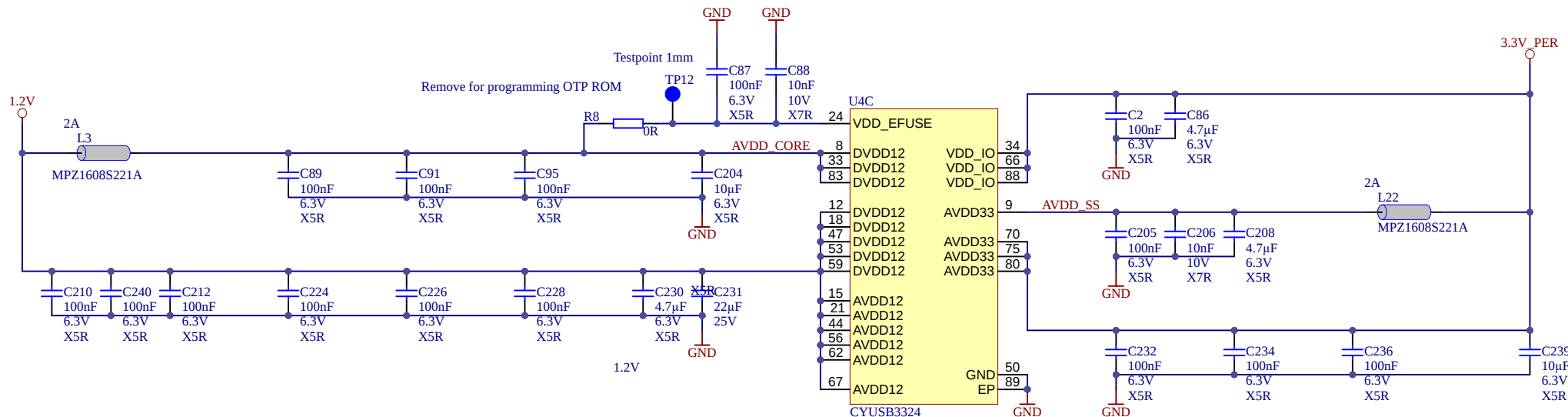
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A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page12 of 38
Filename: USB-CONN.schdoc		

1

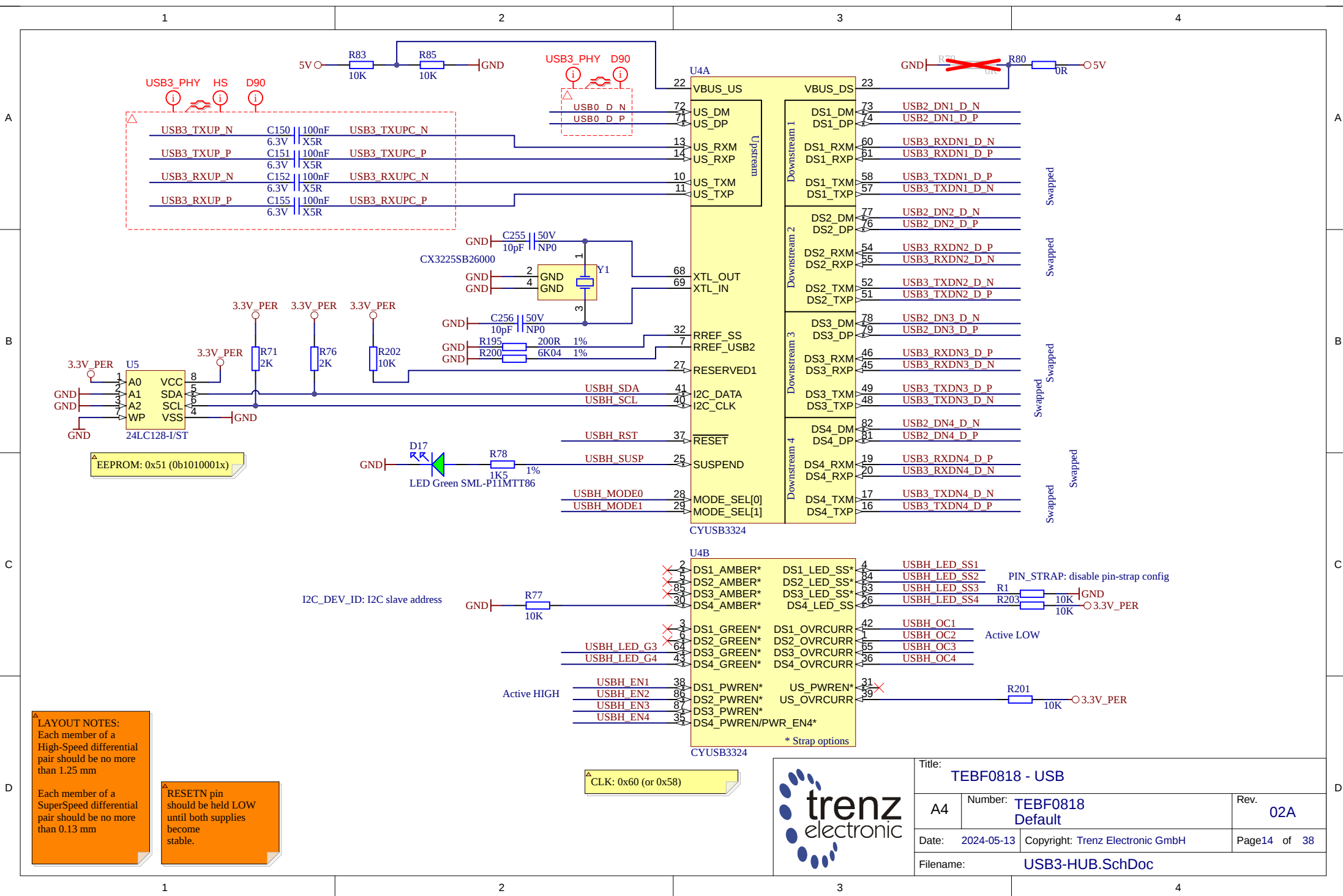
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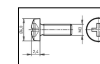
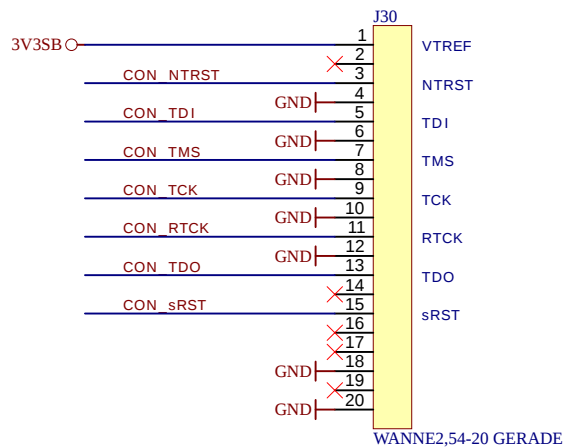
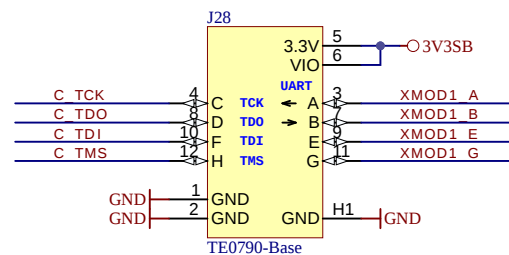
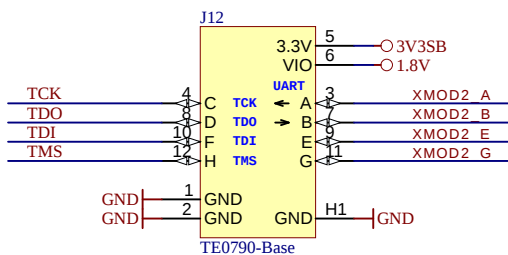
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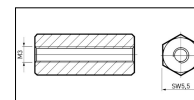


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		A4	Rev. 02A
Date: 2024-05-13		Page13 of 38	
Filename: USB.schdoc		Copyright: Trenz Electronic GmbH	
Number: TEBF0818 Default		Rev. 02A	

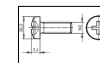




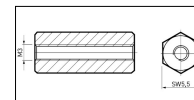
Screw M3x6



Standoff M3x10 II



Screw M3x6



Standoff M3x10 II

	Title: TEBF0818 - XMOD JTAG	
	A4	Number: TEBF0818 Default
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	
Filename: XMOD.schdoc	Page18 of 38	
		Rev. 02A

FMC_DP D100

B228 TX0 P	C2	DP0_C2M_P	DP0_M2C_P	C6	B228 RX0 P
B228 TX0 N	C3	DP0_C2M_N	DP0_M2C_N	C7	B228 RX0 N
B228 TX1 P	A22	DP1_C2M_P	DP1_M2C_P	A2	B228 RX1 P
B228 TX1 N	A23	DP1_C2M_N	DP1_M2C_N	A3	B228 RX1 N
B228 TX2 P	A26	DP2_C2M_P	DP2_M2C_P	A6	B228 RX2 P
B228 TX2 N	A27	DP2_C2M_N	DP2_M2C_N	A7	B228 RX2 N
B228 TX3 P	A30	DP3_C2M_P	DP3_M2C_P	A10	B228 RX3 P
B228 TX3 N	A31	DP3_C2M_N	DP3_M2C_N	A11	B228 RX3 N
B229 TX0 P	A34	DP4_C2M_P	DP4_M2C_P	A14	B229 RX0 P
B229 TX0 N	A35	DP4_C2M_N	DP4_M2C_N	A15	B229 RX0 N
B229 TX1 P	A38	DP5_C2M_P	DP5_M2C_P	A18	B229 RX1 P
B229 TX1 N	A39	DP5_C2M_N	DP5_M2C_N	A19	B229 RX1 N
B229 TX2 P	B36	DP6_C2M_P	DP6_M2C_P	B16	B229 RX2 P
B229 TX2 N	B37	DP6_C2M_N	DP6_M2C_N	B17	B229 RX2 N
B229 TX3 P	B32	DP7_C2M_P	DP7_M2C_P	B12	B229 RX3 P
B229 TX3 N	B33	DP7_C2M_N	DP7_M2C_N	B13	B229 RX3 N
B230 TX0 P	B28	DP8_C2M_P	DP8_M2C_P	B8	B230 RX0 P
B230 TX0 N	B29	DP8_C2M_N	DP8_M2C_N	B9	B230 RX0 N
B230 TX1 P	B24	DP9_C2M_P	DP9_M2C_P	B4	B230 RX1 P
B230 TX1 N	B25	DP9_C2M_N	DP9_M2C_N	B5	B230 RX1 N

ASP-134486-01, CC-HPC-10

LA D100

CLK1	G6	LA00_P_CC	LA19_P	H22	B65 L2 P
CLK1	G7	LA00_N_CC	LA19_N	H23	B65 L2 N
CLK1	D8	LA01_P_CC	LA20_P	G21	B65 L9 P
CLK1	D9	LA01_N_CC	LA20_N	G22	B65 L9 N
	H7	LA02_P	LA21_P	G25	B65 L18 N
	H8	LA02_N	LA21_N	G26	B65 L18 P
	G9	LA03_P	LA22_P	G24	B65 L15 P
	G10	LA03_N	LA22_N	G25	B65 L15 N
	H10	LA04_P	LA23_P	G23	B65 L7 P
	H11	LA04_N	LA23_N	G24	B65 L7 N
	D11	LA05_P	LA24_P	H28	B65 L17 P
	D12	LA05_N	LA24_N	G29	B65 L17 N
	C10	LA06_P	LA25_P	G27	B65 L4 N
	C11	LA06_N	LA25_N	G28	B65 L4 P
	H13	LA07_P	LA26_P	G26	B65 L6 P
	H14	LA07_N	LA26_N	G27	B65 L6 N
	G12	LA08_P	LA27_P	G26	B65 L10 P
	G13	LA08_N	LA27_N	G27	B65 L10 N
	D14	LA09_P	LA28_P	G31	B65 L19 N
	D15	LA09_N	LA28_N	G32	B65 L19 P
	C14	LA10_P	LA29_P	G30	B65 L8 P
	C15	LA10_N	LA29_N	G31	B65 L8 N
	H16	LA11_P	LA30_P	G34	B65 L3 N
	H17	LA11_N	LA30_N	G35	B65 L3 P
	G15	LA12_P	LA31_P	G33	B65 L16 P
	G16	LA12_N	LA31_N	G34	B65 L16 N
	D17	LA13_P	LA32_P	H37	B65 L5 P
	D18	LA13_N	LA32_N	H38	B65 L5 N
	C18	LA14_P	LA33_P	G36	B65 L1 P
	C19	LA14_N	LA33_N	G37	B65 L1 N
	H19	LA15_P			
	H20	LA15_N			
	G18	LA16_P			
	G19	LA16_N			
	D20	LA17_P_CC			
	D21	LA17_N_CC			
	C22	LA18_P_CC			
	C23	LA18_N_CC			

ASP-134486-01, CC-HPC-10

FMC_DP D100

HA D100

B66 L11 P	F4	HA00_P_CC	HA13_P	F12	B66 L12 P
B66 L11 N	F5	HA00_N_CC	HA13_N	F13	B66 L12 N
B66 L13 P	F6	HA01_P_CC	HA14_P	F15	B66 L22 N
B66 L13 N	F7	HA01_N_CC	HA14_N	F16	B66 L22 P
B66 L5 P	K7	HA02_P	HA15_P	F16	B66 L8 P
B66 L5 N	K8	HA02_N	HA15_N	F17	B66 L8 N
B66 L6 P	J6	HA03_P	HA16_P	F15	B66 L20 N
B66 L6 N	J7	HA03_N	HA16_N	F16	B66 L20 P
B66 L7 P	F7	HA04_P	HA17_P_CC	F16	B66 L14 P
B66 L7 N	F8	HA04_N	HA17_N_CC	F17	B66 L14 N
B66 L9 P	E6	HA05_P	HA18_P	F18	B66 L19 N
B66 L9 N	E7	HA05_N	HA18_N	F19	B66 L19 P
B66 L3 N	K10	HA06_P	HA19_P	F19	B66 L24 N
B66 L3 P	K11	HA06_N	HA19_N	F20	B66 L24 P
B66 L4 N	J9	HA07_P	HA20_P	F18	B66 L15 P
B66 L4 P	J10	HA07_N	HA20_N	F19	B66 L15 N
B66 L23 N	F10	HA08_P	HA21_P	F19	B66 L18 P
B66 L23 P	F11	HA08_N	HA21_N	F20	B66 L18 N
B66 L21 N	E9	HA09_P	HA22_P	F21	B66 L10 N
B66 L21 P	E10	HA09_N	HA22_N	F22	B66 L10 P
B66 L17 P	K13	HA10_P	HA23_P	F22	B66 L1 P
B66 L17 N	K14	HA10_N	HA23_N	F23	B66 L1 N
B66 L2 N	J13	HA11_P			
B66 L2 P	J14	HA11_N			
B66 L16 N	F14	HA12_P			
B66 L16 P	F15	HA12_N			

ASP-134486-01, CC-HPC-10

HB D100

B64 L13 P	K25	HB00_P_CC	HB13_P	F30	B48 L5 P
B64 L13 N	K26	HB00_N_CC	HB13_N	F31	B48 L5 N
B64 L23 P	J25	HB01_P	HB14_P	F34	B48 L2 P
B64 L23 N	J26	HB01_N	HB14_N	F35	B48 L2 N
B64 L22 N	F24	HB02_P	HB15_P	F33	B48 L3 P
B64 L22 P	F25	HB02_N	HB15_N	F34	B48 L3 N
B64 L20 P	E21	HB03_P	HB16_P	F34	B48 L9 P
B64 L20 N	E22	HB03_N	HB16_N	F35	B48 L9 N
B64 L24 P	F25	HB04_P	HB17_P_CC	F37	B48 L8 P
B64 L24 N	F26	HB04_N	HB17_N_CC	F38	B48 L8 N
B64 L21 N	E23	HB05_P	HB18_P	F36	B48 L11 P
B64 L21 P	E24	HB05_N	HB18_N	F37	B48 L11 N
B65 L11 P	K28	HB06_P_CC	HB19_P	F33	B48 L4 P
B65 L11 N	K29	HB06_N_CC	HB19_N	F34	B48 L4 N
B65 L21 N	J27	HB07_P	HB20_P	F37	B48 L12 P
B65 L21 P	J28	HB07_N	HB20_N	F38	B48 L12 N
B65 L20 P	F28	HB08_P	HB21_P	F36	B48 L10 P
B65 L20 N	F29	HB08_N	HB21_N	F37	B48 L10 N
B65 L23 P	E27	HB09_P			
B65 L23 N	E28	HB09_N			
B65 L24 N	K31	HB10_P			
B65 L24 P	K32	HB10_N			
B65 L22 N	J30	HB11_P			
B65 L22 P	J31	HB11_N			
B48 L1 N	F33	HB12_P			
B48 L1 P	F34	HB12_N			

ASP-134486-01, CC-HPC-10



Title:

TEBF0818 - FMC

A4

Number:

TEBF0818
Default

Rev.

02A

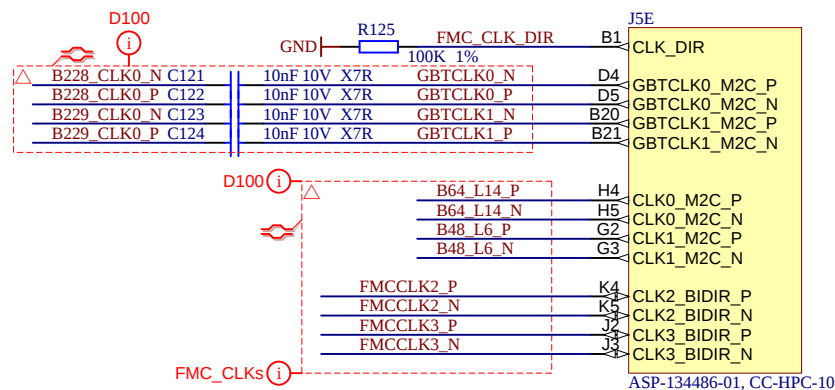
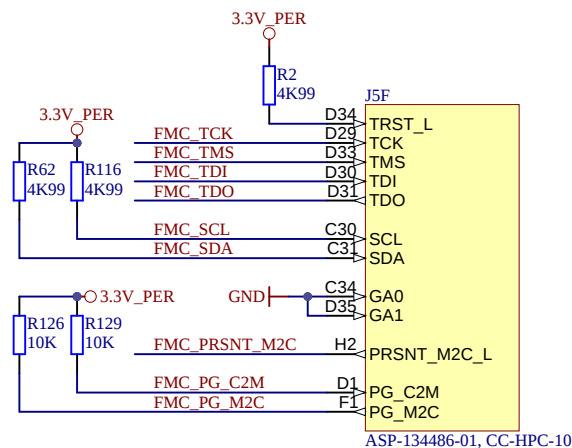
Date: 2024-05-13

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Page19 of 38

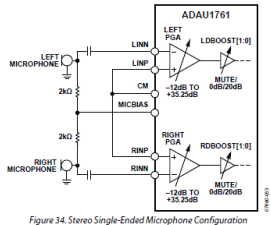
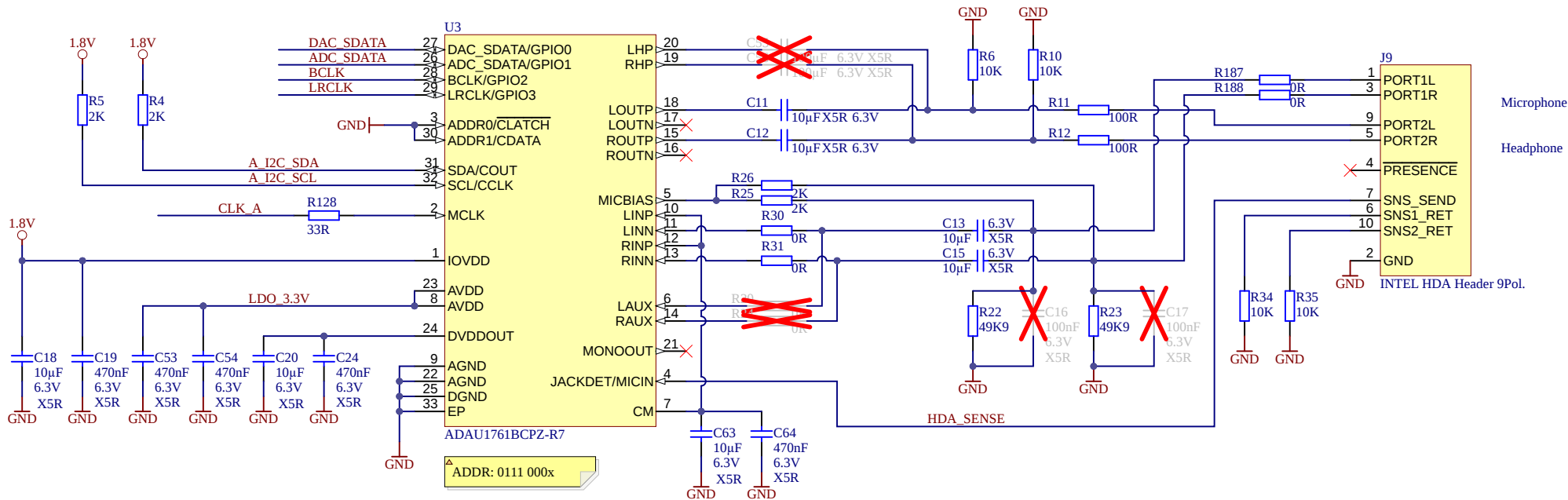
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FMC.SchDoc



Title: TEBF0818 - FMC			
A4	Number: TEBF0818 Default	Rev. 02A	
Date: 2024-05-13	Copyright: Trenz Electronic GmbH		Page20 of 38
Filename: FMC_MISC.SchDoc			





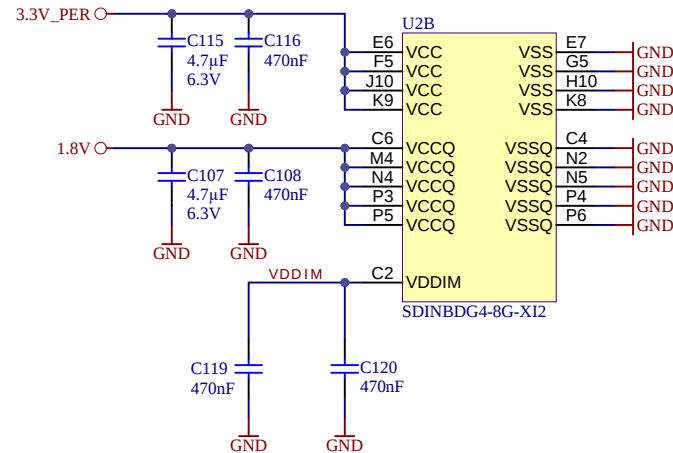
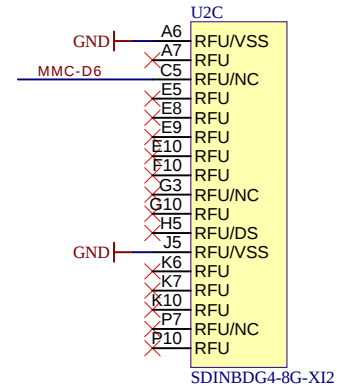
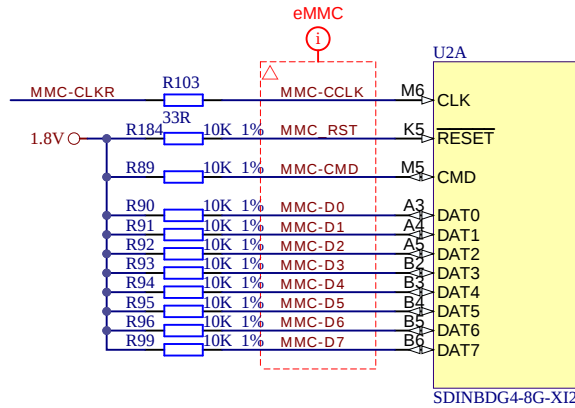
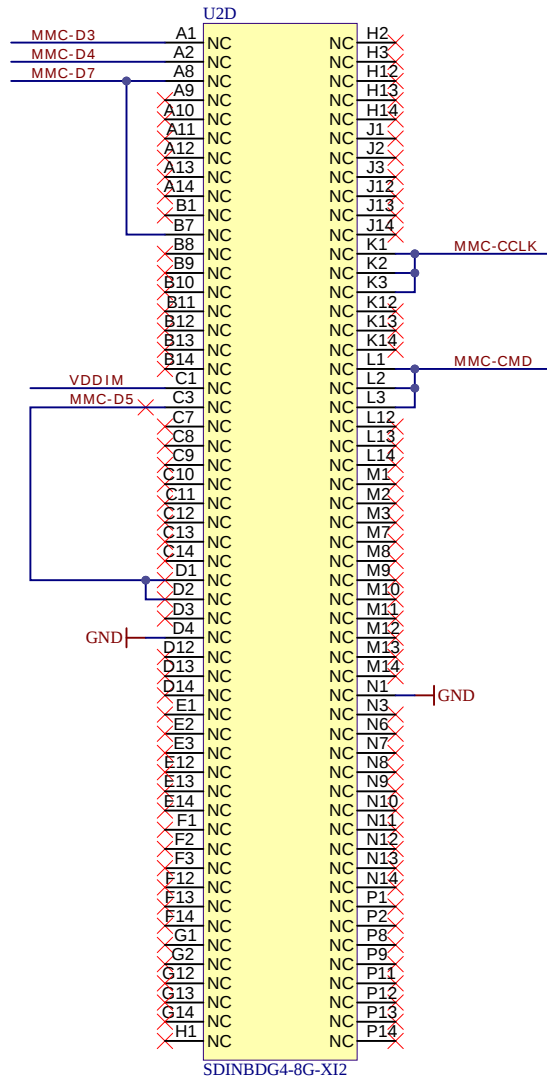
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		A4	Number: TEBF0818 Default
Date: 2024-05-13	Copyright: Trenz Electronic GmbH		Rev. 02A
Filename: Audio.SchDoc		Page22 of 38	

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LAYOUT NOTES:
Signal trace length skew constraints
- Mismatch within DAT0DAT7 < 250 mil
- CLK to DAT0-7 mismatch < 250 mil
- CLK to CMD mismatch < 250 mil
- CLK to RST_N mismatch < 1000 mil



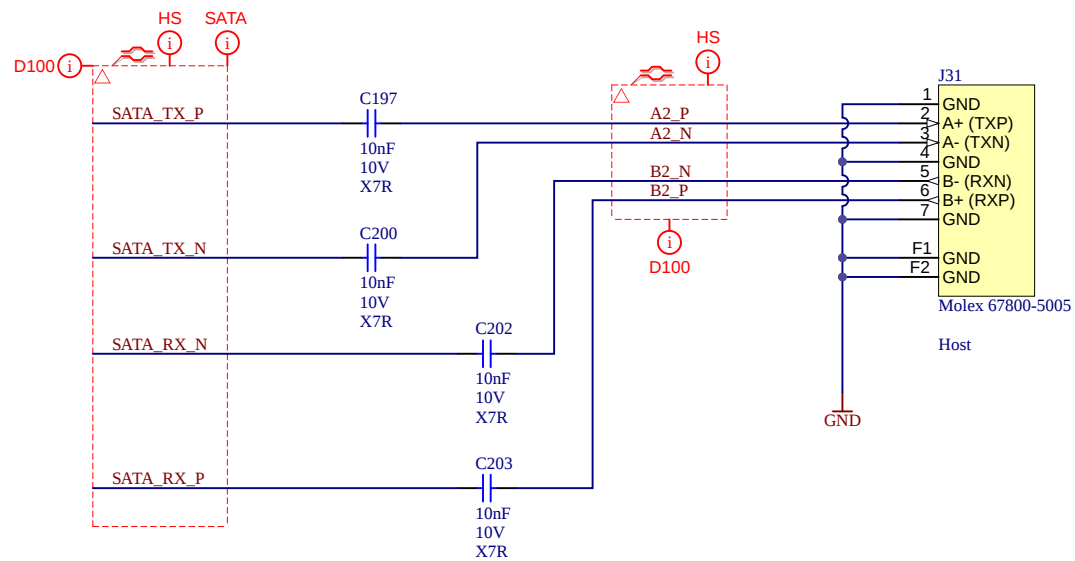
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Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page25 of 38
Filename: eMMC.SchDoc		

1

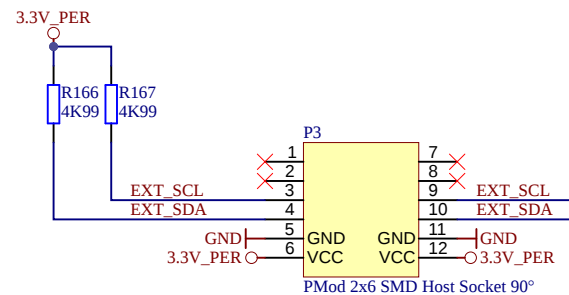
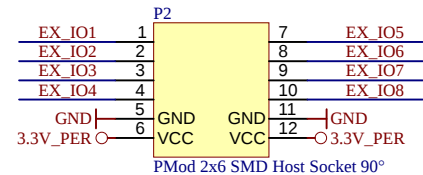
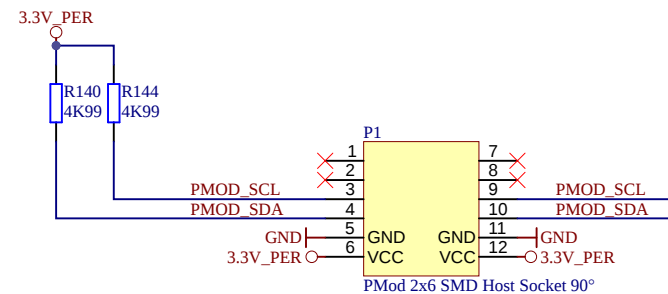
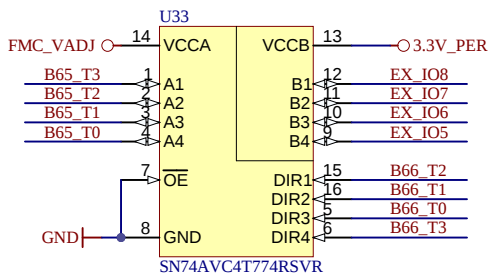
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		Title: TEBF0818 - SATA		
		A4	Number: TEBF0818 Default	Rev. 02A
		Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page26 of 38
		Filename: SATA.schdoc		



	Title: TEBF0818 - MISC		
	A4	Number: TEBF0818 Default	Rev. 02A
	Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page 27 of 38
	Filename: PMODE.schdoc		

1

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A

A

B

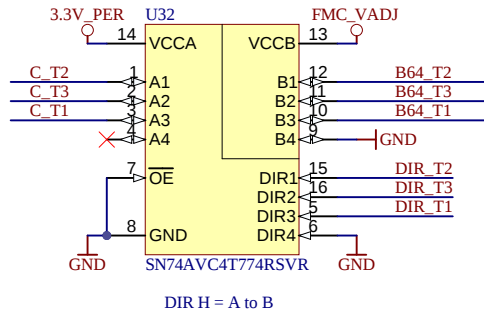
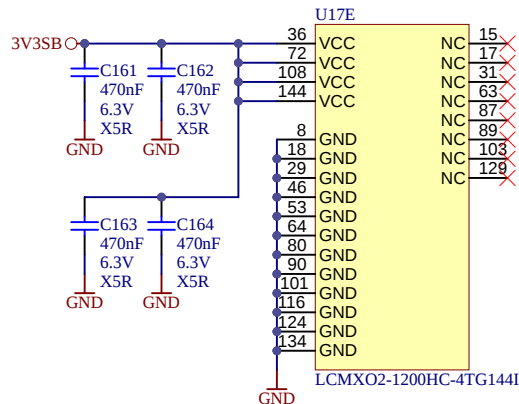
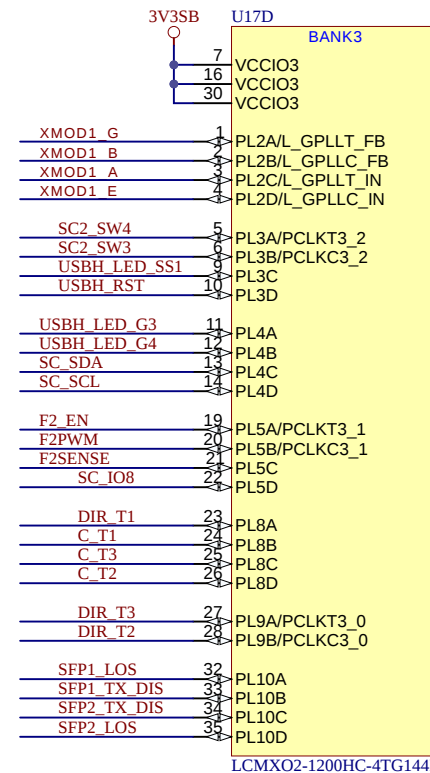
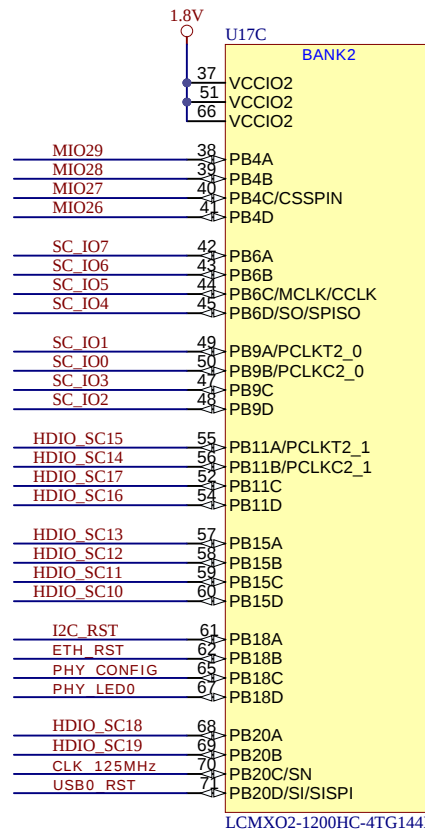
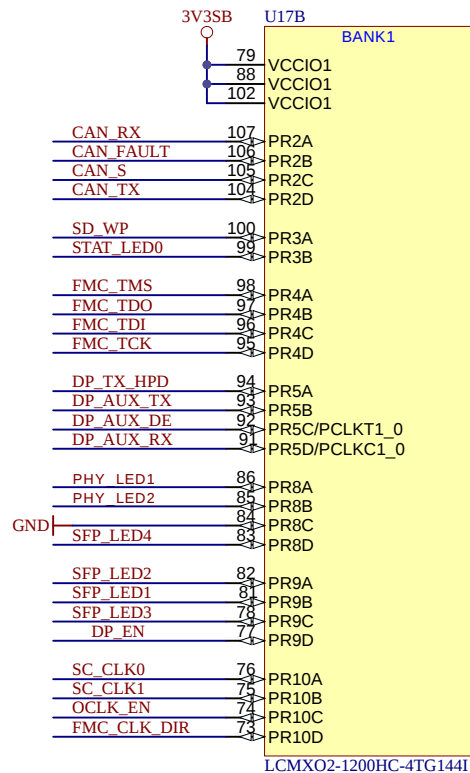
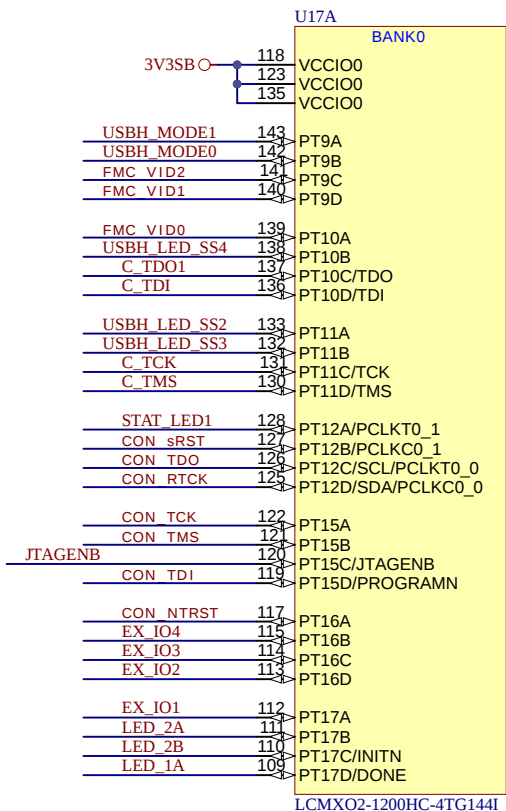
B

C

C

D

D



Title: TEBF0818 - CPLD		
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page28 of 38
Filename: SC.schdoc		

Pin 84 PR8C "GND": REV04

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A

A

B

B

C

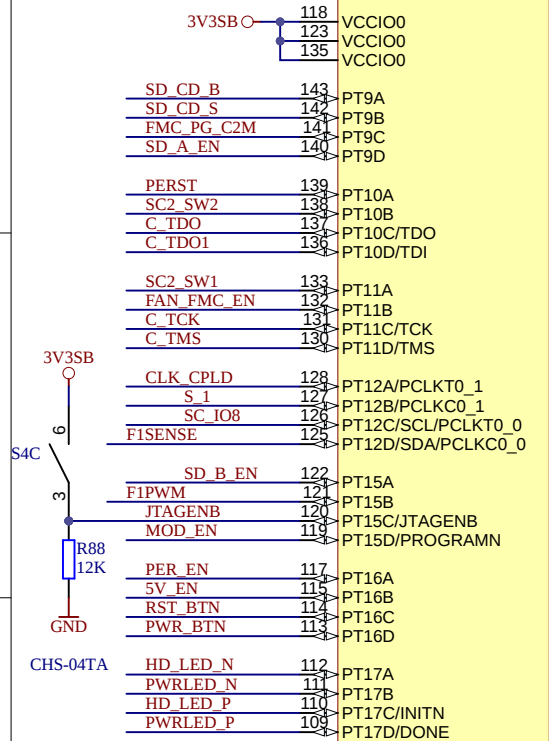
C

D

D

U39A

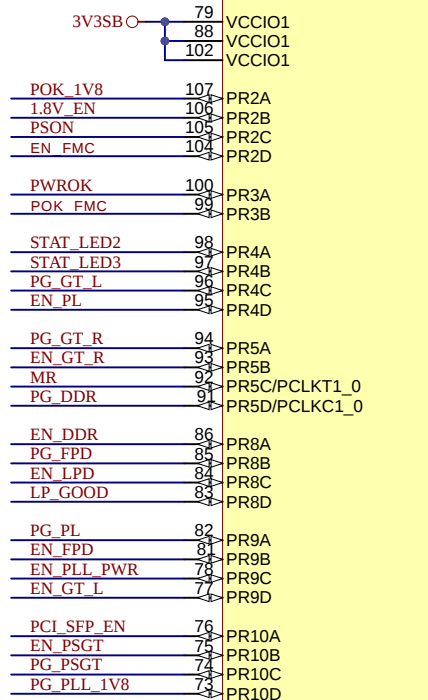
BANK0



LCMXO2-1200HC-4TG144I

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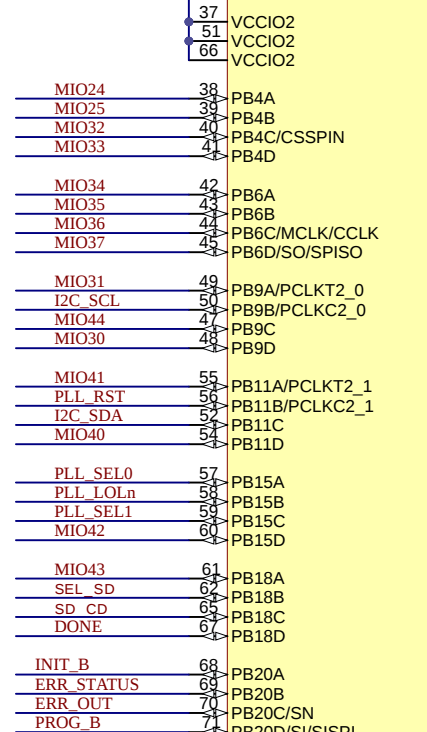
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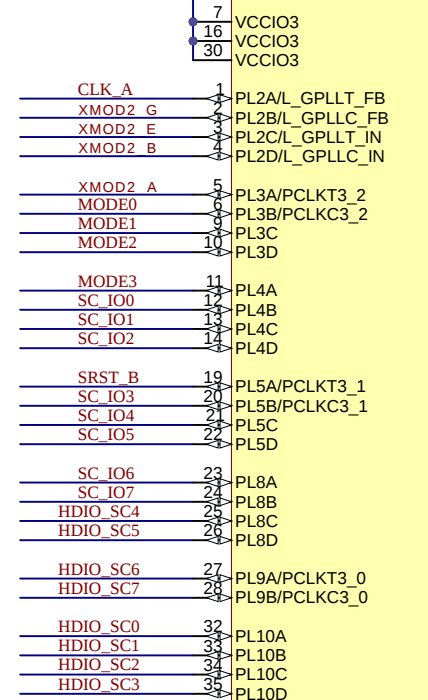
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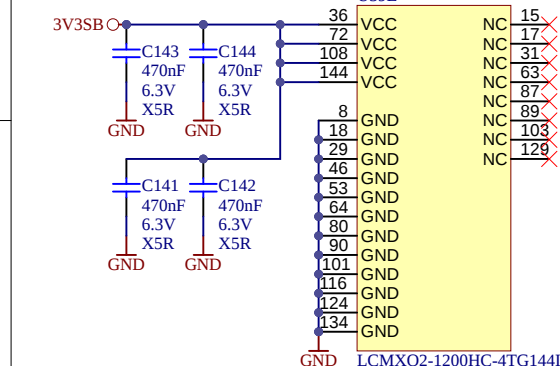
U39D

BANK3



LCMXO2-1200HC-4TG144I

U39E



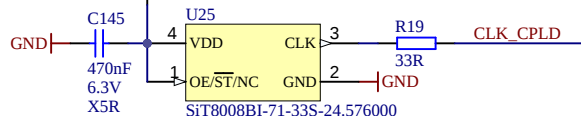
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S_1

R189

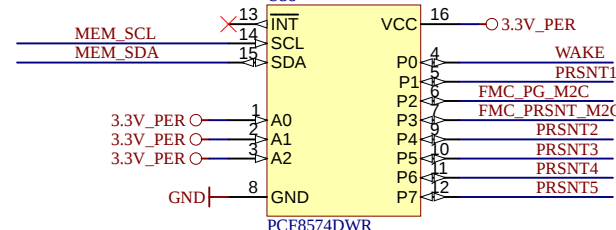
BEEPER

3.3V_PER

L15
MPZ1608S221A

Clock source for audio codec.

U38



Slave Address: 0x27



Title: TEBF0818 - CPLD_2		
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page29 of 38
Filename: SC2.schdoc		

1

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A

A

B

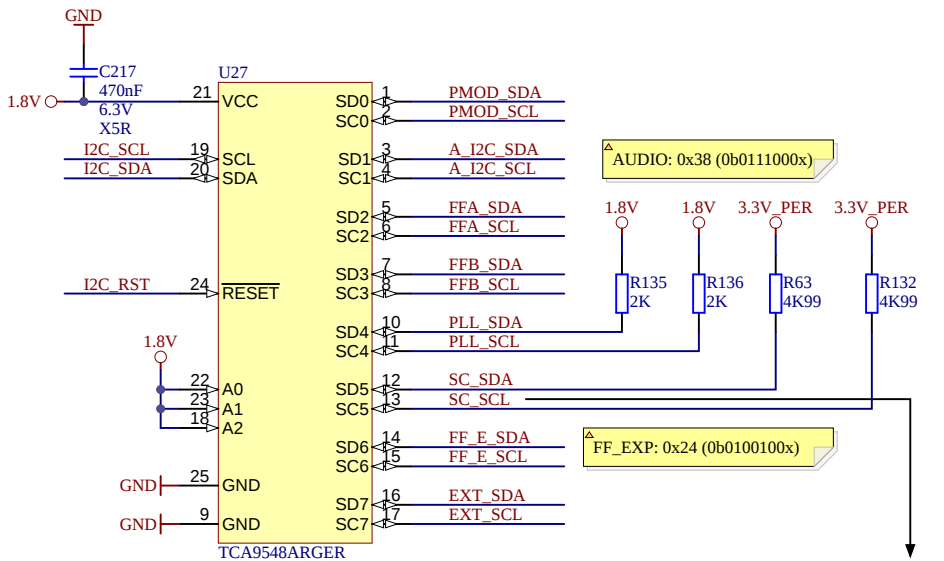
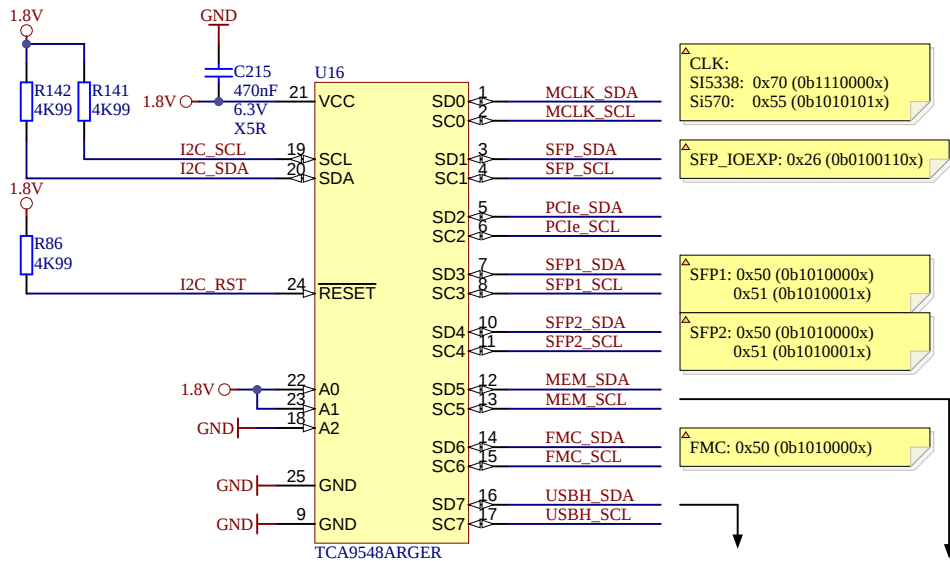
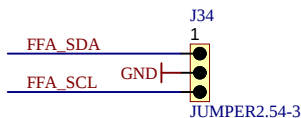
B

C

C

D

D



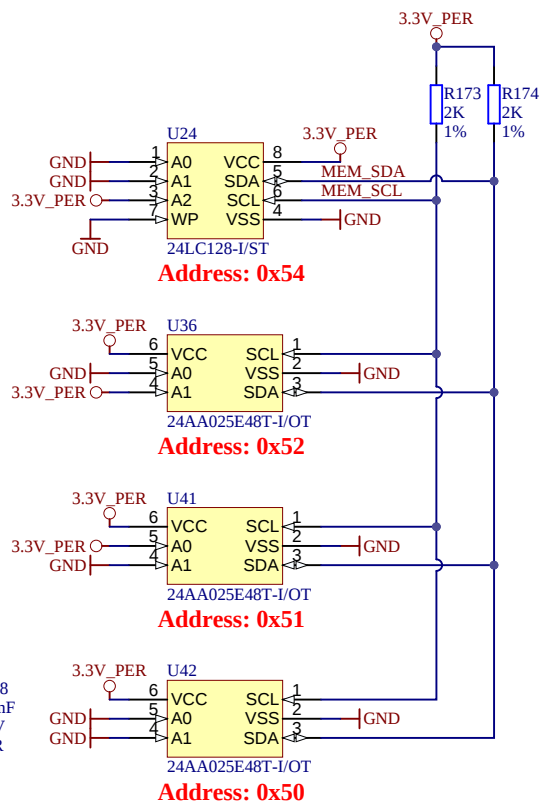
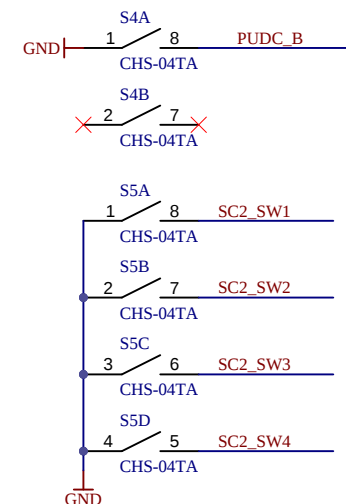
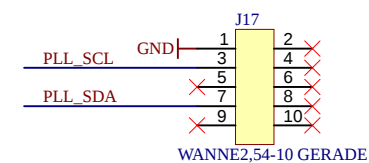
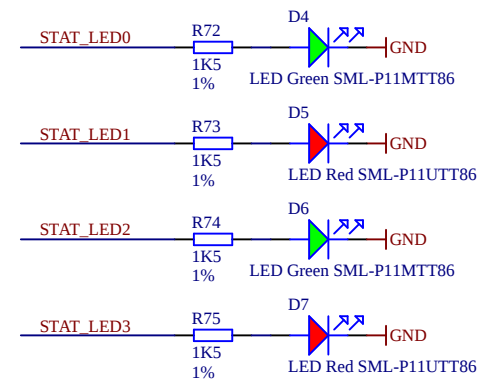
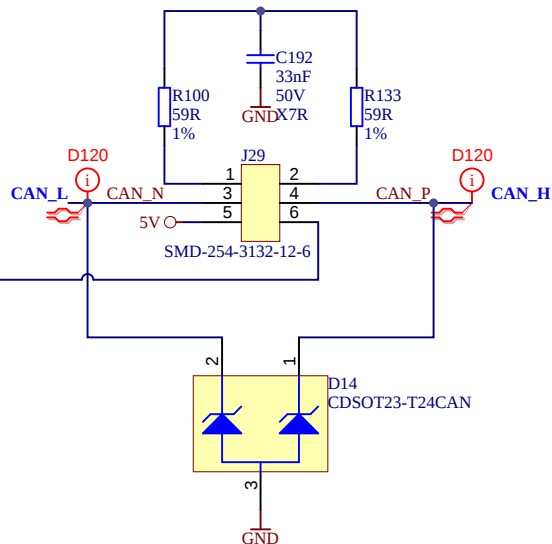
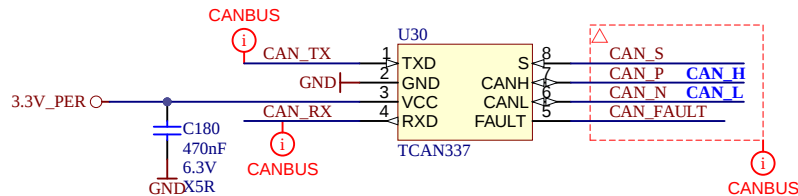
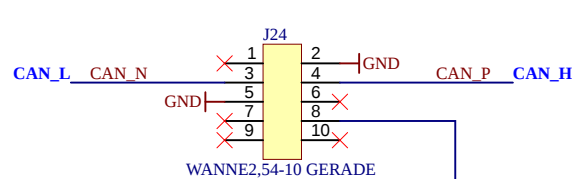
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Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page30 of 38
Filename: I2C_system.schdoc		

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2

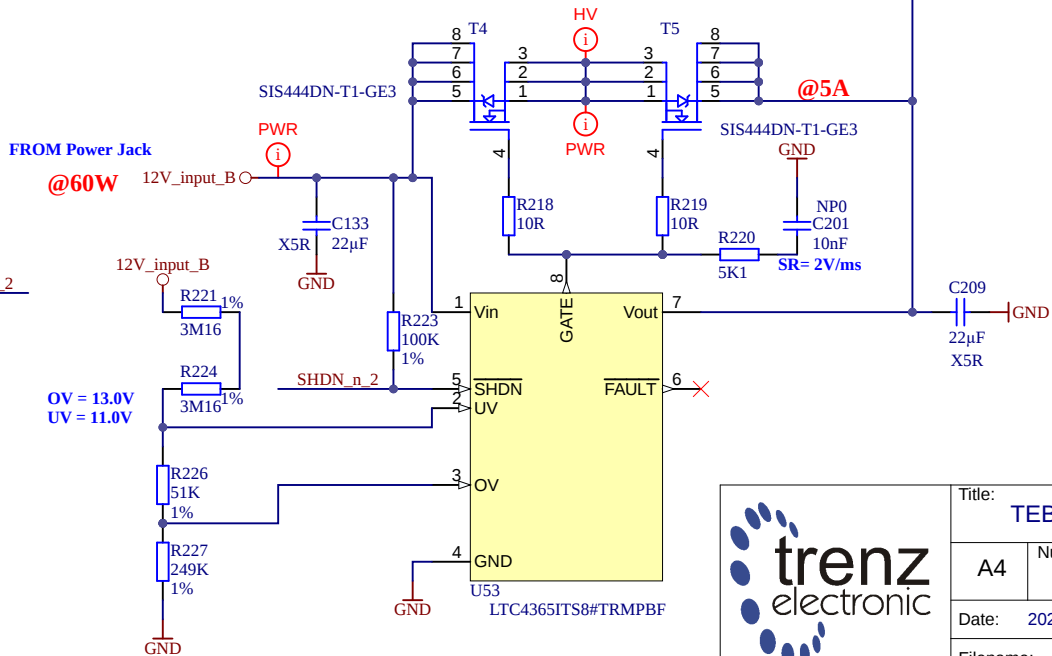
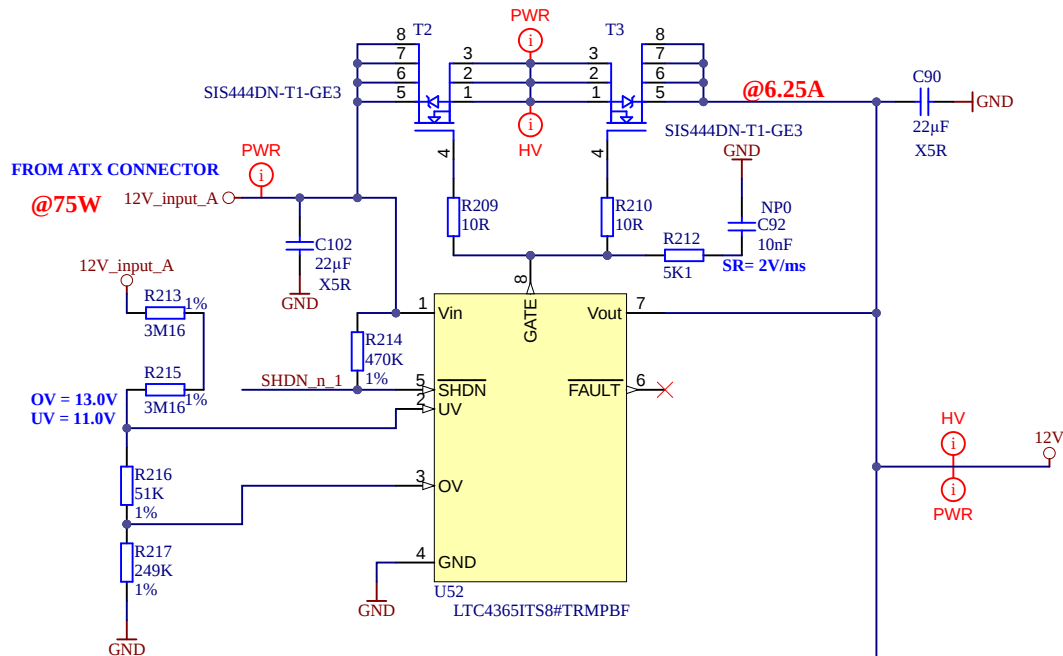
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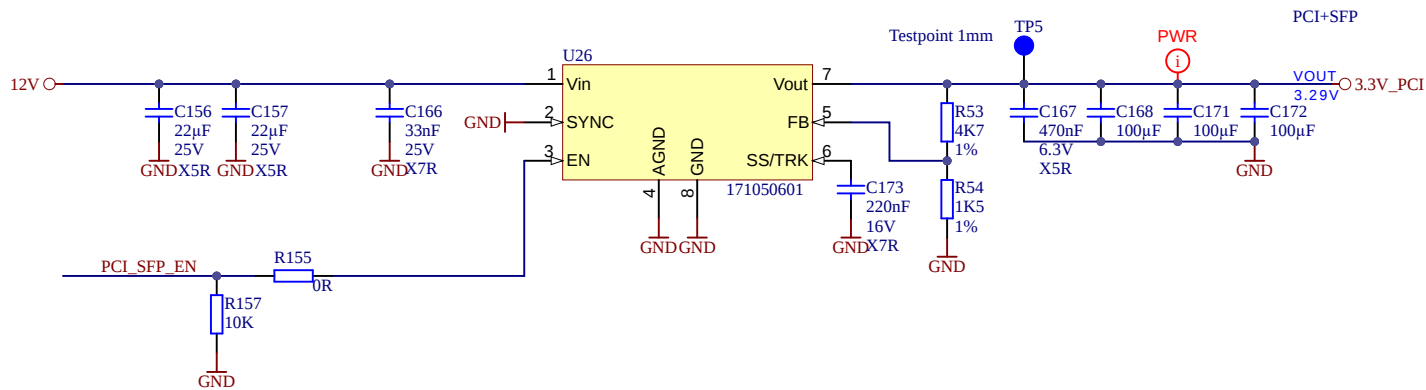
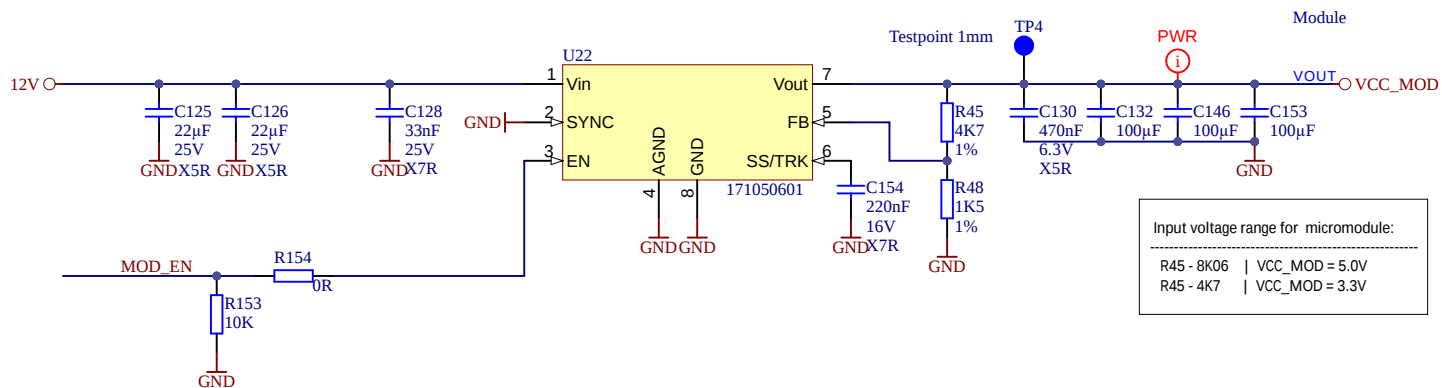


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Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page31 of 38
Filename: MISC.schdoc		

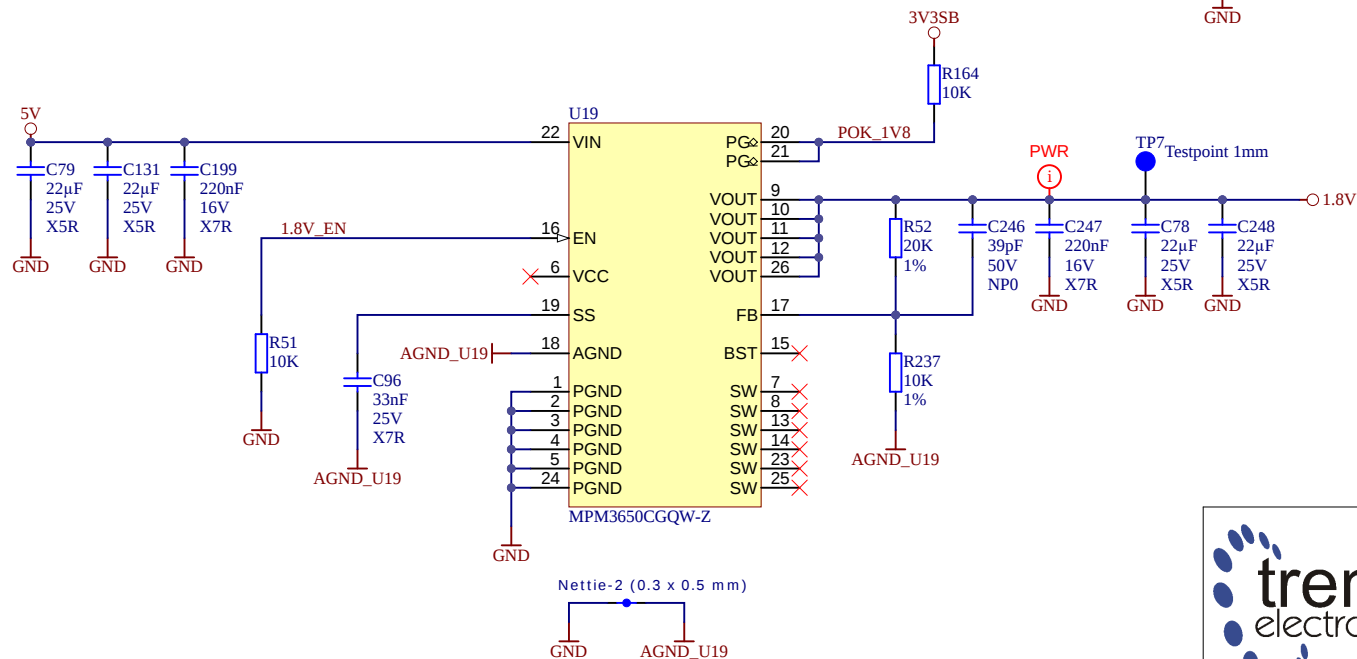
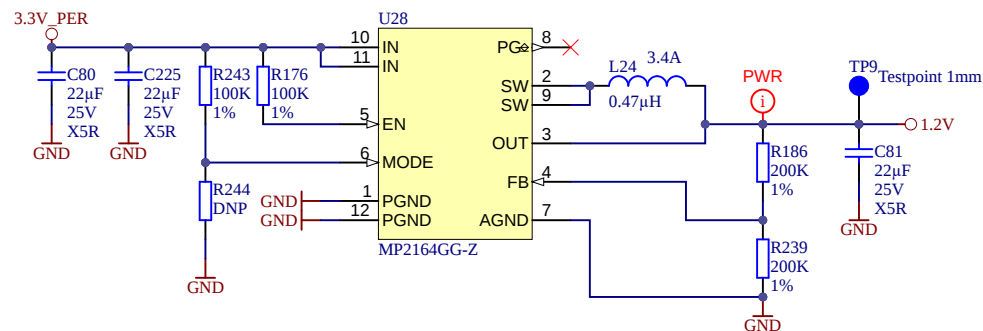
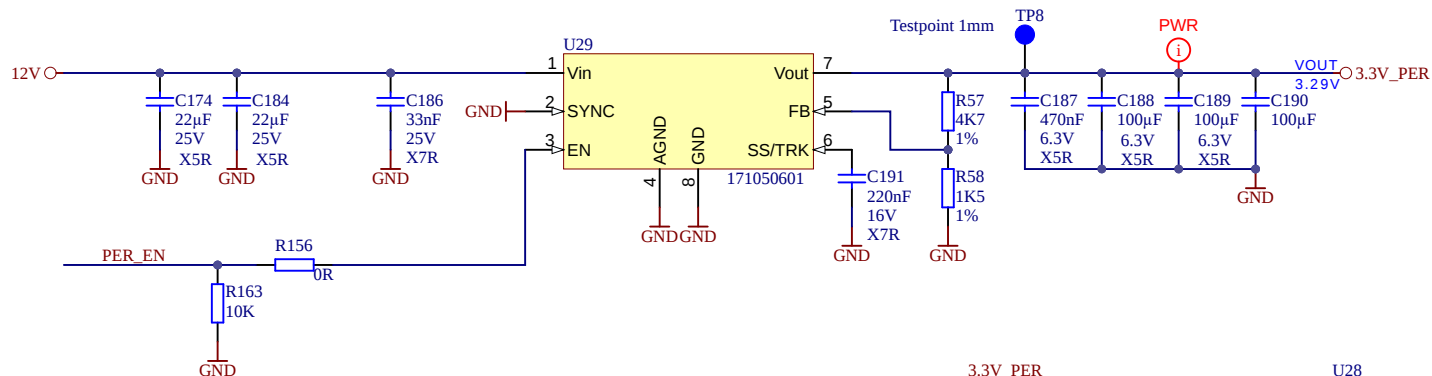
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- Power_1.schdoc
- U_Power2
- Power_2.schdoc
- U_Power3
- Power_3.schdoc
- U_Power4
- Power_4.schdoc



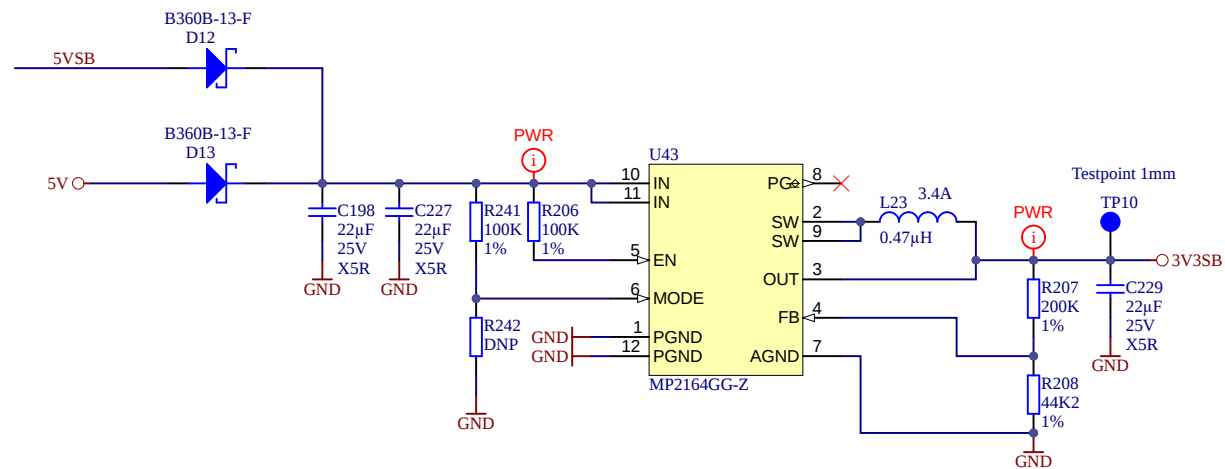
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Date: 2024-05-13	Copyright: Trenz Electronic GmbH	
Filename: Power.SchDoc		Page32 of 38



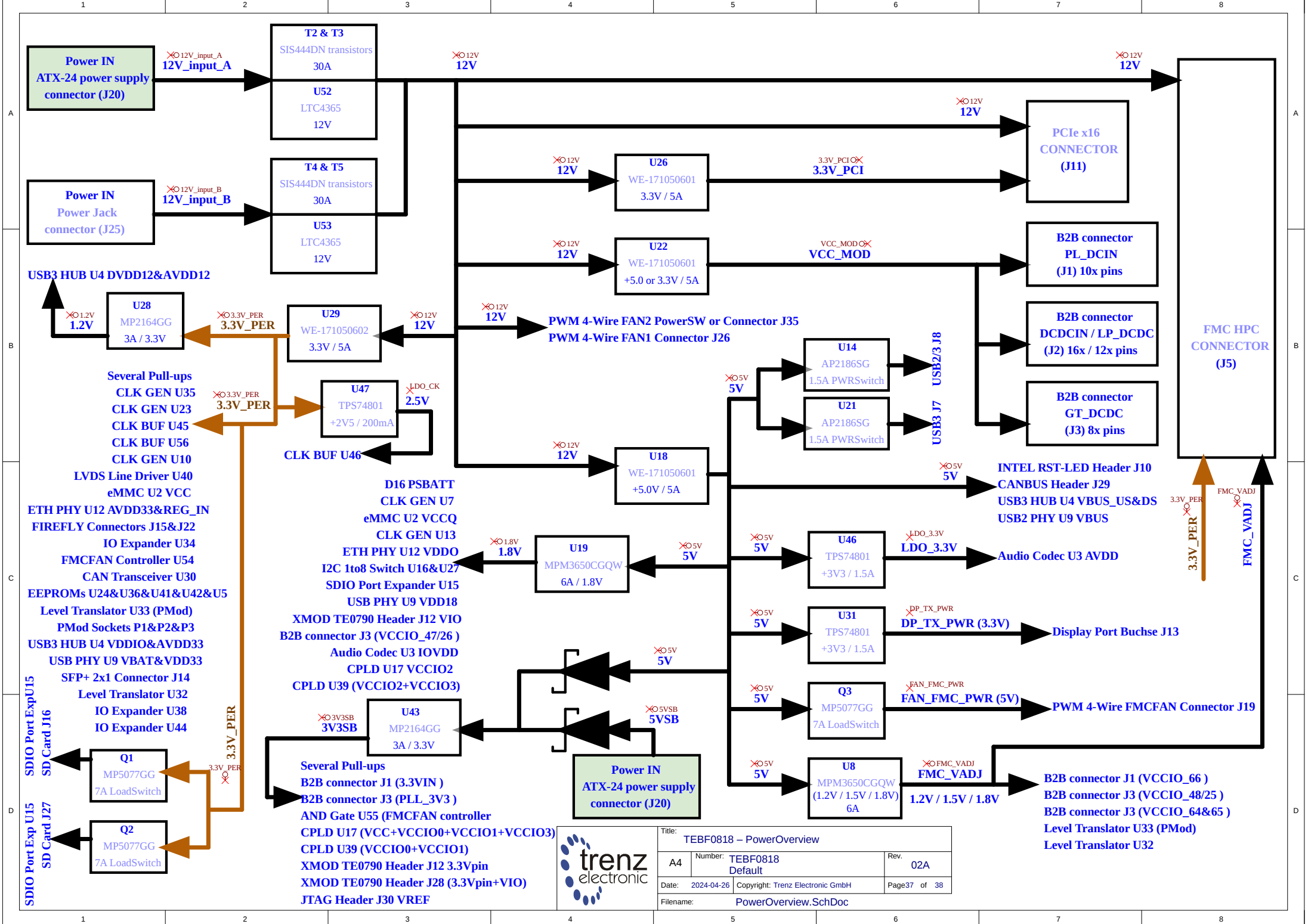
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Date: 2024-05-13	Copyright: Trenz Electronic GmbH		Rev. 02A
Filename: Power_1.schdoc		Page33 of 38	



Title: TEBF0818 - Power		
A4	Number: TEBF0818 Default	Rev. 02A
Date: 2024-05-13	Copyright: Trenz Electronic GmbH	
Filename: Power_3.schdoc		Page35 of 38



	Title: TEBF0818 - Power		
	A4	Number: TEBF0818 Default	Rev. 02A
	Date: 2024-05-13	Copyright: Trenz Electronic GmbH	Page36 of 38
	Filename: Power_4.schdoc		



1		2		3		4	
A	REV	Description					
	-01	Initial revision			VT		
	-02	1) Q1, Q2 and Q3 changed to MP5077GG (previous Load Switch is EOL), and their external components added. 2) U2 changed to SDINBDG4-8G-XI2 (previous eMMC is EOL). 3) R14 changed to 2KΩ. 4) J1, J2, J3 and J4 Connectors changed to ADF6-60-03.5-L-4-2-FR. 5) D8 and D10 updated (previous LEDs are EOL). 6) FAN M1 changed to HY45T-05A (previous FAN is EOL). 7) U54 (new) FAN Controller LM96163CISD/NOPB and its external components added. 8) Hardware (screws and nuts) for new FMC FAN updated. 9) J19 Connector changed to 4 Position type 2,54mm pitch. 10) Module orientation mark placed in Silkscreen Top Side. 11) R164 pull-up resistor on PGood of U19 added. 12) R41 changed to 200K and made it fitted in Default and A variants. 13) C93/C94 Capacitors changed to 0R Resistors (now R263/R264). 14) U56 (new) Differential 1:2 LVDS to HCSSL Buffer NB3L202KMN and its peripheral components added. 15) One U56 Buffered PCIe REFCLK Output connected to PS-GT BANK input MGTCLKREF in modules. 16) C179, C182, C185, C221, C222 and C223 Multipurpose MGT CLKs capacitors changed to 10nF. 17) C69 and C70 changed to 220nF. 18) T12 added as a Temperature Sensor and connected to U54. 19) L25 added. 20) U55 added. 21) C207, C39 and C82 changed to 10μF and 10V Voltage Rating. 22) U8 (FMC_VADJ) changed to MPM3650CGQW-Z (previous Regulator is EOL). 23) U19 (1.8V) changed to MPM3650CGQW-Z (previous Regulator is EOL). 24) U28 (1.2V) changed to MP2164GG (previous Regulator is EOL). 25) U43 (3V3SB) changed to MP2164GG (previous Regulator is EOL). 26) VID circuit for U8 (FMC_VADJ) implemented for 1.2V, 1.5V and 1.8V selection only. 27) Differential Pair Impedance Directive D90 set in PCIe RX and TX signal paths. 28) C121, C122, C123 and C124 FMC MGT CLKs capacitors changed to 10nF. 29) C197, C200, C202 and C203 SATA Capacitors changed to 10nF. 30) C72 and C73 changed to capacitors with a minimum of 10V Voltage Rating. 31) C263 capacitor added. 32) R175 set as not fitted in all variants. 33) R65 changed to 1KΩ. 34) Power overview added. 35) I2C net (SC_SDA, SC_SCL) for FMC FAN Controller connected to U27 and its slave address label added. 36) L8, L9, L11 and L12 changed to 4.7uH. 37) C47, C48, C50, C52, C57, C58, C60 and C62 changed to 100nF. 38) C49, C51, C59 and C61 changed to 22uF. 39) J14 SFP+ connector and its pull-ups resistors, now sourced from 3.3V_PER. 40) Differential Pair Impedance Directive D100 set where applicable. 41) CAN Bus Differential Pair Impedance Directive set to 120Ohms (H and L netnames modified to match). 42) R107 and R108 set as fitted in all variants. 43) C16 and C17 set as not fitted in all variants. 44) J6 pin connections updated 1-to-1 as follows: RX1P/N #NAME? B128_RX0_P/N, RX2P/N #NAME? B128_RX1_P/N RX3P/N #NAME? B128_RX2_P/N, RX4P/N #NAME? B128_RX3_P/N, TX1P/N #NAME? B128_TX0_P/N, TX2P/N #NAME? B128_TX1_P/N TX3P/N #NAME? B128_TX2_P/N, TX4P/N #NAME? B128_TX3_P/N. 45) J21 pin connections updated 1-to-1 as follows: FF_L1_TX to FF_L1_RX, FF_L2_TX to FF_L2_RX, FF_L3_TX to FF_L3_RX, FF_L4_TX to FF_L4_RX. 46) Pullups on T7 changed to 5VSB. 47) Serie Rgate and gate pull-down Resistor added to T7 and T8. 48) Shield of all J7 connectors (USB and ETH) tied together to FGND net. 49) Shield of J13 connected to FGND2 net. 50) Cage of J14 connected to FGND3 net. 51) PCB Rules updated. 52) Signals layout updated. 53) LayerStack improved. 54) Polygons updated. 55) UKCA marking added.			GHC, VT		
B							
C							
D							
	-02A	1) Added additional label on the Top Overlay layer, removed label "TEBF0818-02" from TOP layer 2) Removed polygon cutout from Top solder mask			VT		



Title:TEBF0818 - Revision history

A4

Number:TEBF0818Default

Rev.02A

Date:2024-05-13

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Page38 of 38

Filename:Revision_Changes.SchDoc

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