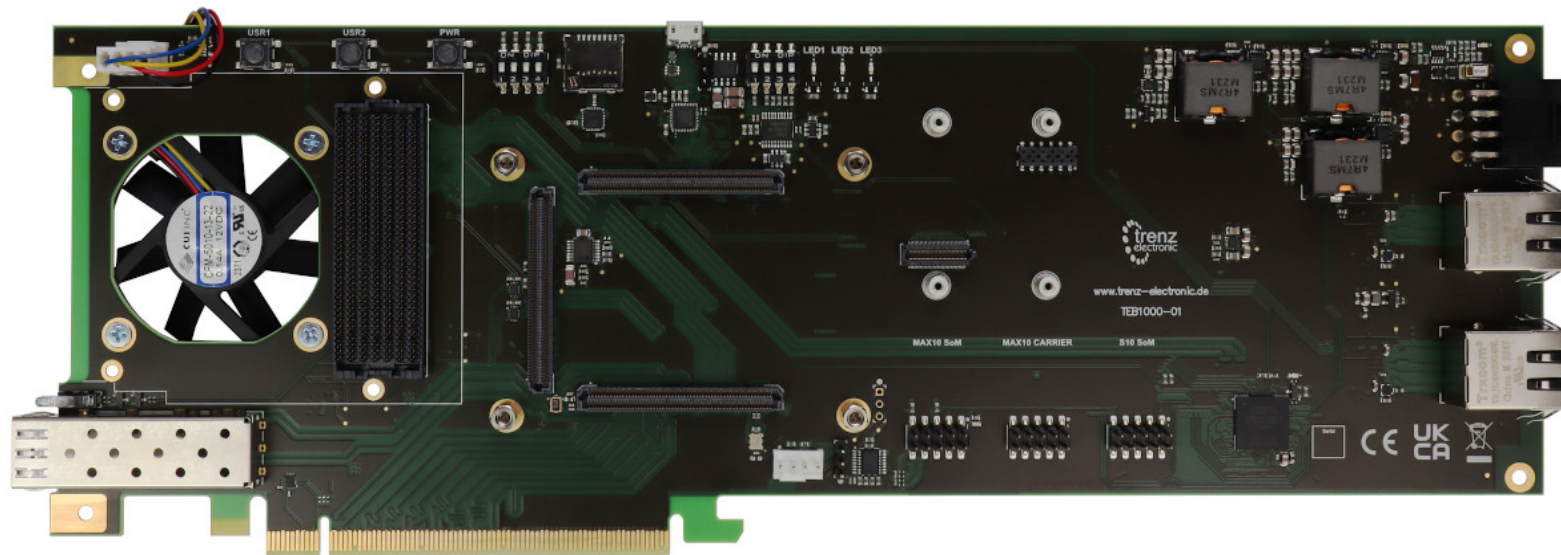




The view may differ depending on the revision and assembly version.

Regarding the usage of our schematics and alike documentation for Trenz baseboard TEB1000.

Baseboard is Open Source Hardware and therefore free to use, adapt and can be modified to suit your needs as described in the "The MIT License".

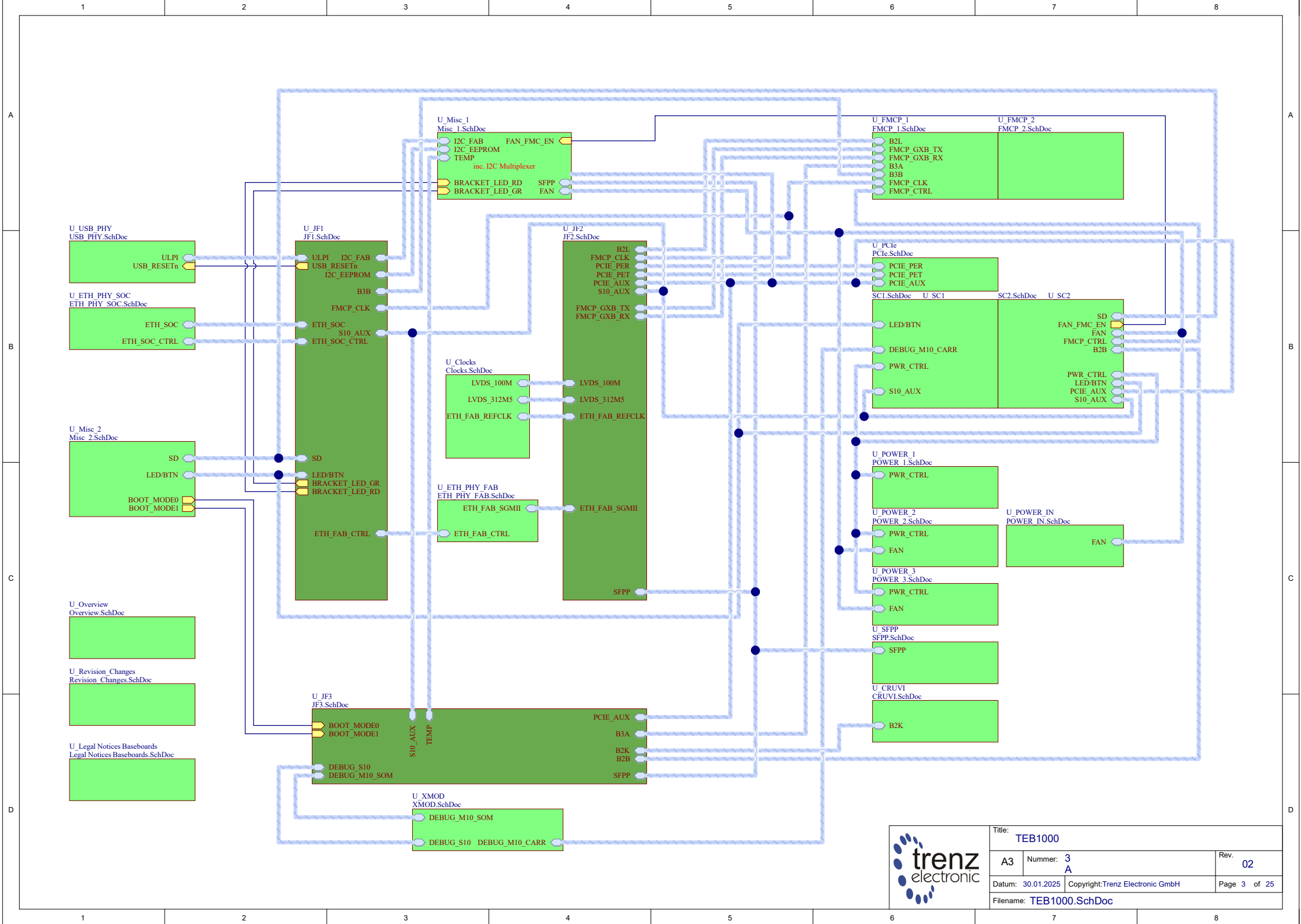
Permission is hereby granted, free of charge, to any person obtaining a copy of this hardware and associated documentation files (the "Product"), to deal in the Product without restriction, including without limitation the rights to use, copy, modify, merge, publish, distribute, sublicense, and/or sell copies of the Product, and to permit persons to whom the Product is furnished to do so.

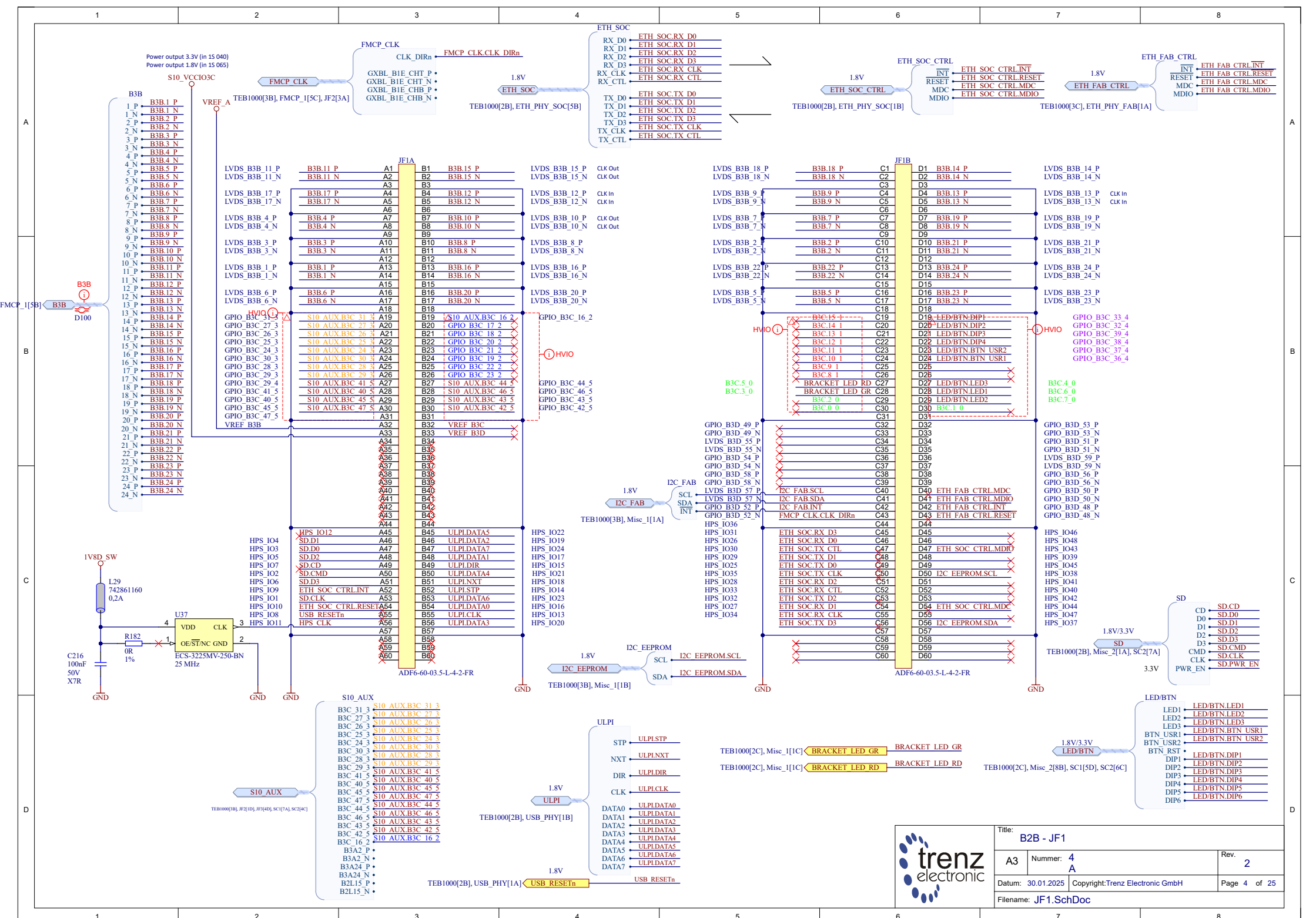
|                  |   |
|------------------|---|
| Drawn by         | * |
| Checked by       | * |
| Assembly variant | A |
| Created by       |   |
| Modified by      |   |
| Modified at      |   |



|                                           |                                       |              |
|-------------------------------------------|---------------------------------------|--------------|
| Title:                                    |                                       |              |
| A4                                        | Number: 1<br>A                        | Rev. 01      |
| Date: *                                   | Copyright: Trenz Electronic GmbH / TT | Page 1 of 25 |
| Filename: Legal Notices Baseboards.SchDoc |                                       |              |

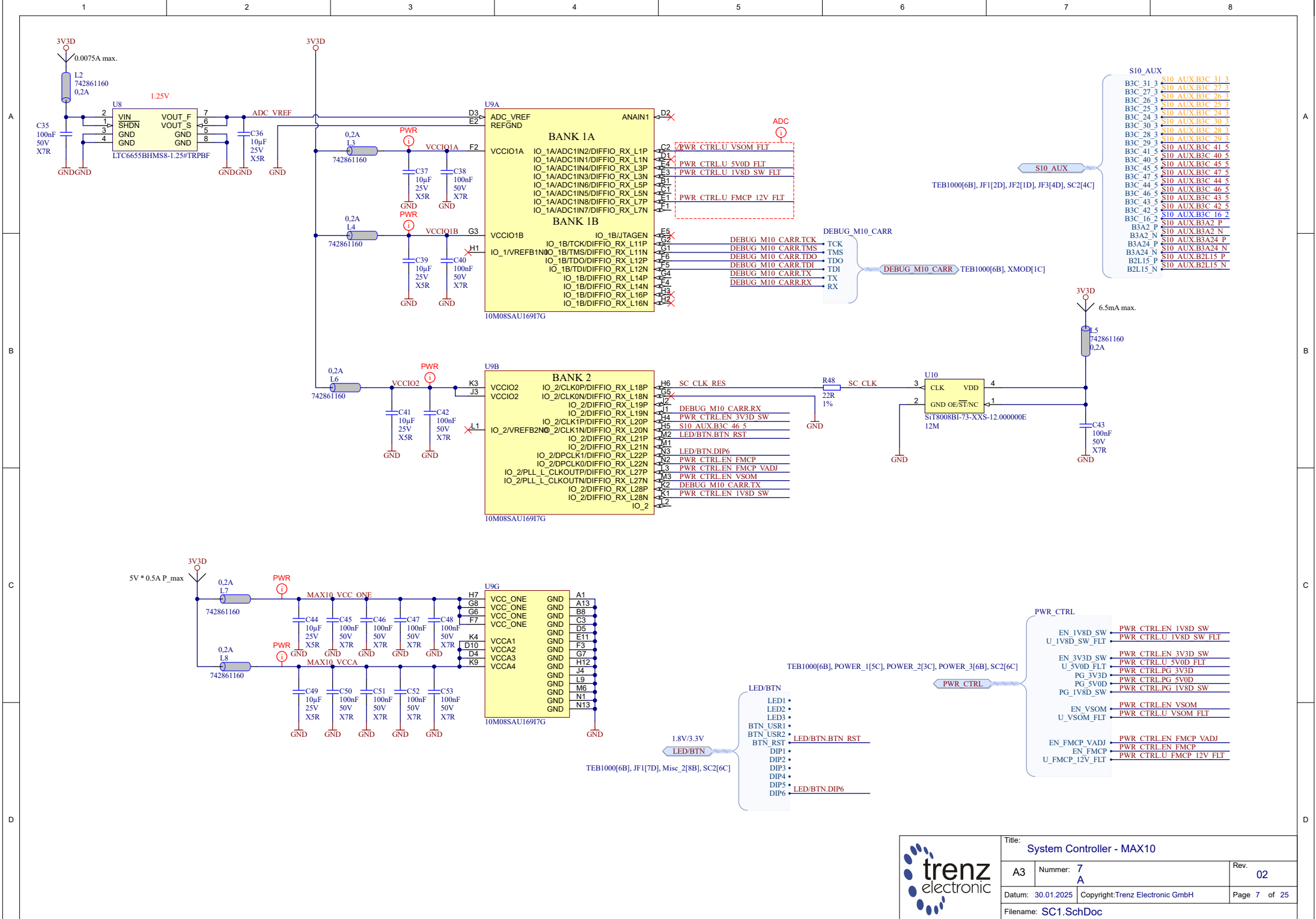


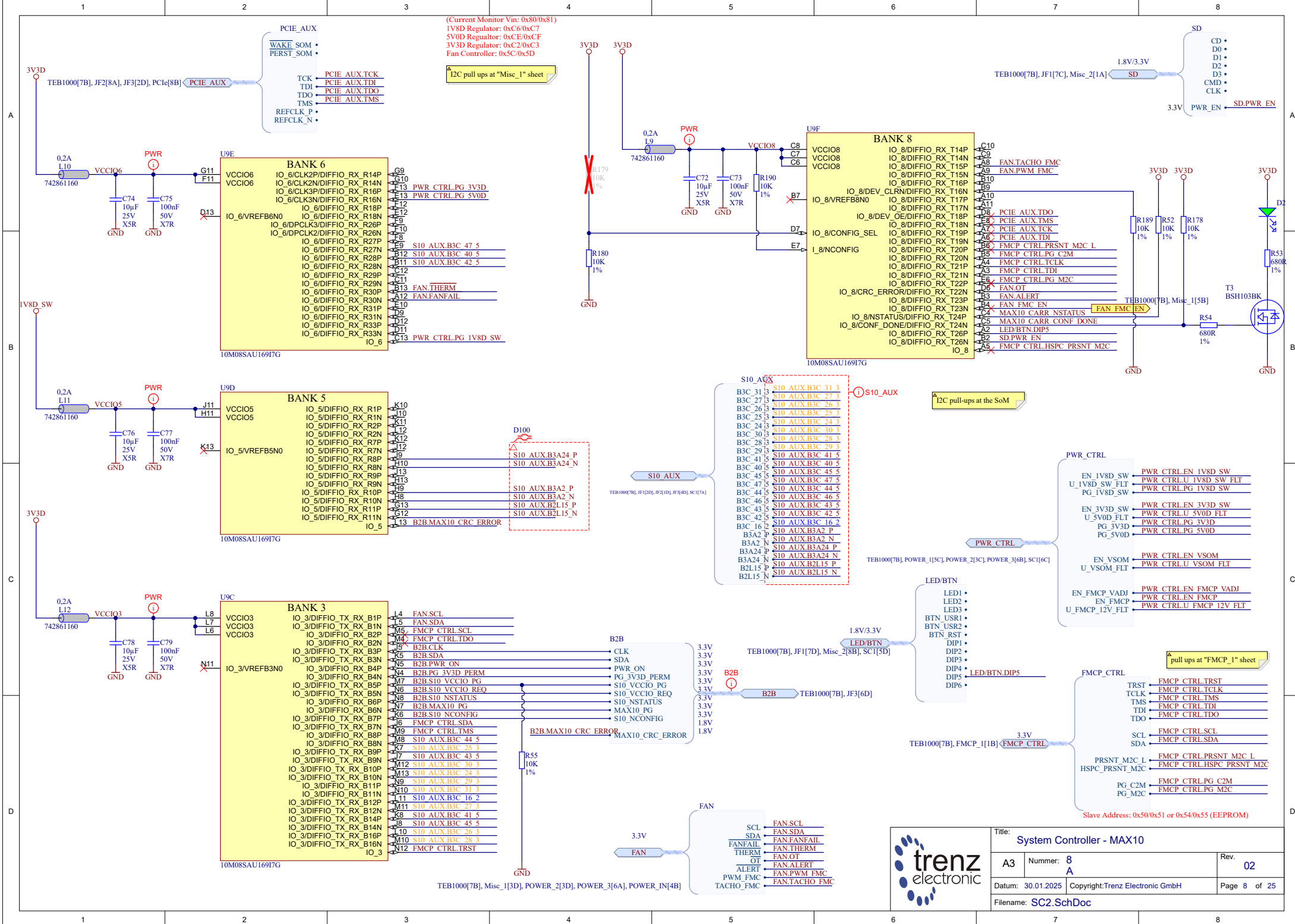












A

B

C

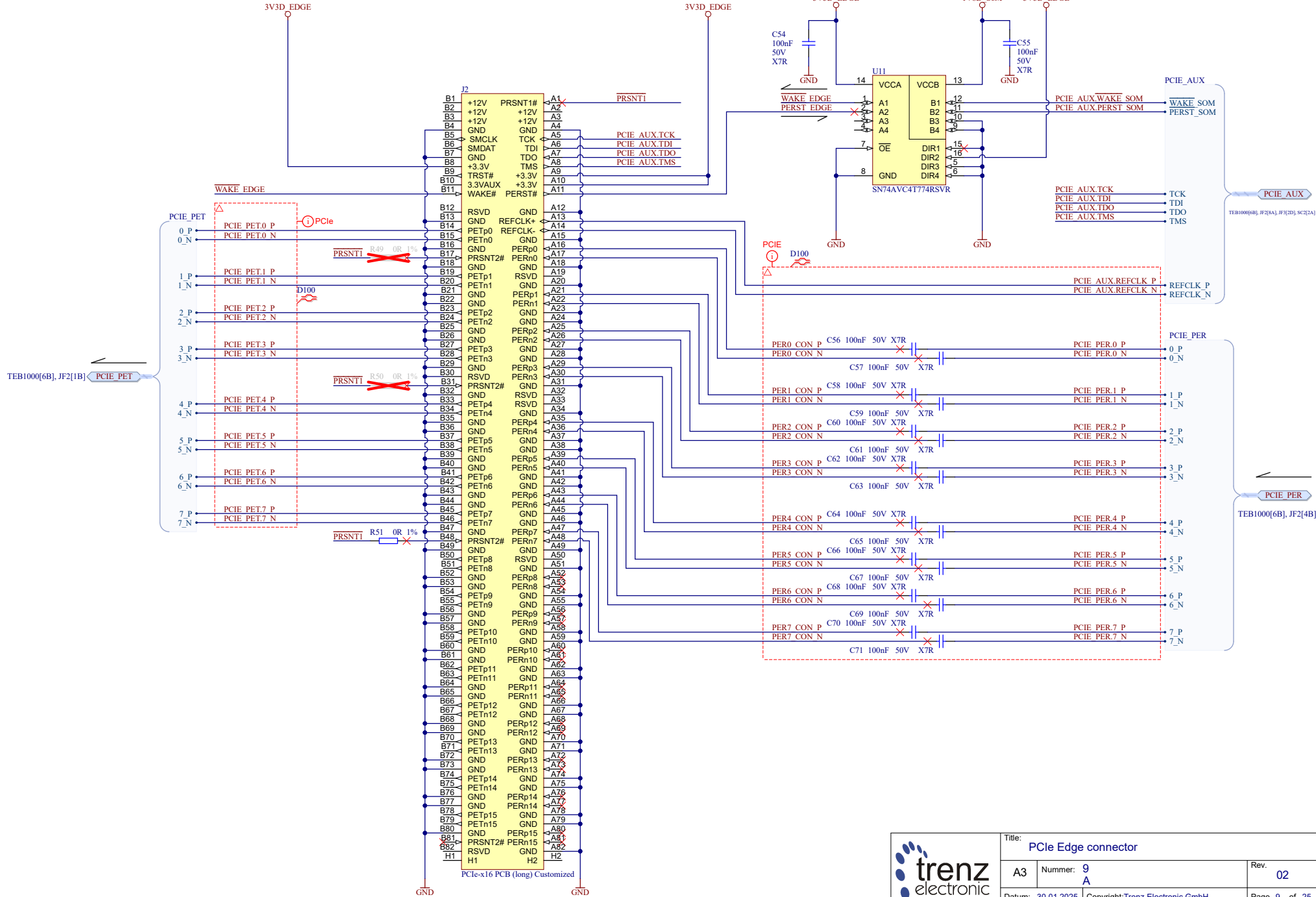
D

A

B

C

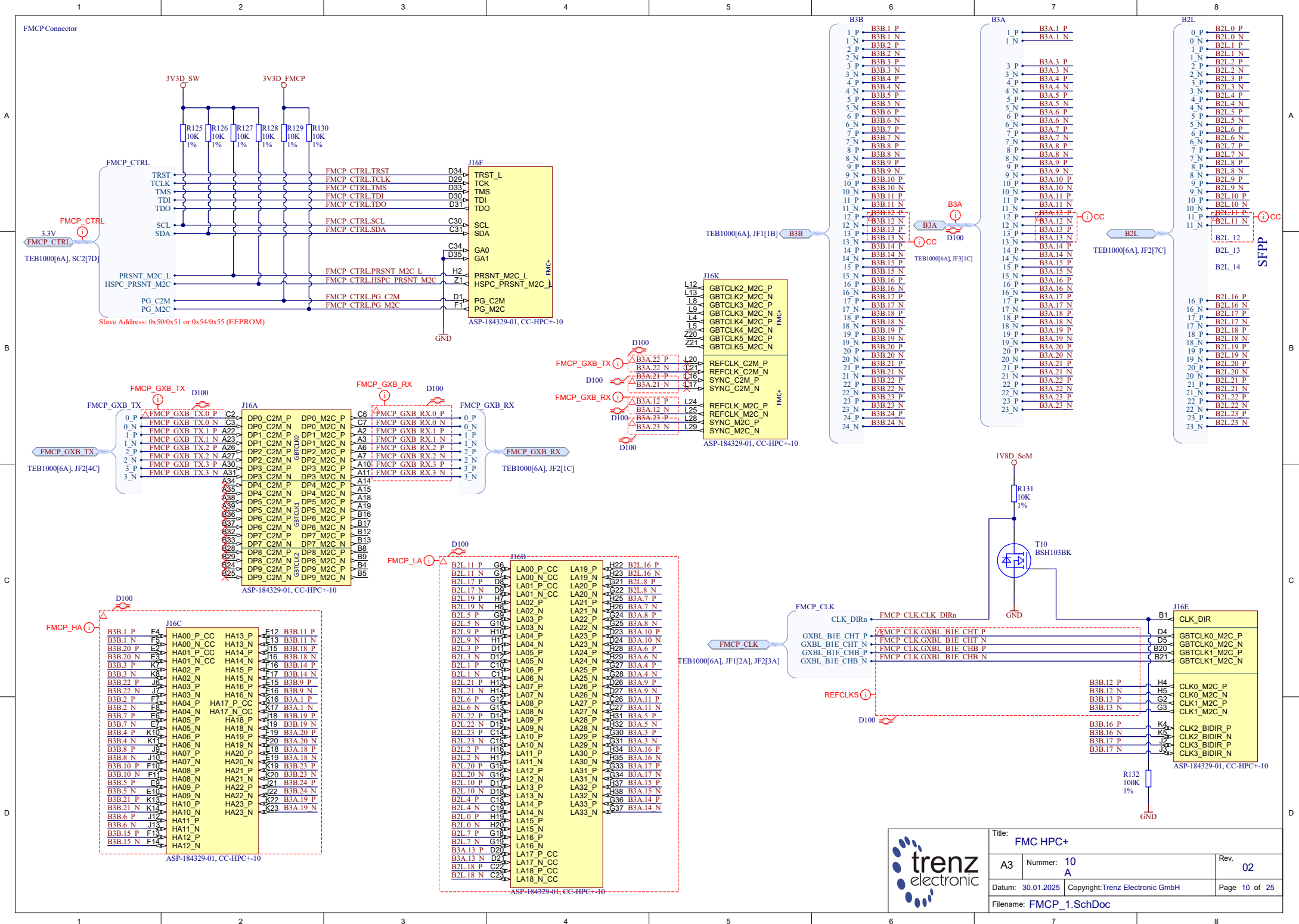
D



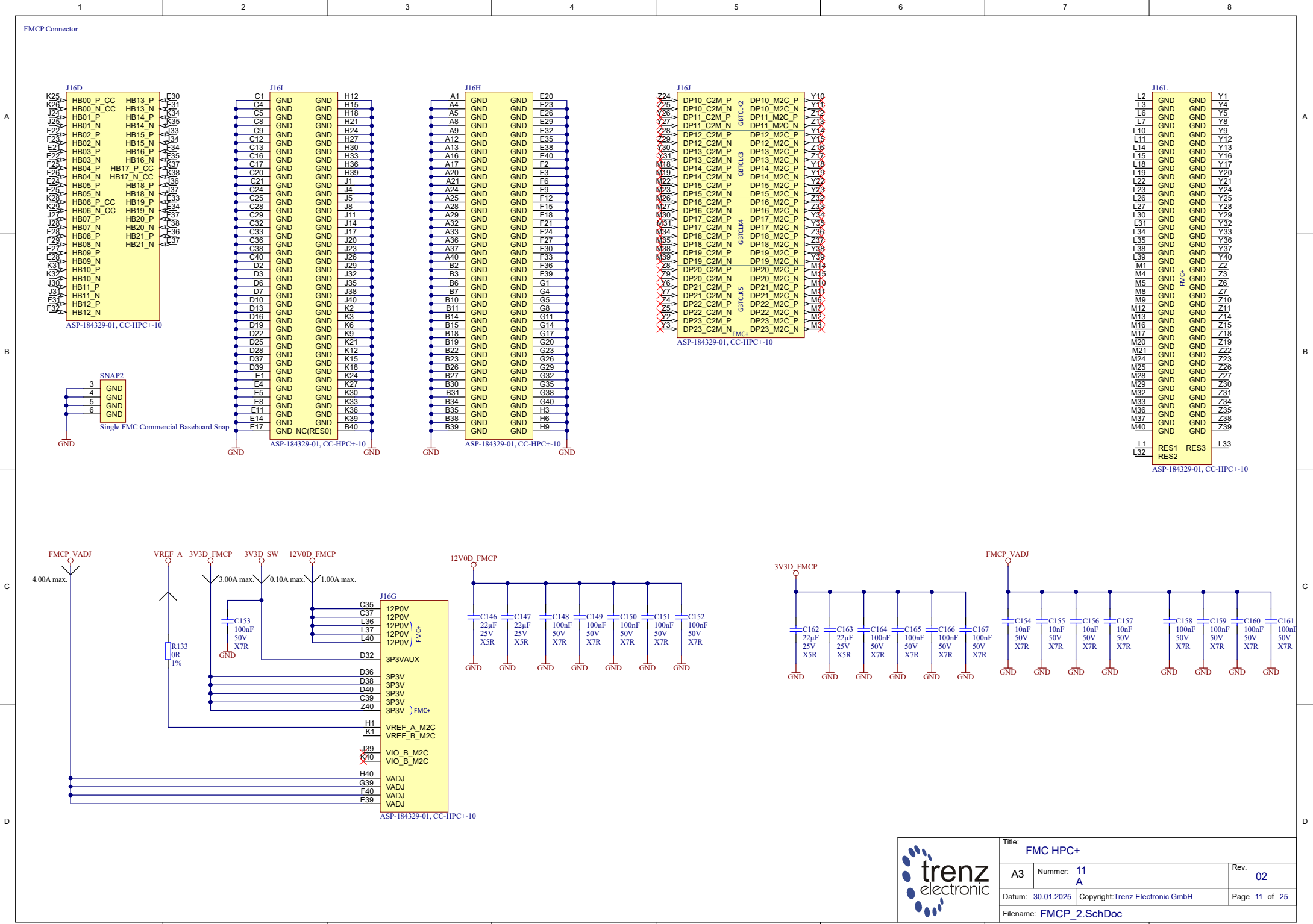


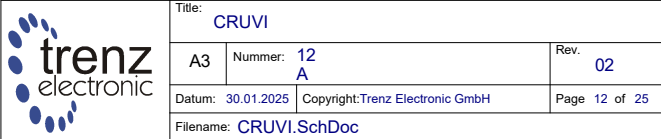
Title: **PCIe Edge connector**

|                              |                    |                                  |
|------------------------------|--------------------|----------------------------------|
| A3                           | Number: <b>9 A</b> | Rev. <b>02</b>                   |
| Datum: 30.01.2025            |                    | Copyright: Trenz Electronic GmbH |
| Filename: <b>PCIe.SchDoc</b> |                    | Page 9 of 25                     |



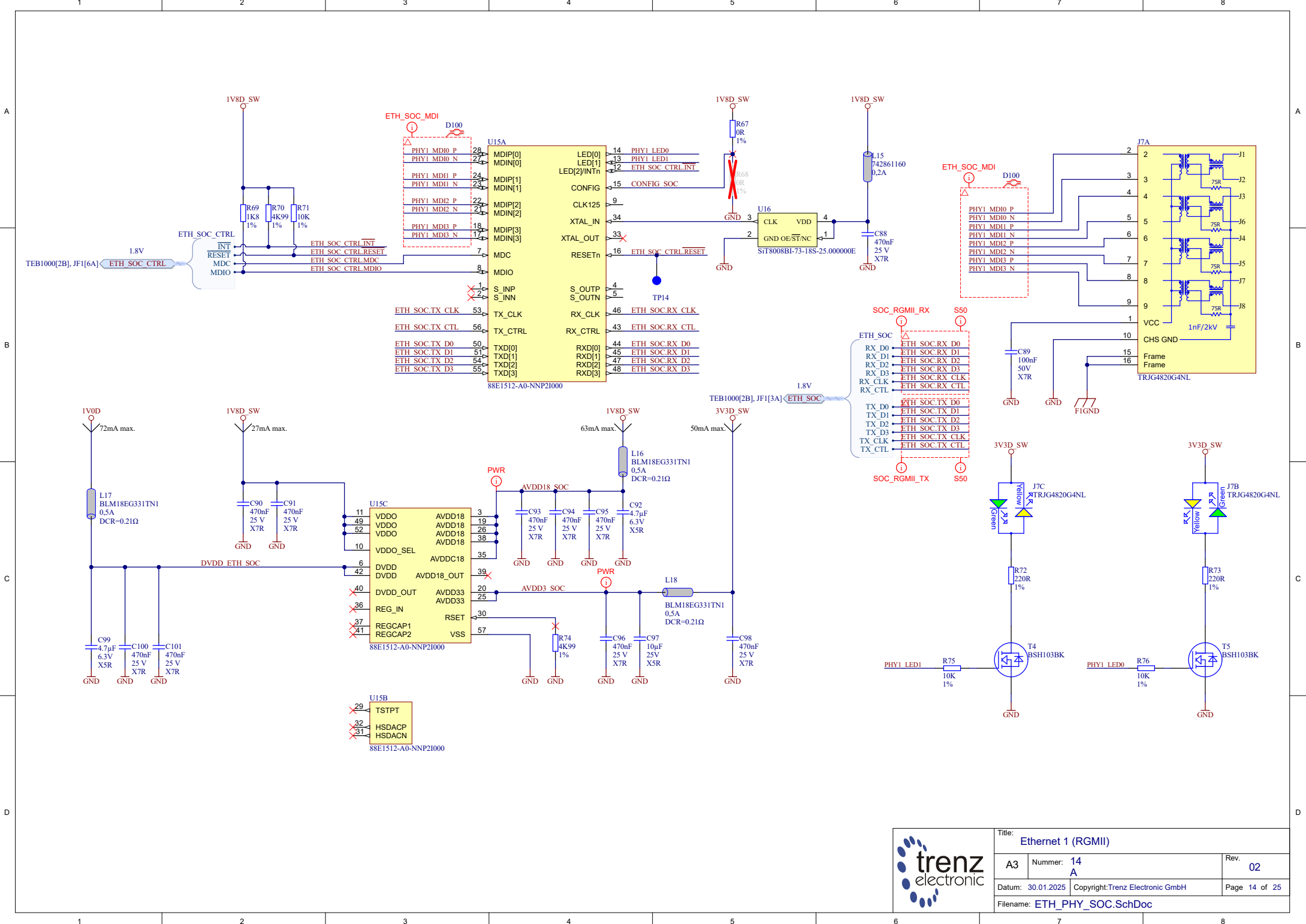
|                         |              |                                  |               |
|-------------------------|--------------|----------------------------------|---------------|
| Title: FMC HPC+         |              |                                  |               |
| A3                      | Number: 10 A | Rev. 02                          |               |
| Datum: 30.01.2025       |              | Copyright: Trenz Electronic GmbH | Page 10 of 25 |
| Filename: FMCP_1.SchDoc |              |                                  |               |



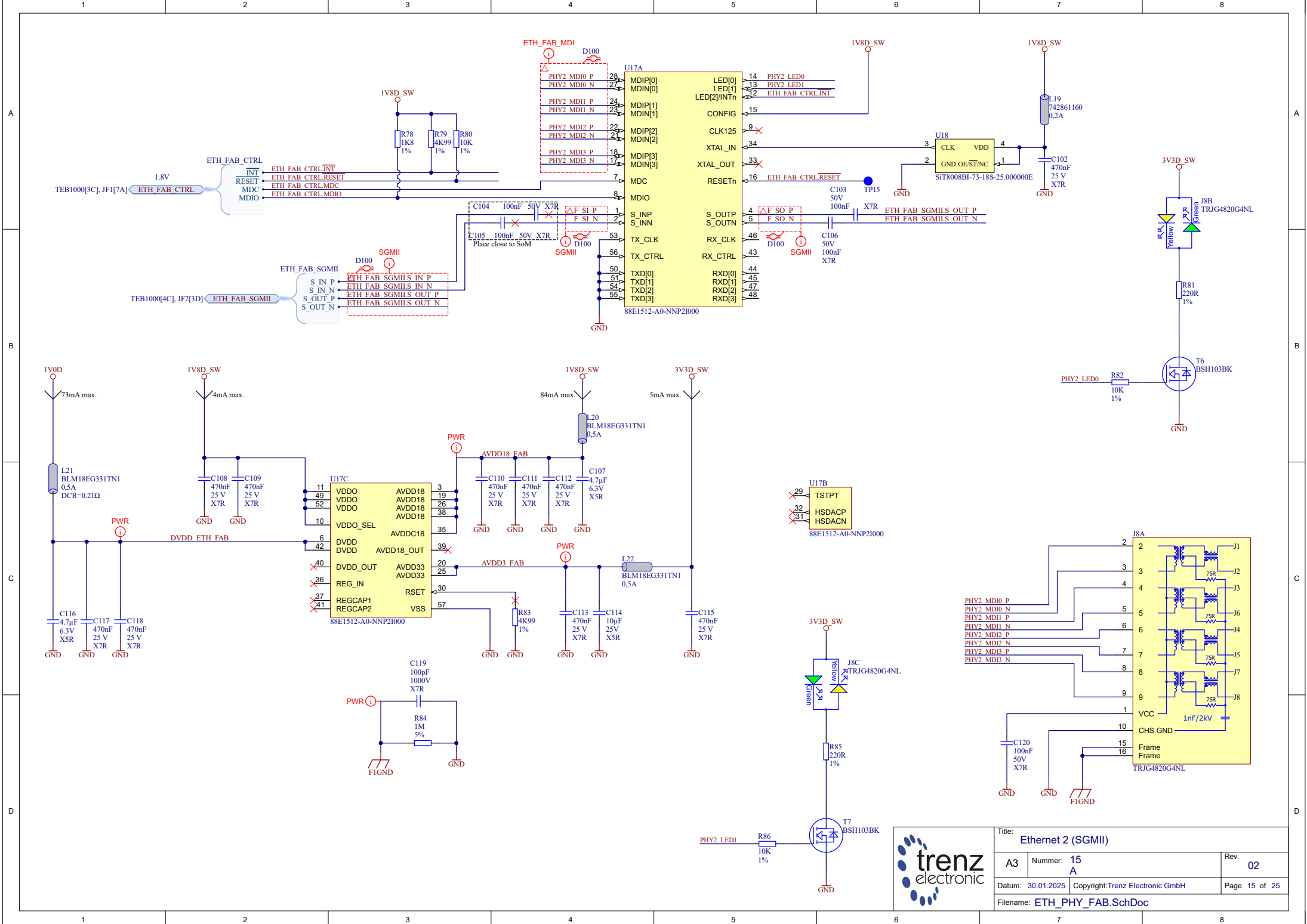




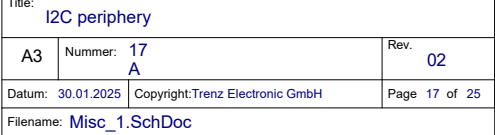
|                                 |                     |                                         |                             |
|---------------------------------|---------------------|-----------------------------------------|-----------------------------|
| Title: <b>USB 2.0</b>           |                     |                                         |                             |
| A3                              | Nummer: <b>13 A</b> | Rev.                                    | <b>02</b>                   |
| Datum: <b>30.01.2025</b>        |                     | Copyright: <b>Trenz Electronic GmbH</b> | Page <b>13</b> of <b>25</b> |
| Filename: <b>USB_PHY.SchDoc</b> |                     |                                         |                             |



|                              |                                  |               |
|------------------------------|----------------------------------|---------------|
| Title: Ethernet 1 (RGMII)    |                                  |               |
| A3                           | Number: 14 A                     | Rev. 02       |
| Datum: 30.01.2025            | Copyright: Trenz Electronic GmbH | Page 14 of 25 |
| Filename: ETH_PHY_SOC.SchDoc |                                  |               |

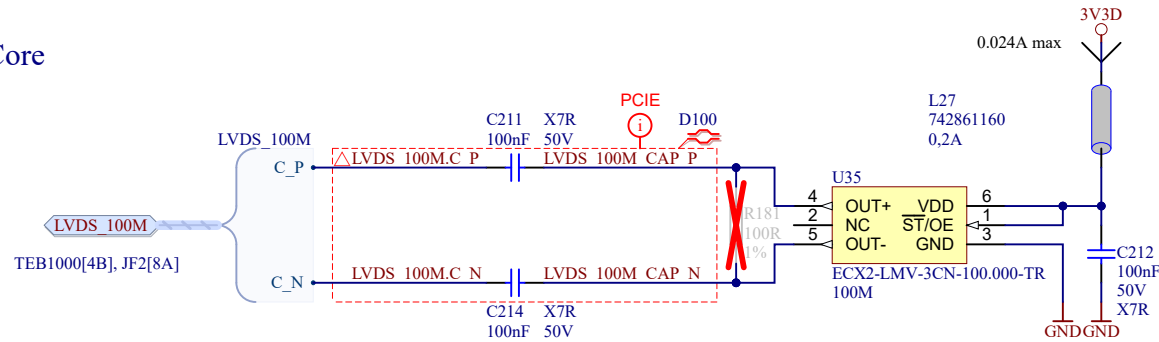




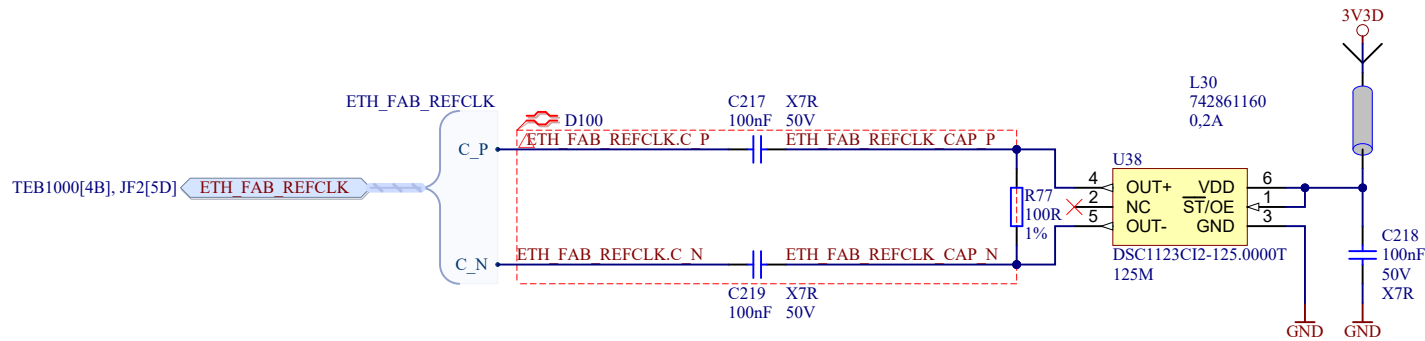




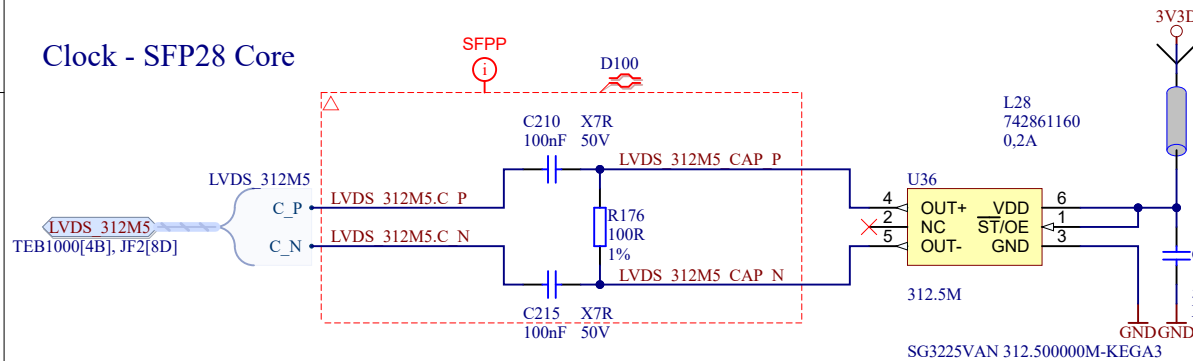
## Clock - PCIe Core



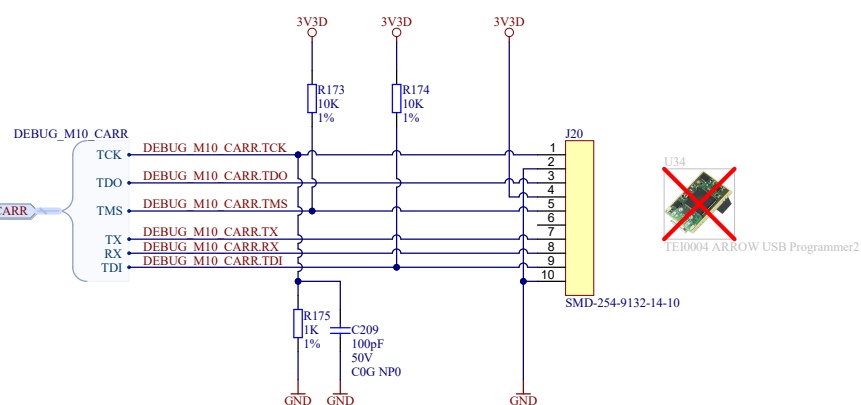
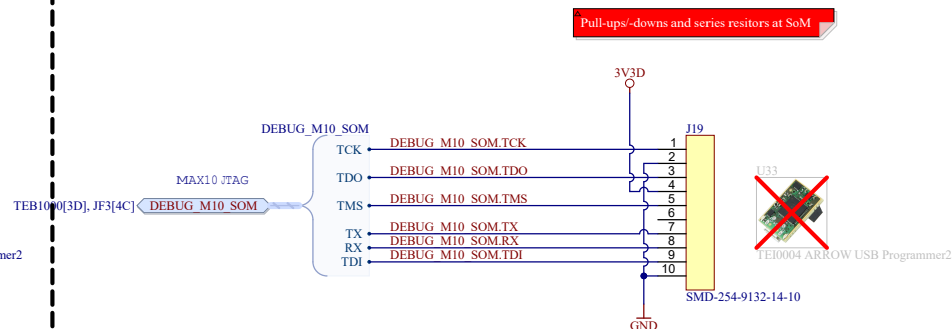
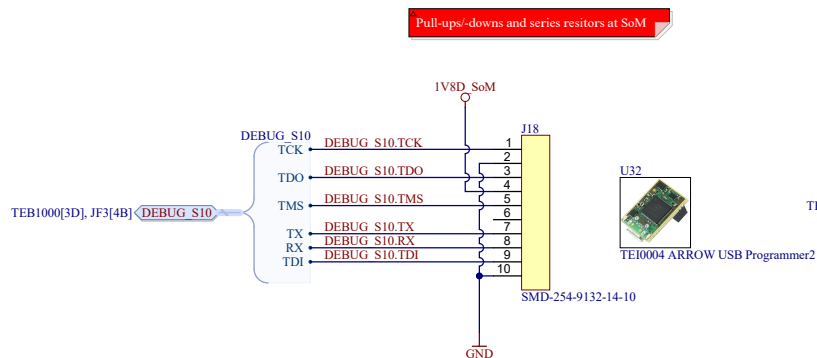
## Clock - SGMII Core

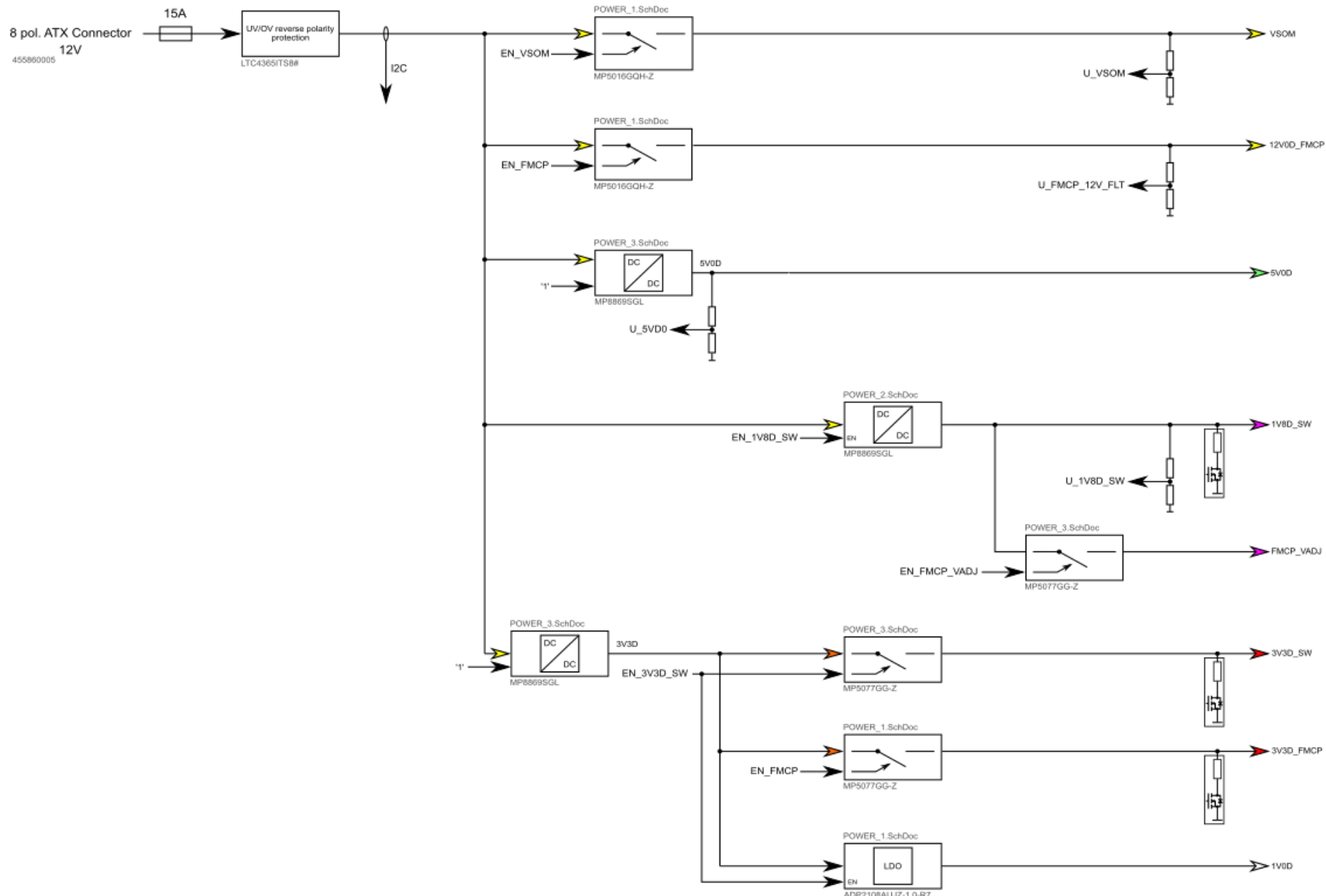


## Clock - SFP28 Core



|                         |                                  |               |
|-------------------------|----------------------------------|---------------|
| Title: Clock            |                                  |               |
| A4                      | Number: 19<br>A                  | Rev. 02       |
| Date: 29.01.2025        | Copyright: Trenz Electronic GmbH | Page 19 of 25 |
| Filename: Clocks.SchDoc |                                  |               |





|                           |                                  |               |
|---------------------------|----------------------------------|---------------|
| Title: Power diagram      |                                  |               |
| A3                        | Number: 21<br>A                  | Rev. 02       |
| Datum: 30.01.2025         | Copyright: Trenz Electronic GmbH | Page 21 of 25 |
| Filename: Overview.SchDoc |                                  |               |

