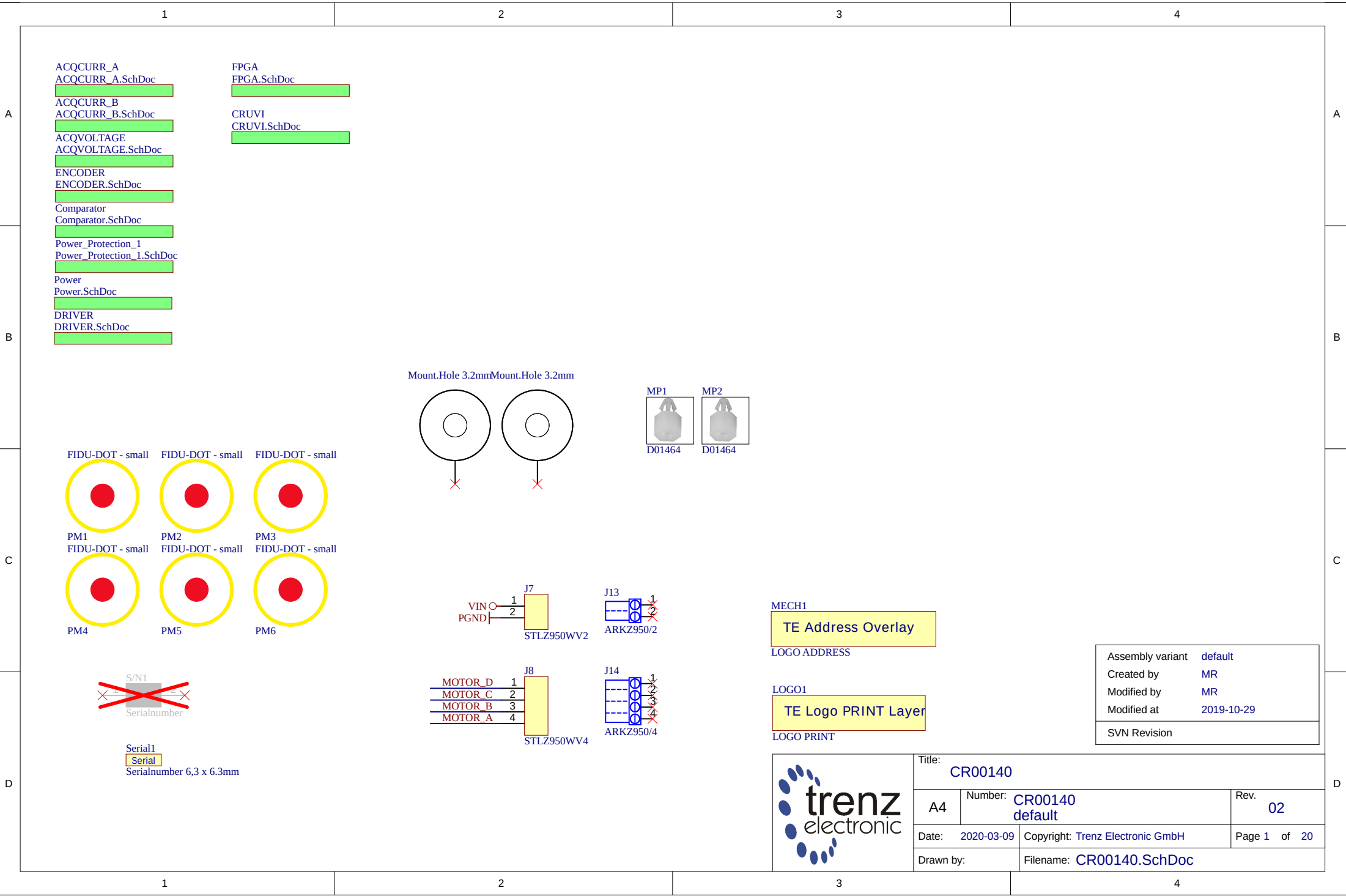
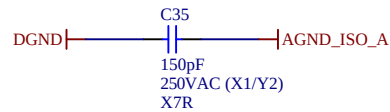
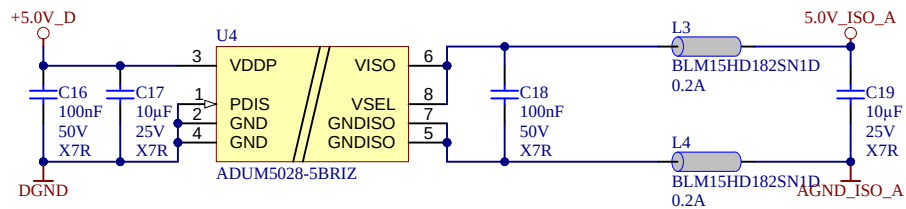
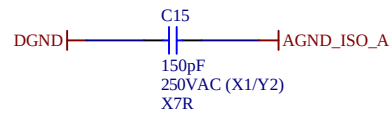
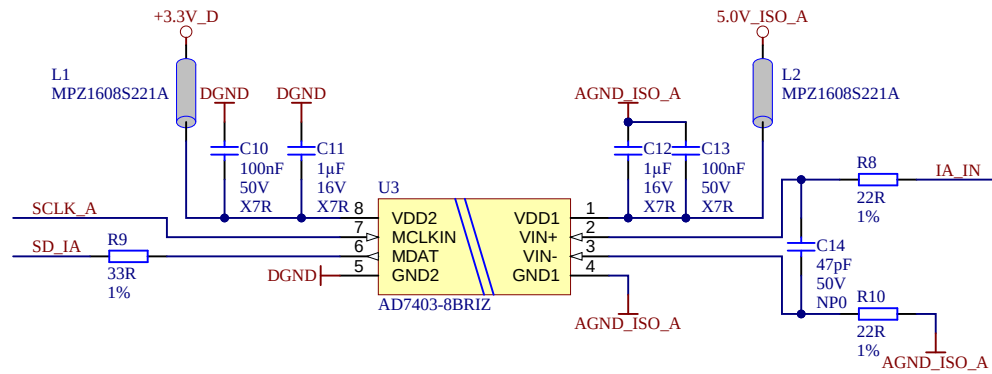
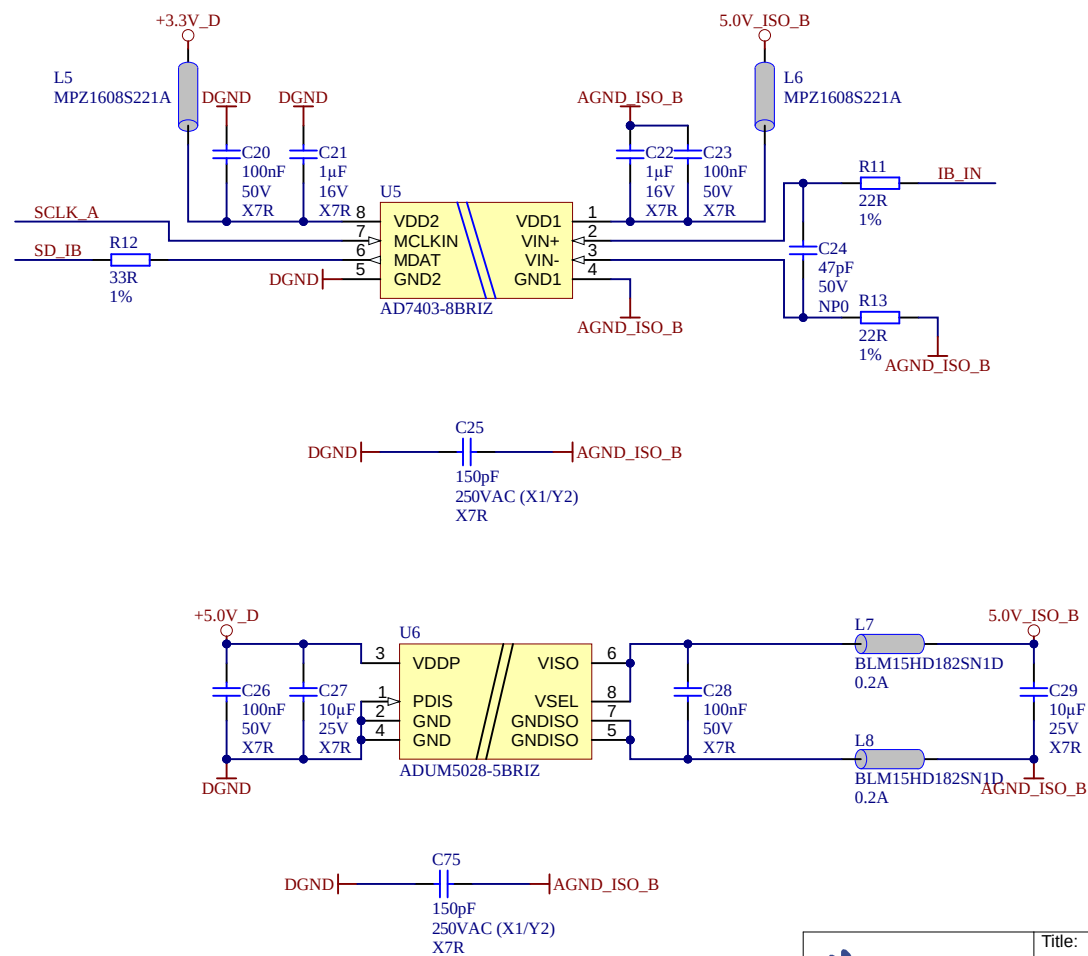


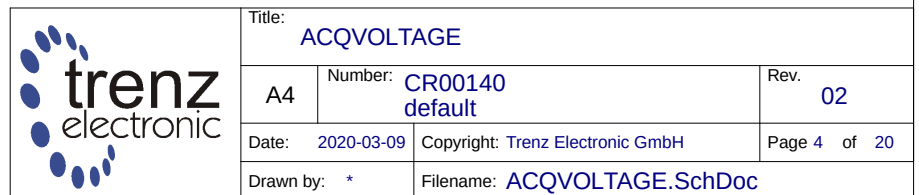


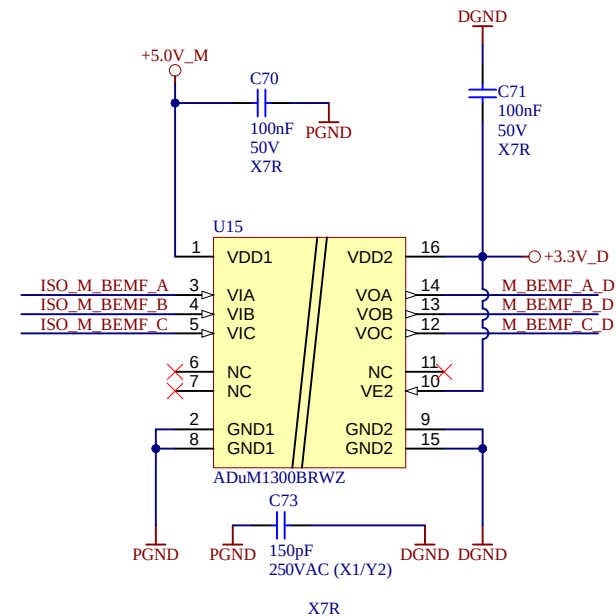
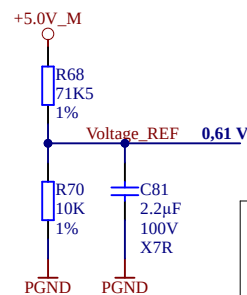
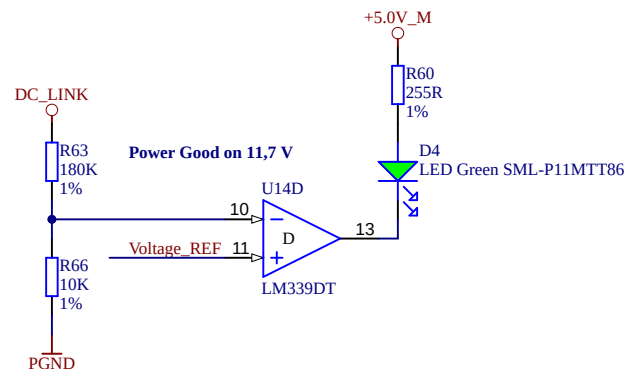
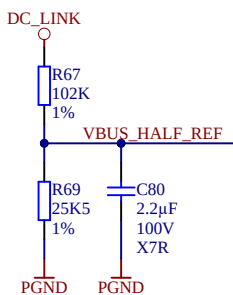
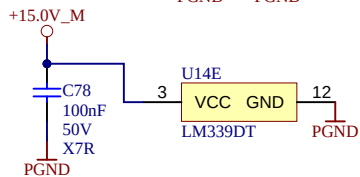
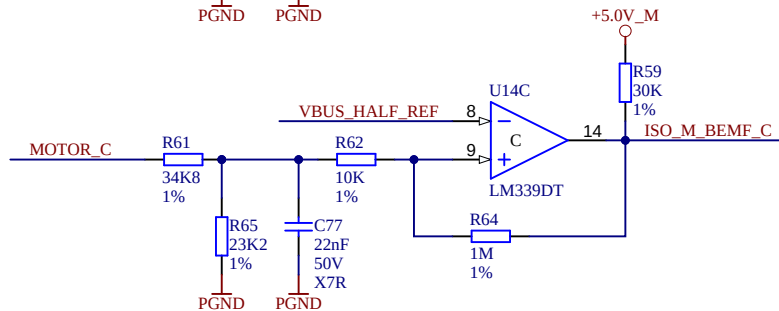
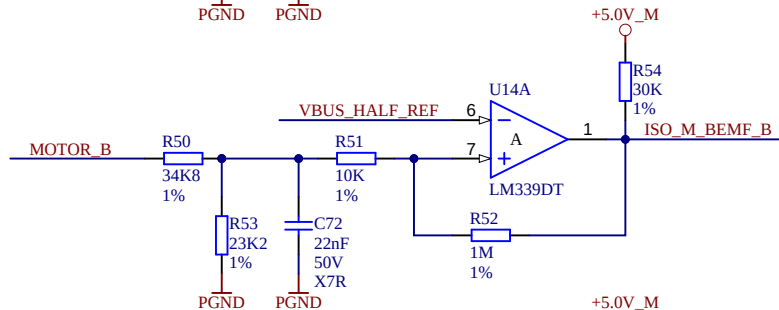
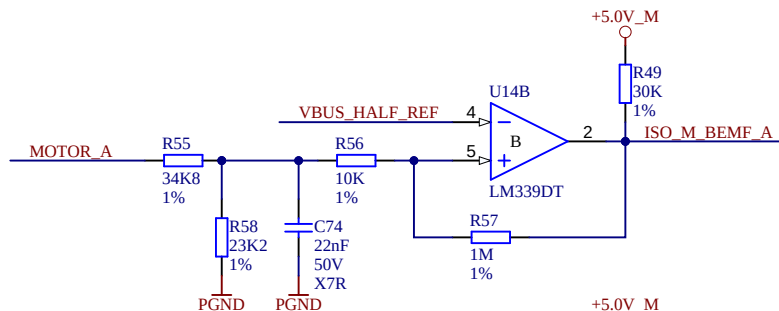


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		Number: CR00140 default	Rev. 02
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Drawn by: *	Filename: ACQCURR_A.SchDoc		



	Title: ACQCURR_B	
	A4	Number: CR00140 default
	Date: 2020-03-09	Copyright: Trenz Electronic GmbH
	Drawn by: *	Filename: ACQCURR_B.SchDoc
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Title: Comparator		
A4	Number: CR00140 default	Rev. 02
Date: 2020-03-09	Copyright: Trenz Electronic GmbH	Page 5 of 20
Drawn by: *	Filename: Comparator.SchDoc	

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DRIVER_A
DRIVER_A.SchDoc

DRIVER_B
DRIVER_B.SchDoc

DRIVER_C
DRIVER_C.SchDoc

DRIVER_D
DRIVER_D.SchDoc

A

A

B

E

C

C

D

□



Title:

DRIVER

A4

Number:	CR00140 default
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Rev.	02
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Date: 2020-03-09

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Drawn by: *

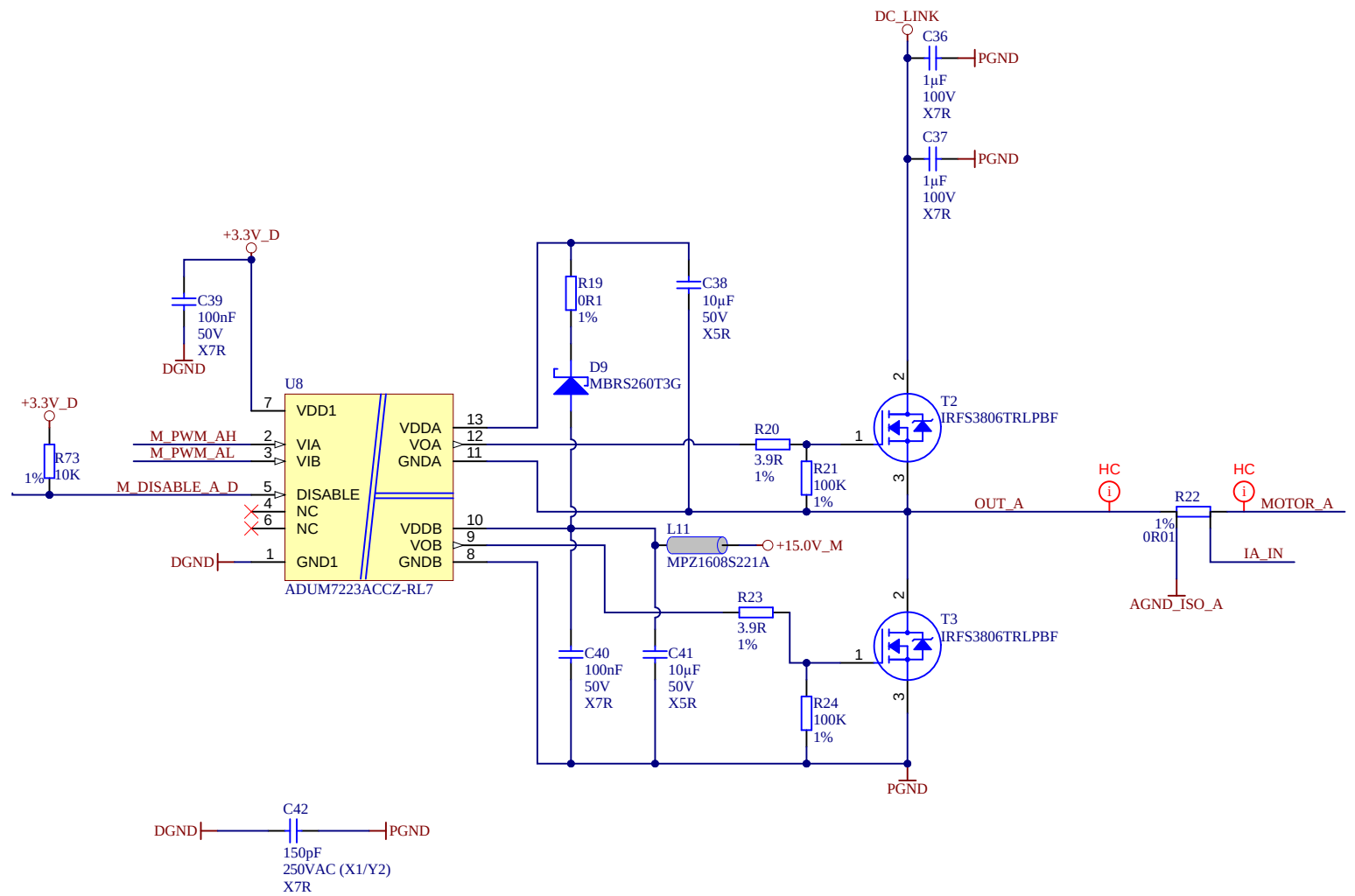
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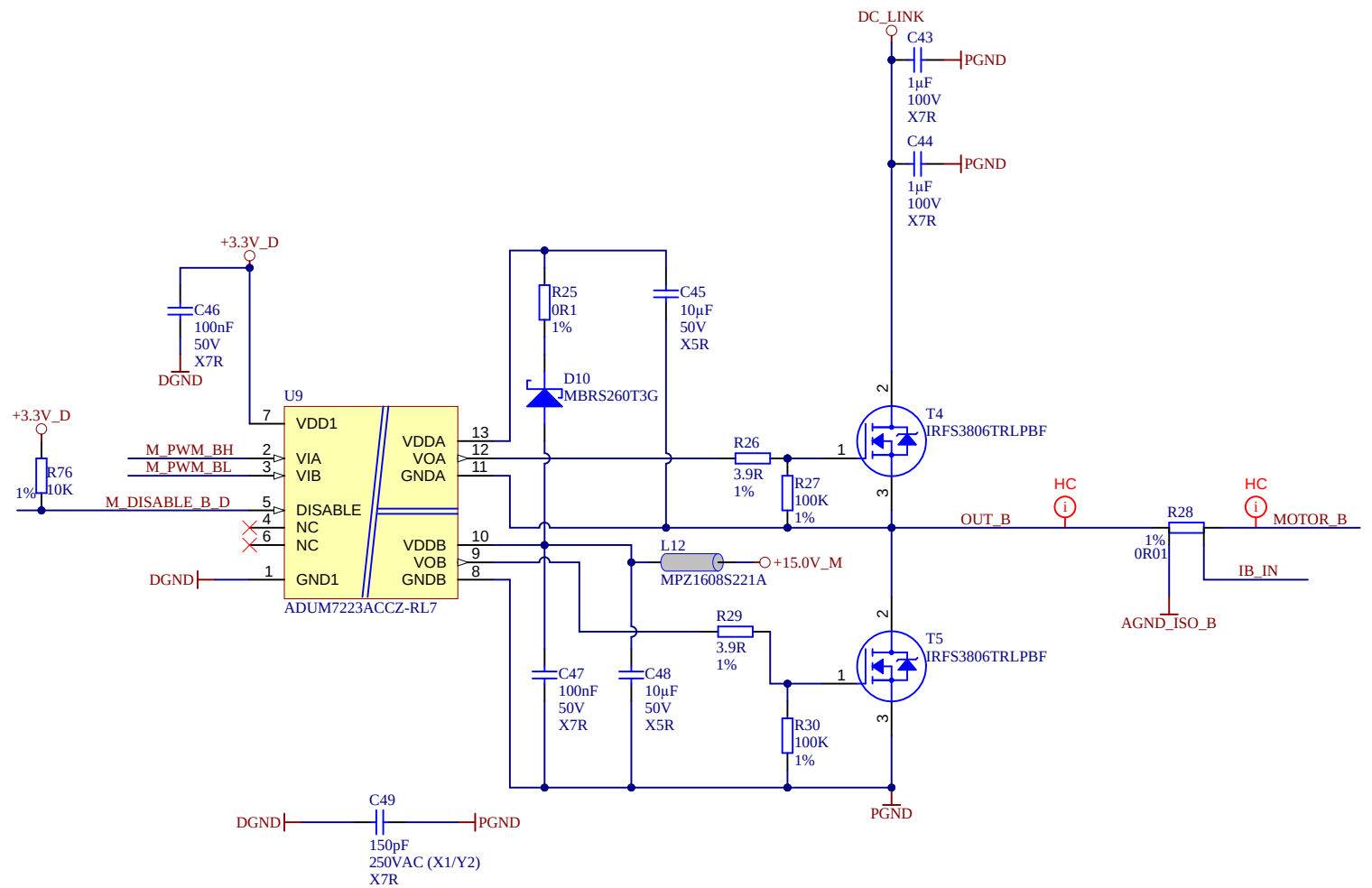
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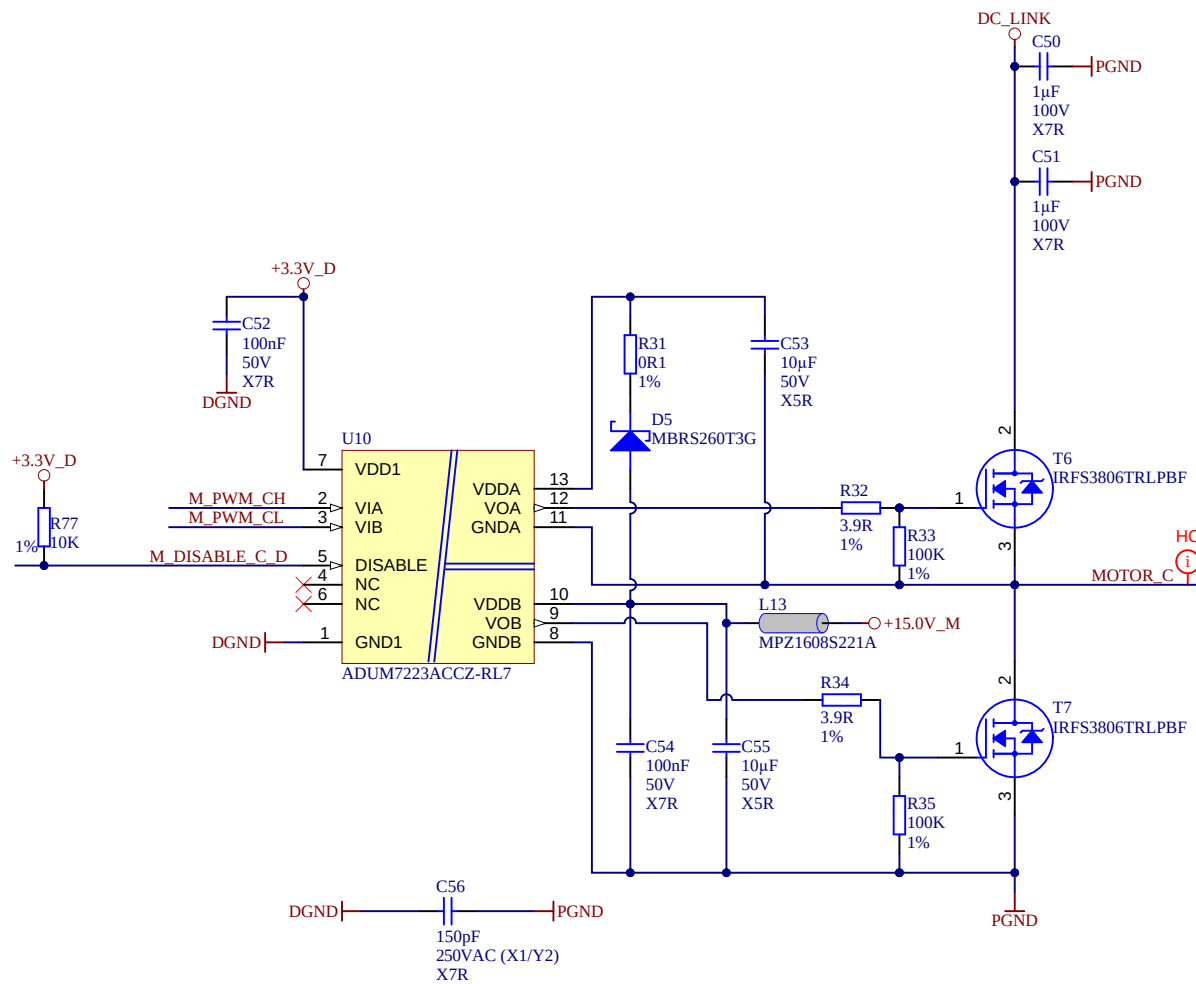


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	Date: 2020-03-09	Copyright: Trenz Electronic GmbH	Page 7 of 20
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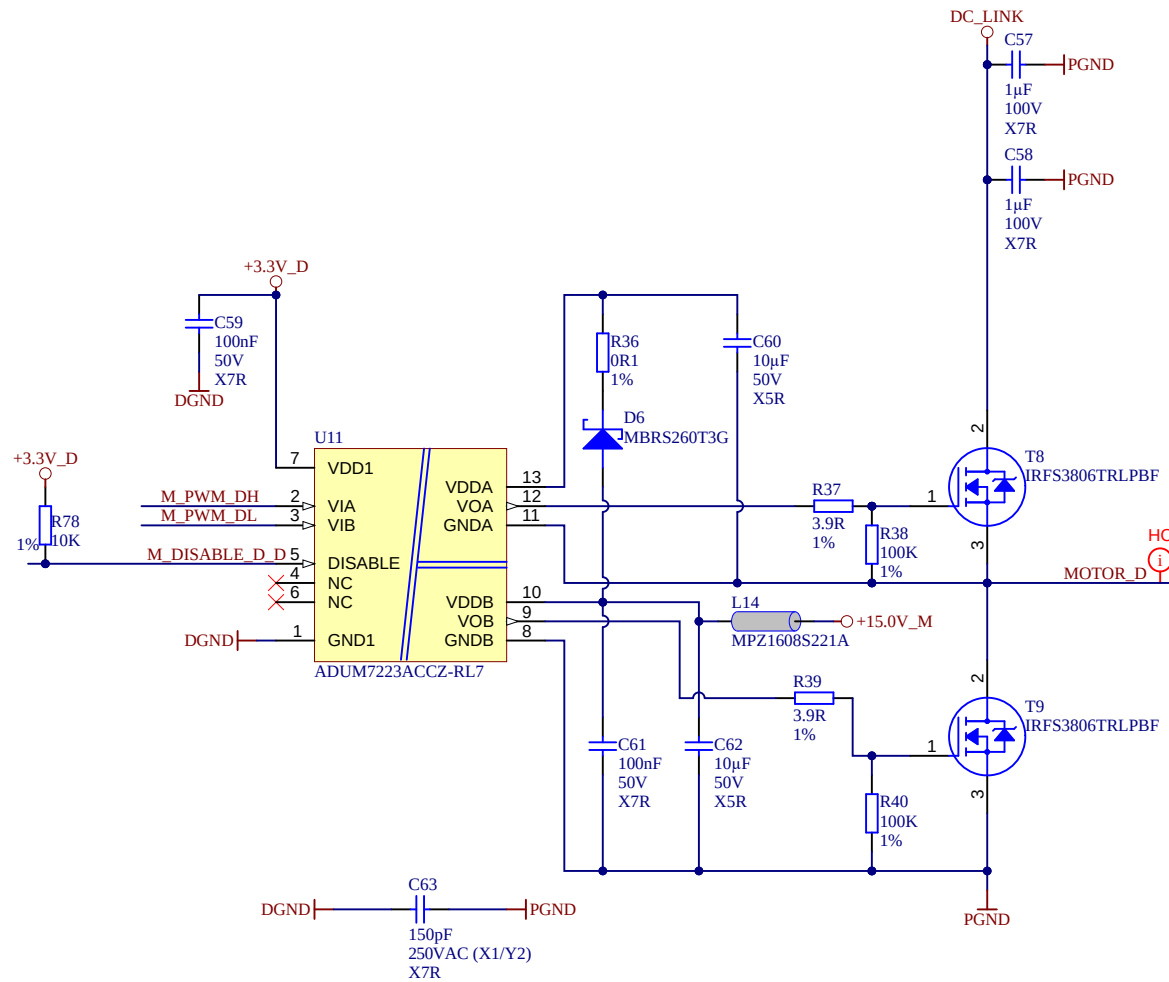


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Date:	2020-03-09	Copyright: Trenz Electronic GmbH	Page 8 of 20
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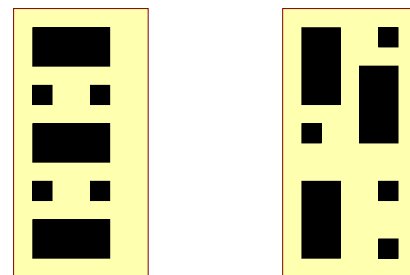
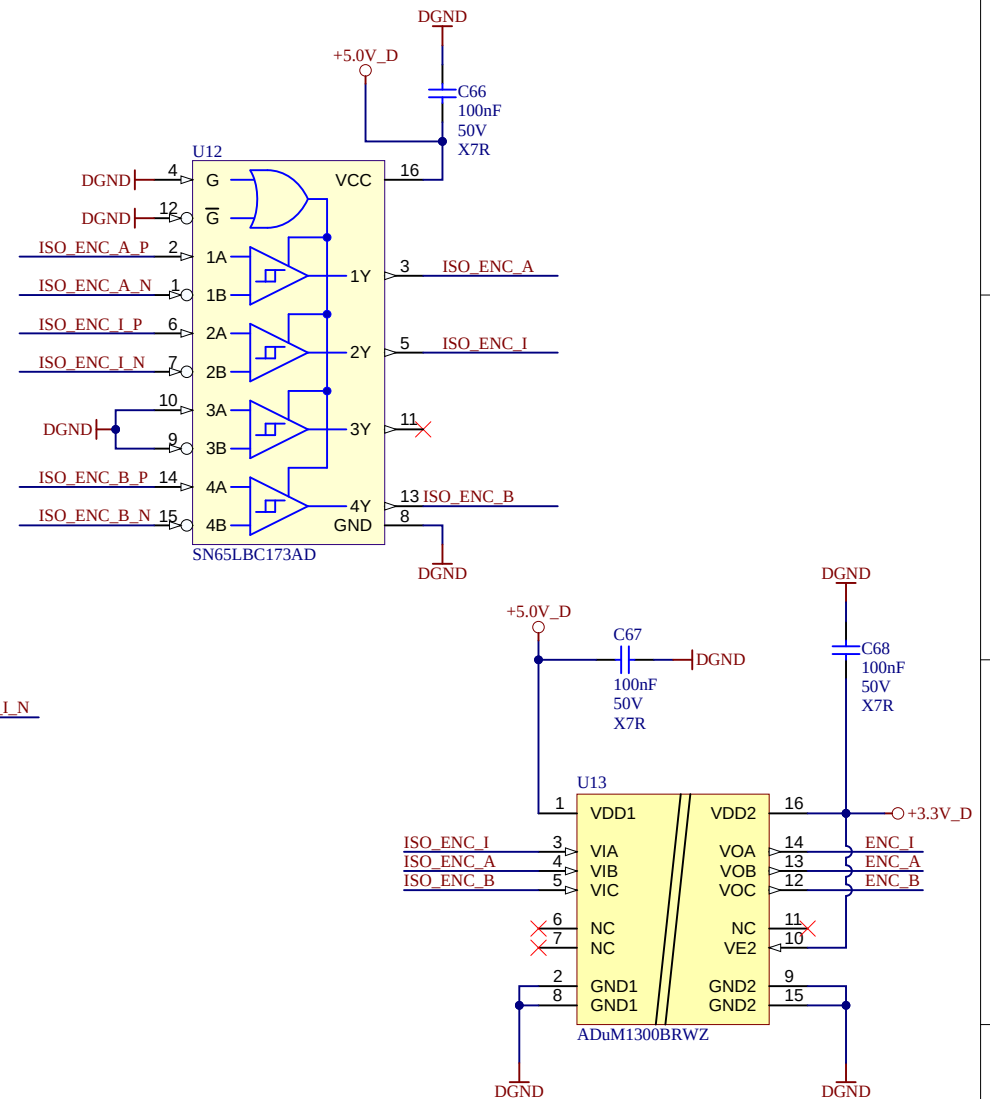


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Date:	2020-03-09	Copyright:	Trenz Electronic GmbH Page 9 of 20
Drawn by:	*	Filename: DRIVER_C.SchDoc	





Title:		DRIVER_D	
A4	Number:	CR00140 default	Rev. 02
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Title: ENCODER			
A4	Number: CR00140 default	Rev. 02	
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A

A

FPGA_Main
FPGA_Main.SchDoc

FPGA_MISC
FPGA_MISC.SchDoc

B

B

C

C

D

D

		Title: FPGA	
		A4	Number: CR00140 default
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Drawn by: *		Page 12 of 20	
		Filename: FPGA.SchDoc	
		Rev. 02	

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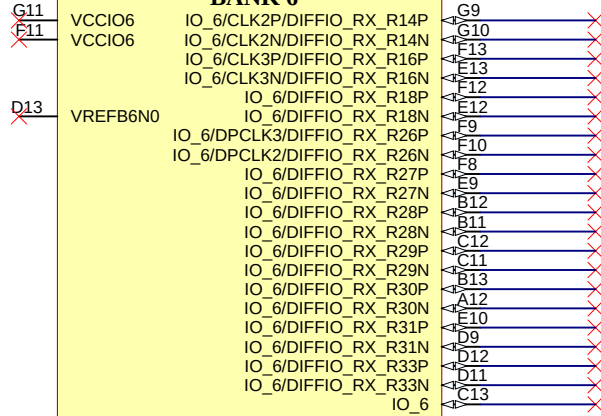
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U25E

RIGTH

BANK 6

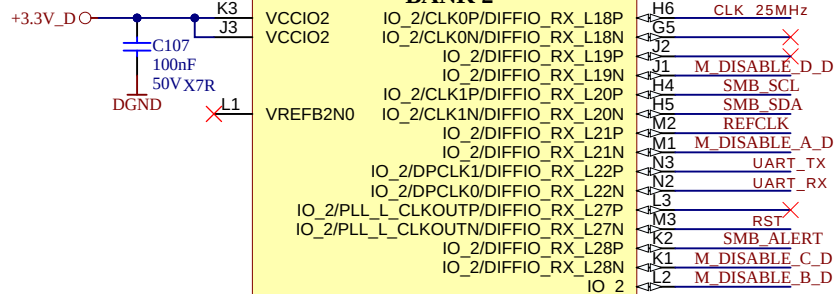


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U25B

LEFT

BANK 2

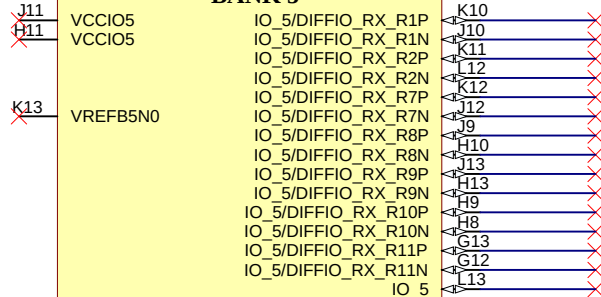


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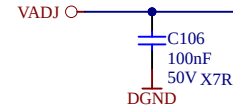
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RIGTH

BANK 5



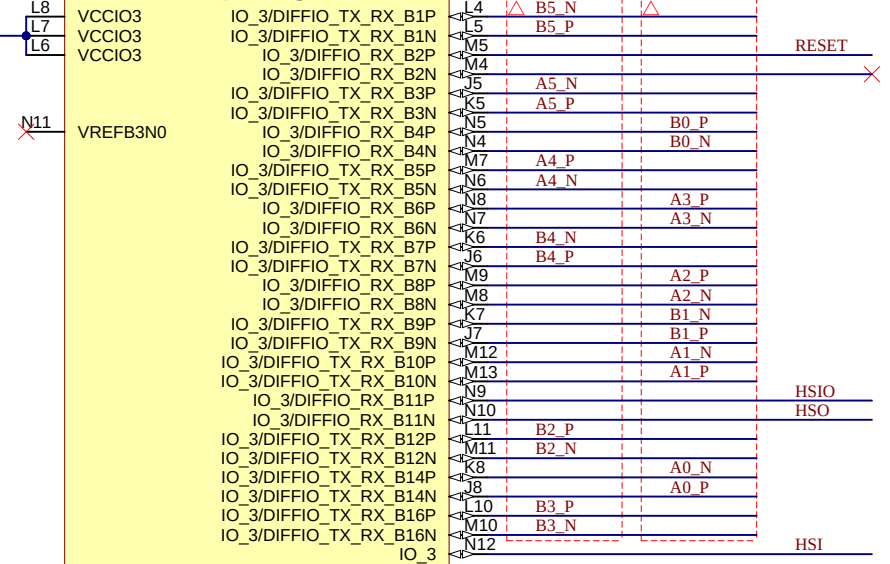
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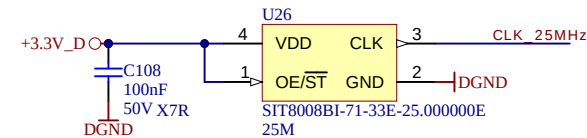
U25C

BOTTOM

BANK 3



10M08SAU169C8G



Max10 View



Title: FPGA_Main		
A4	Number: CR00140 default	Rev. 02
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A

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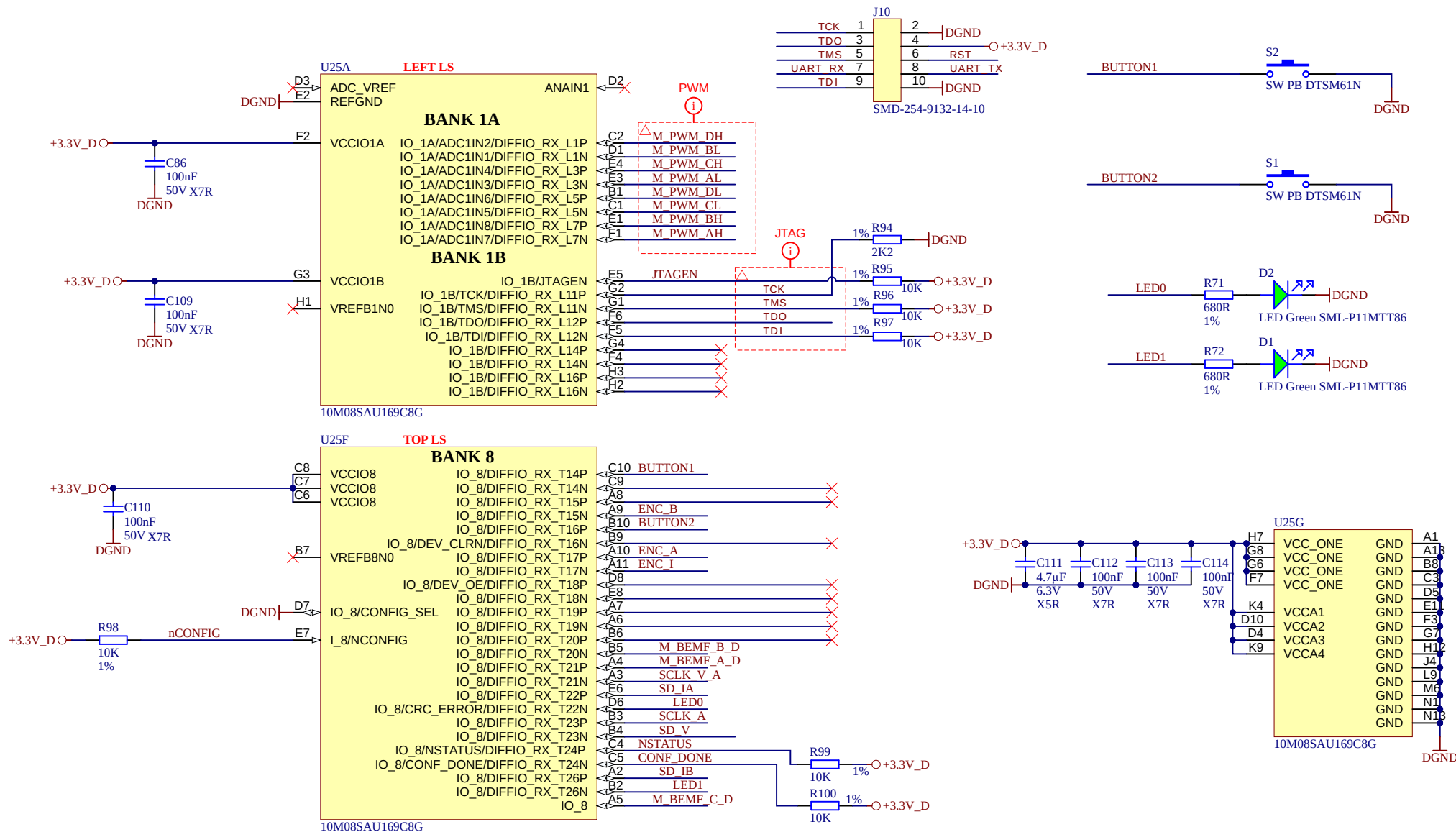
B

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D



Title: FPGA_MISC		
A4	Number: CR00140 default	Rev. 02
Date: 2020-03-09	Copyright: Trenz Electronic GmbH	Page 14 of 20
Drawn by: *	Filename: FPGA_MISC.SchDoc	

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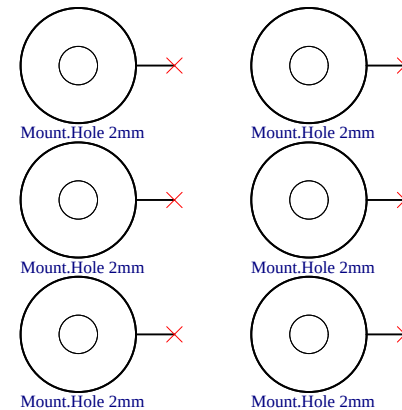
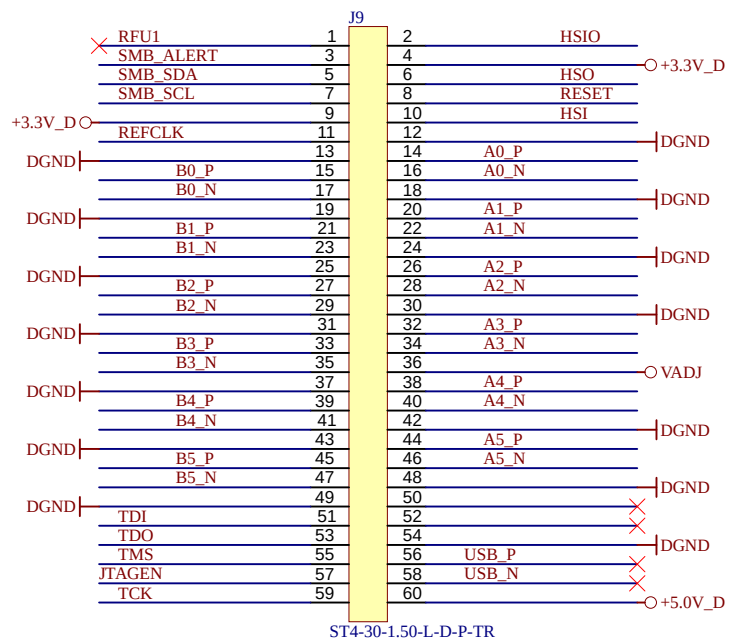
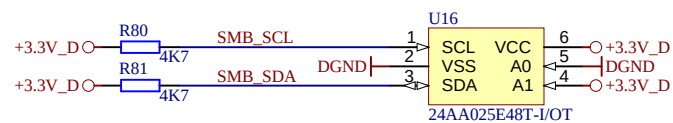
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SNAP1



SNAP CRUVI 3x



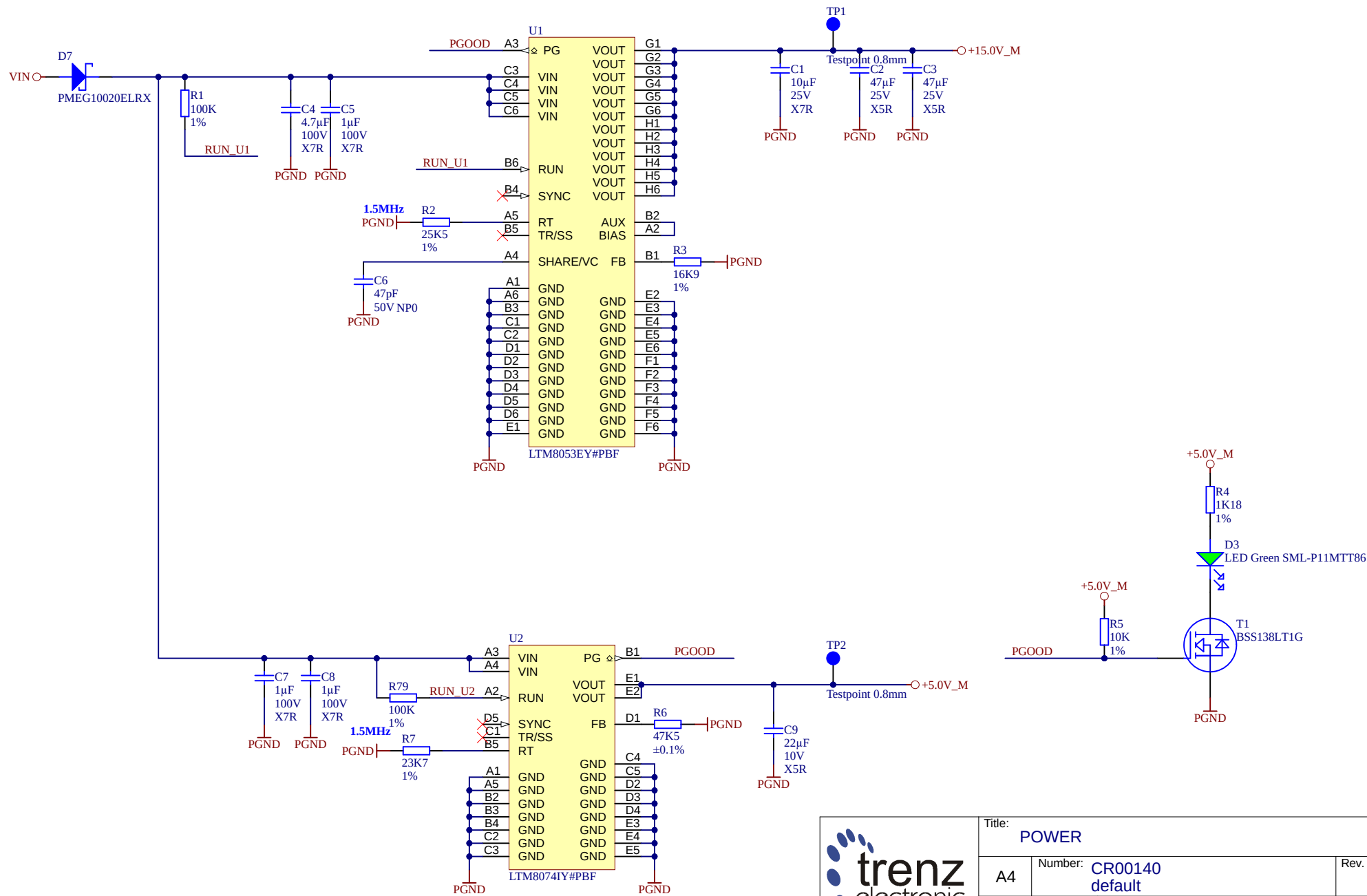
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Drawn by: *	Filename: CRUVI.SchDoc	

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Title: POWER			
A4	Number: CR00140 default	Rev. 02	
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		Title: Power_Protection_1	
		Number: CR00140 default	Rev. 02
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Drawn by: *	Filename: Power_Protection_1.SchDoc		

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CHANGES REV01

initial Revision

REV01 -REV02

Removed LS connector J11
Added identification EEPROM
Updated to new TE Address

2025-07-10 MR
set S/N1 Traceability pad to not fitted (EOL)

A

A

B

B

C

C

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D

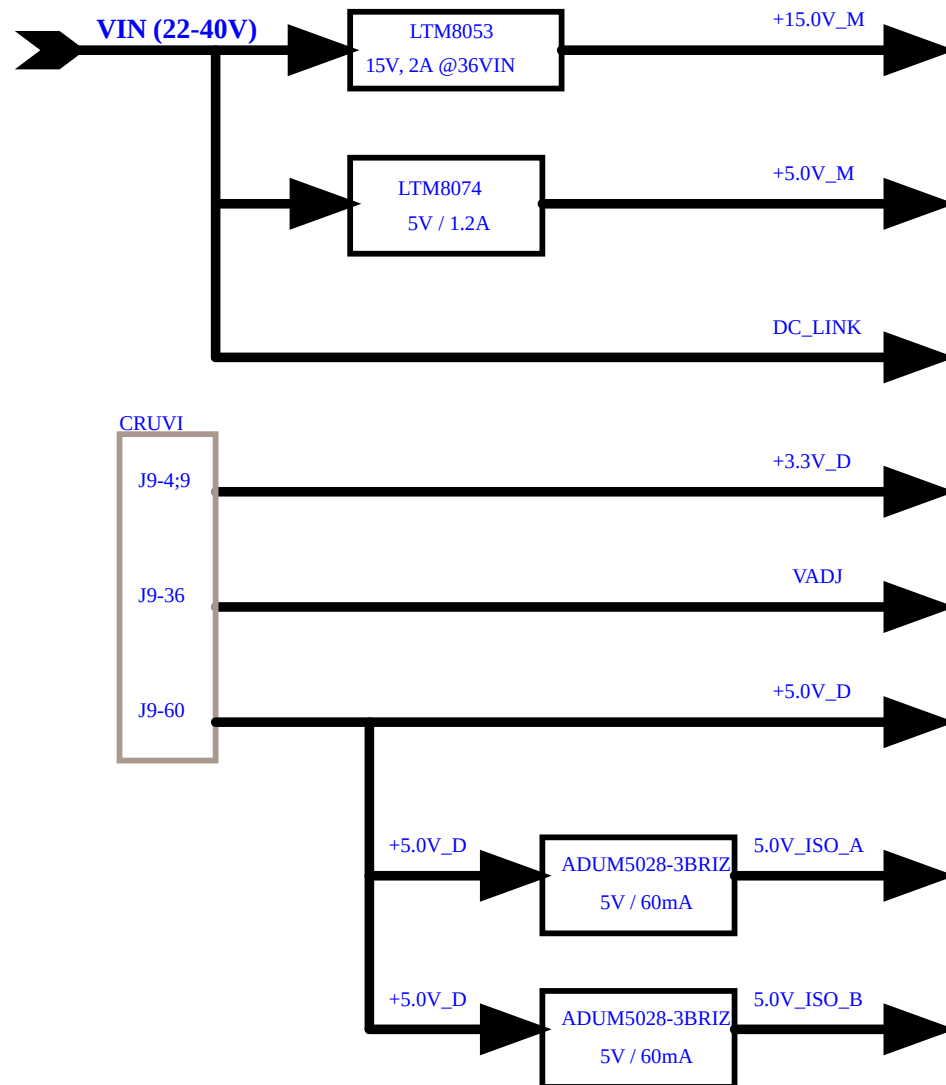
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		Date: 2020-03-09	Page 19 of 20
		Drawn by:	Filename: Revision_Changes.SchDoc

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			Title: Power Overview	
			A4	Number: CR00140 default
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			Drawn by:	Filename: Power_Overview.SchDoc
Rev. 02				