Phase Leg IGBT 7 Power Module

APTGX300A120T6LIAG

Product Overview

The APTGX300A120T6LIAG device is a phase leg 1200V, 300A Insulated-Gate Bipolar Transistor (IGBT) 7 power module.

The following figures show the electrical diagram and pinout location of the device.

Figure 1. Electrical Diagram

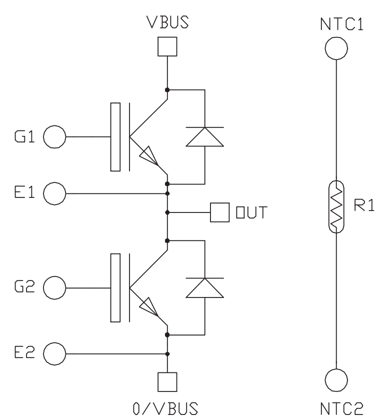
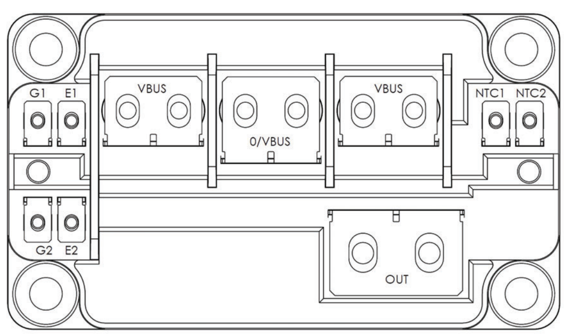


Figure 2. Pinout Location



Note:

- All ratings are at T_J = 25 °C, unless otherwise specified.

⚠ CAUTION These devices are sensitive to electrostatic discharge. Proper handling procedures must be followed.

Features

The APTGX300A120T6LIAG device has the following key features:

- IGBT 7
  - Low-voltage drop
  - Low-leakage current
- Very low-stray inductance
- Internal thermistor for temperature monitoring
- M4 and M5 power connectors
- M2.5 signal connectors
- AlN substrate for improved thermal performance
- Copper base plate

Benefits

The APTGX300A120T6LIAG device has the following benefits:

- High efficiency converter
- Direct mounting to heatsink (isolated package)
- Low junction-to-case thermal resistance
- Low profile
- RoHS compliant

Potential Applications

The APTGX300A120T6LIAG device has the following potential applications:

- Welding converters
- Switched-mode power supplies
- Uninterruptible power supplies
- Electric Vehicle (EV) motor and traction drive

1. Electrical Specifications

The following sections show the electrical specifications of the APTGX300A120T6LIAG device.

1.1 IGBT Characteristics (Per IGBT)

The following table lists the absolute maximum ratings (per IGBT) of the APTGX300A120T6LIAG device.

Table 1-1. Absolute Maximum Ratings

Symbol	Parameter		Maximum Ratings	Unit
V_{CES}	Collector-emitter voltage		1200	V
I_C	Continuous collector current	T_C = 25 °C	540	A
		T_C = 115 °C	300	
I_{CM}	Pulsed collector current, t_P limited by $T_{J(max)}$		600	
V_{GE}	Gate-emitter voltage		±20	V
	Transient gate-emitter voltage		±25	
P_D	Power dissipation	T_C = 25 °C	1612	W

The following table lists the electrical characteristics (per IGBT) of the APTGX300A120T6LIAG device.

Table 1-2. Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
I_{CES}	Zero gate voltage collector current	V_{GE} = 0V; V_{CE} = 1200V		—	—	60	µA
$V_{CE(sat)}$	Collector-emitter saturation voltage	V_{GE} = 15V I_C = 300A	T_J = 25 °C	—	1.55	1.8	V
			T_J = 125 °C	—	1.64	—	
			T_J = 175 °C	—	1.72	—	
$V_{GE(th)}$	Gate threshold voltage	V_{GE} = V_{CE}; I_C = 7 mA		5.15	5.8	6.45	
I_{GES}	Gate-emitter leakage current	V_{GE} = 20V; V_{CE} = 0V		—	—	400	nA

The following table lists the dynamic characteristics (per IGBT) of the APTGX300A120T6LIAG device.

Table 1-3. Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
C_{ies}	Input capacitance	V_{GE} = 0V V_{CE} = 25V f = 100 kHz		—	60	—	nF
C_{oes}	Output capacitance			—	0.76	—	
C_{res}	Reverse transfer capacitance			—	0.22	—	
Q_G	Gate charge	V_{GE} = ±15V V_{CE} = 600V I_C = 300A		—	5	—	µC
$T_{d(on)}$	Turn-on delay time	V_{GE} = ±15V V_{Bus} = 600V I_C = 300A R_G = 0.5Ω	T_J = 25 °C	—	151	—	ns
			T_J = 125 °C	—	155	—	
			T_J = 175 °C	—	157	—	
T_r	Rise time		T_J = 25 °C	—	55	—	
			T_J = 125 °C	—	70	—	
			T_J = 175 °C	—	60	—	
$T_{d(off)}$	Turn-off delay time		T_J = 25 °C	—	342	—	
			T_J = 125 °C	—	420	—	
			T_J = 175 °C	—	453	—	
T_f	Fall time		T_J = 25 °C	—	102	—	
			T_J = 125 °C	—	200	—	
			T_J = 175 °C	—	263	—	
E_{on}	Turn-on energy	V_{GE} = ±15V V_{Bus} = 600V I_C = 300A R_G = 0.5Ω di/dt = 4500 A/µs dv/dt = 3900 V/µs	T_J = 25 °C	—	17	—	mJ
			T_J = 125 °C	—	22	—	
			T_J = 175 °C	—	25.1	—	
E_{off}	Turn-off energy		T_J = 25 °C	—	19	—	
			T_J = 125 °C	—	29.1	—	
			T_J = 175 °C	—	36.2	—	
R_{Gint}	Internal gate resistance			—	1	—	Ω
I_{sc}	Short circuit data	V_{GE} ≤ 15V V_{Bus} = 800V t_P ≤ 8 µs	T_J = 150 °C	—	1040	—	A
		V_{GE} ≤ 15V V_{Bus} = 800V t_P ≤ 7 µs	T_J = 175 °C	—	980	—	
R_{thJC}	Junction-to-case thermal resistance			—	—	0.093	°C/W

1.2 Diode Characteristics (Per Diode)

The following table lists the diode characteristics (per diode) of the APTGX300A120T6LIAG device.

Table 1-4. Diode Characteristics

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
V_{RRM}	Peak repetitive reverse voltage			—	—	1200	V
I_{RM}	Reverse leakage current	V_R = 1200V		—	—	20	µA
I_{FRM}	Repetitive forward current, t_P limited by $T_{J(max)}$			—	—	600	A
I^2t	I^2t value	t_p = 10 ms V_R = 0V	T_J = 125 °C	—	—	8390	A²s
			T_J = 175 °C	—	—	5110	
I_F	DC forward current	T_C = 95 °C	T_J = 175 °C	—	300	—	A
V_F	Diode forward voltage	I_F = 300A V_{GE} = 0V	T_J = 25 °C	—	1.75	1.95	V
			T_J = 125 °C	—	1.6	—	
			T_J = 175 °C	—	1.52	—	
I_{RRM}	Reverse recovery current	V_{GE} = –15V I_F = 300A V_R = 600V di/dt = 4500 A/µs	T_J = 25 °C	—	172	—	A
			T_J = 125 °C	—	224	—	
			T_J = 175 °C	—	260	—	
Q_{rr}	Reverse recovery charge		T_J = 25 °C	—	19.6	—	µC
			T_J = 125 °C	—	40.8	—	
			T_J = 175 °C	—	54.8	—	
E_{rr}	Reverse recovery energy		T_J = 25 °C	—	10	—	mJ
			T_J = 125 °C	—	19.4	—	
			T_J = 175 °C	—	27	—	
R_{thJC}	Junction-to-case thermal resistance			—	—	0.149	°C/W

1.3 Thermal and Package Characteristics

The following table lists the thermal and package characteristics of the APTGX300A120T6LIAG device.

Table 1-5. Thermal and Package Characteristics

Symbol	Characteristic			Min.	Typ.	Max.	Unit
V_{ISOL}	RMS isolation voltage, any terminal-to-case t = 1 min, 50/60 Hz			4000	—	—	V
L_{stray}	Stray inductance module			—	3	—	nH
d_{creep}	Creepage distance terminal-to-terminal			—	12.8	—	mm
	Creepage distance terminal-to-heatsink			—	15.4	—	
d_{clear}	Clearance distance terminal-to-terminal			—	9.8	—	
	Clearance distance terminal-to-heatsink			—	13	—	
R_{CE}	Lead resistance terminal-to-chip	T_C = 25 °C, per switch		—	0.4	—	mΩ
T_J	Operating junction temperature range			–40	—	175	°C
T_{STG}	Storage temperature range			–40	—	125	
T_C	Operating case temperature			–40	—	125	
τ_M	Mounting torque	For terminals	M2.5	0.4	—	0.6	N.m
			M4	2	—	3	
			M5	2	—	3.5	
		To heatsink	M6	3	—	5	
Wt	Package weight			—	305	—	g

The following table lists the temperature sensor NTC of the APTGX300A120T6LIAG device.

Table 1-6. Temperature Sensor NTC

Symbol	Characteristic		Min.	Typ.	Max.	Unit
R_{25}	Resistance at 25 °C		—	50	—	kΩ
$\Delta R_{25}/R_{25}$	—		—	5	—	%
$B_{25/85}$	T_{25} = 298.15K		—	3952	—	K
ΔB/B	—	T_C = 100 °C	—	4	—	%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

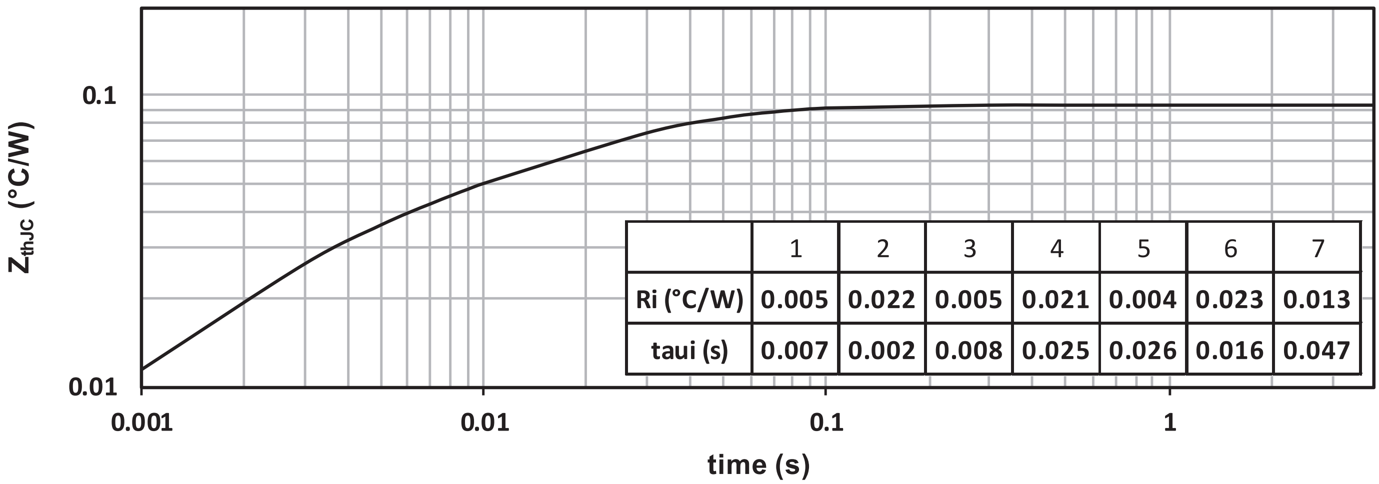
T: Thermistor temperature
R_T: Thermistor value at T

Note: For more information, see APT0406—Using NTC Temperature Sensor Integrated into Power Module.

1.4 Typical IGBT Performance Curve

The following figures show the IGBT performance curves of the APTGX300A120T6LIAG device.

Figure 1-1. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.005	0.022	0.005	0.021	0.004	0.023	0.013
taui (s)	0.007	0.002	0.008	0.025	0.026	0.016	0.047

Figure 1-2. Output Characteristics, V_{GE} = 15V

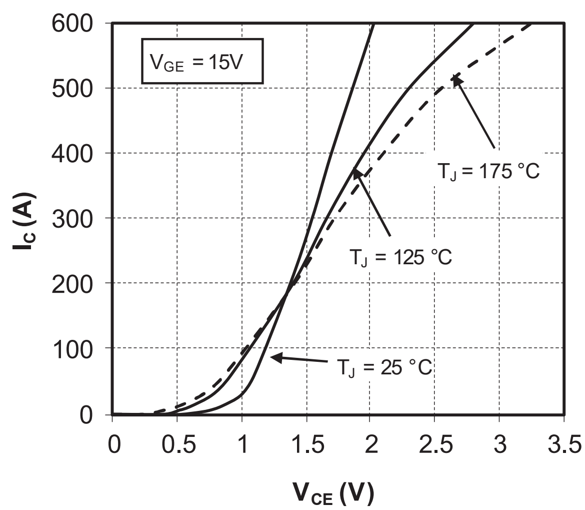


Figure 1-3. Output Characteristics, T_J = 175 °C

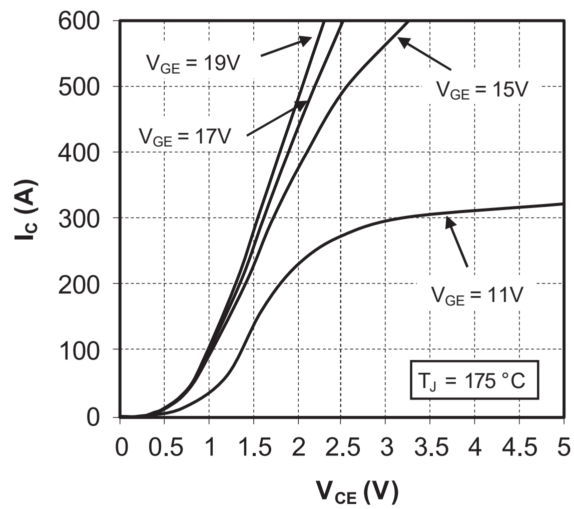


Figure 1-4. Switching Losses vs. Gate Resistance

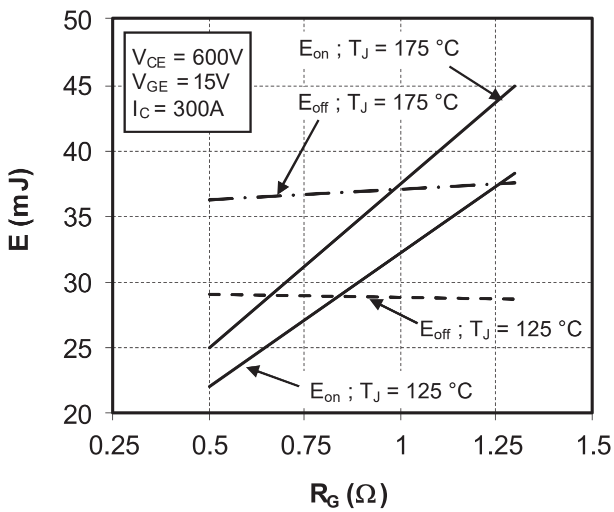


Figure 1-5. Switching Losses vs. Collector Current

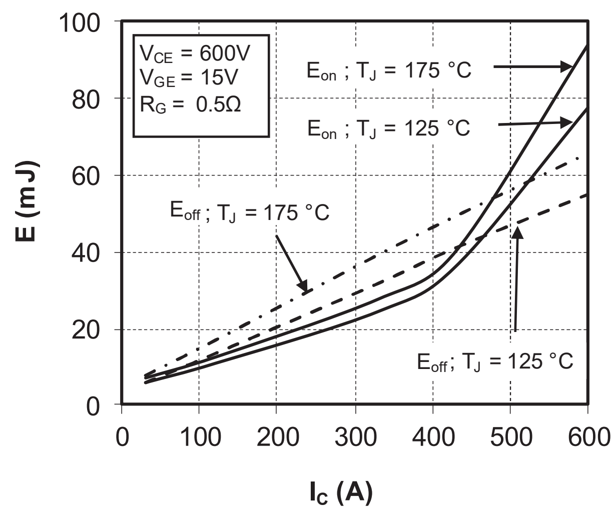


Figure 1-6. Operating Frequency vs. Collector Current

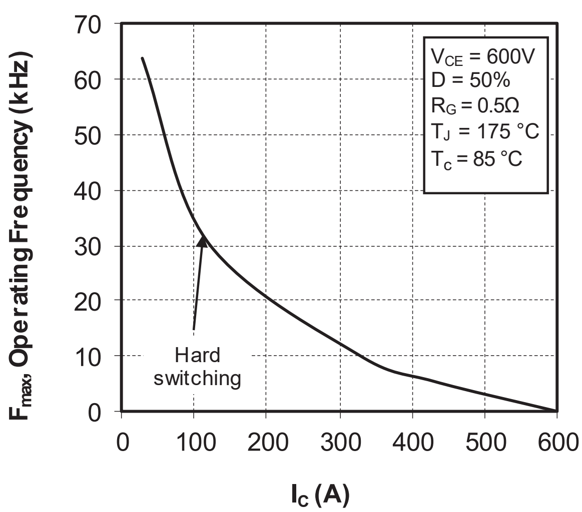


Figure 1-7. Gate Charge Characteristics

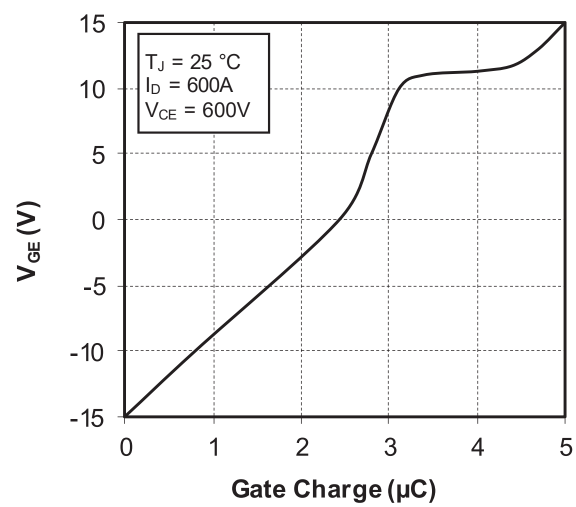


Figure 1-8. Transfer Characteristics

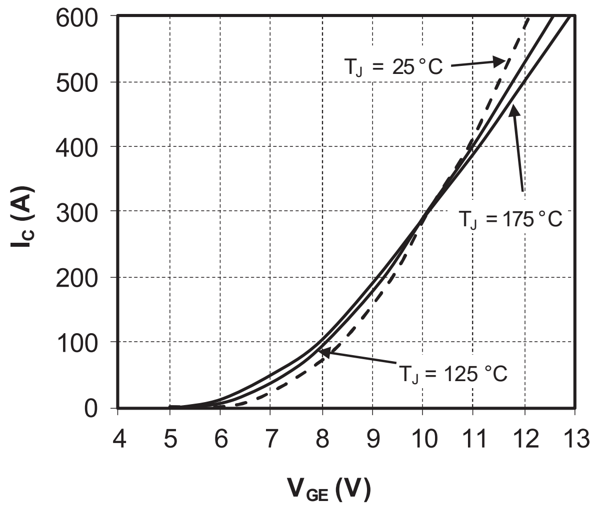


Figure 1-9. Capacity Characteristics

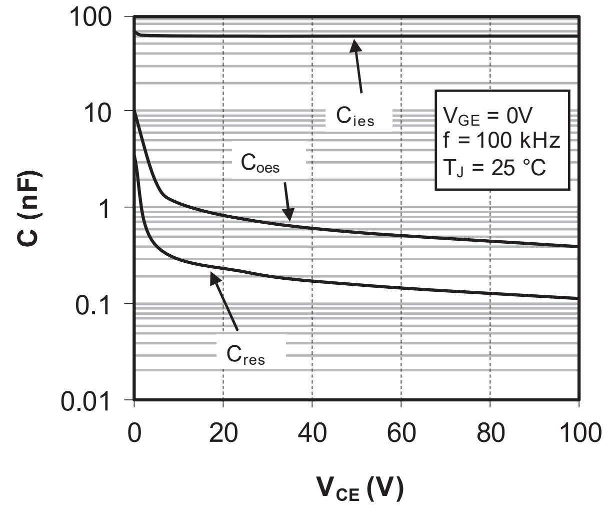
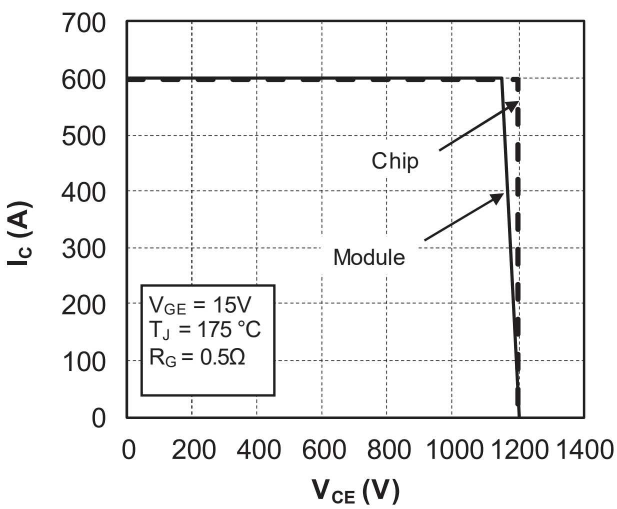


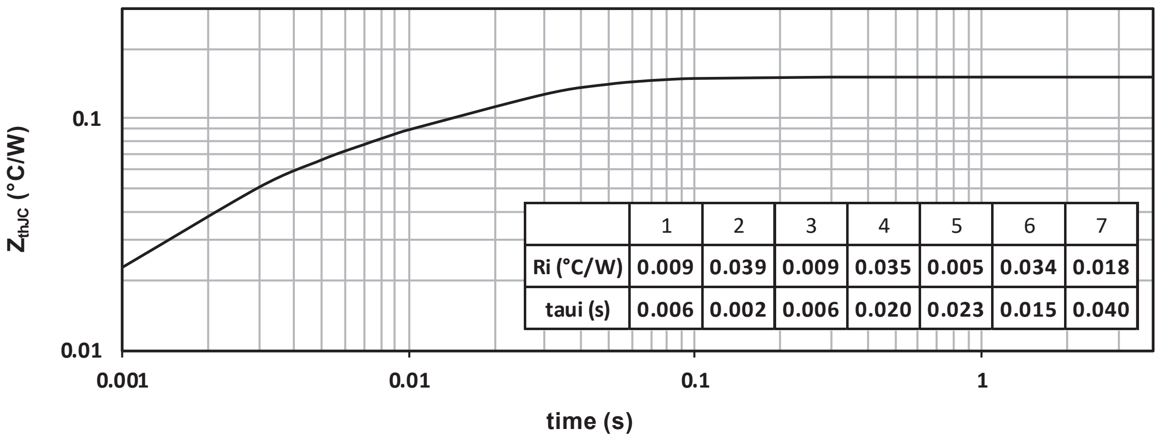
Figure 1-10. Reverse Bias Safe Operating Area



1.5 Typical Diode Performance Curve

The following figures show the diode performance curves of the APTGX300A120T6LIAG device.

Figure 1-11. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.009	0.039	0.009	0.035	0.005	0.034	0.018
taui (s)	0.006	0.002	0.006	0.020	0.023	0.015	0.040

Figure 1-12. Forward Characteristics

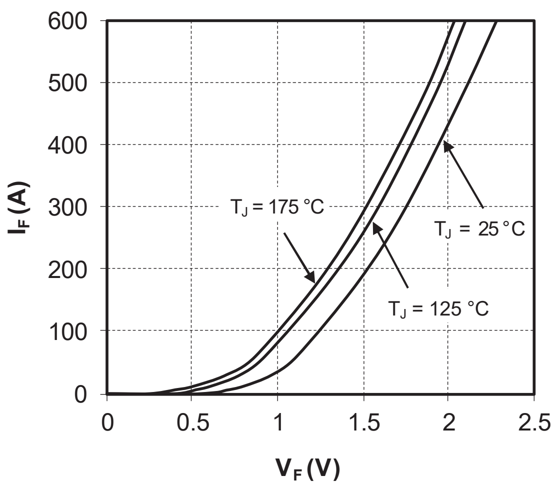


Figure 1-13. Switching Losses vs. Gate Resistance

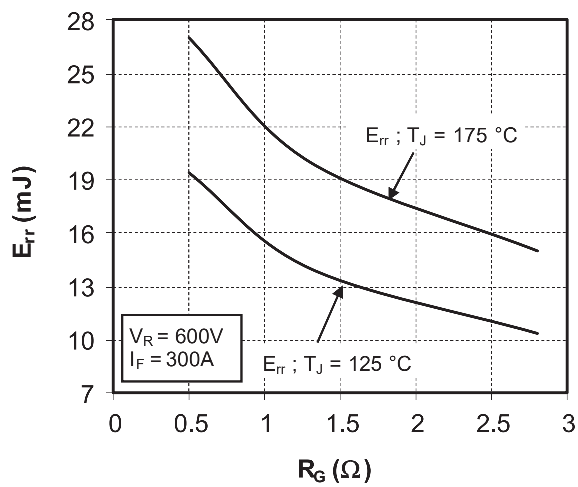
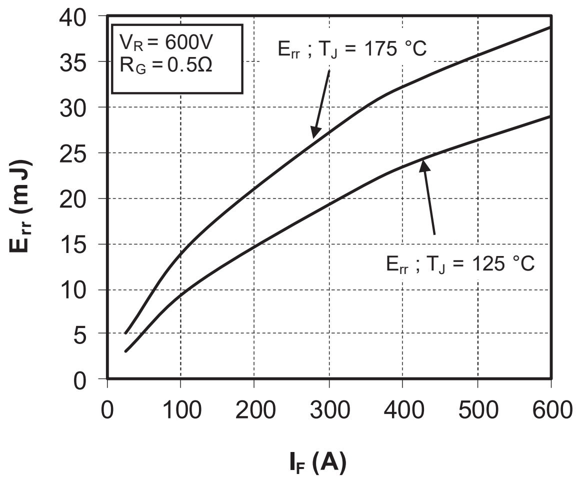


Figure 1-14. Switching Losses vs. Forward Current



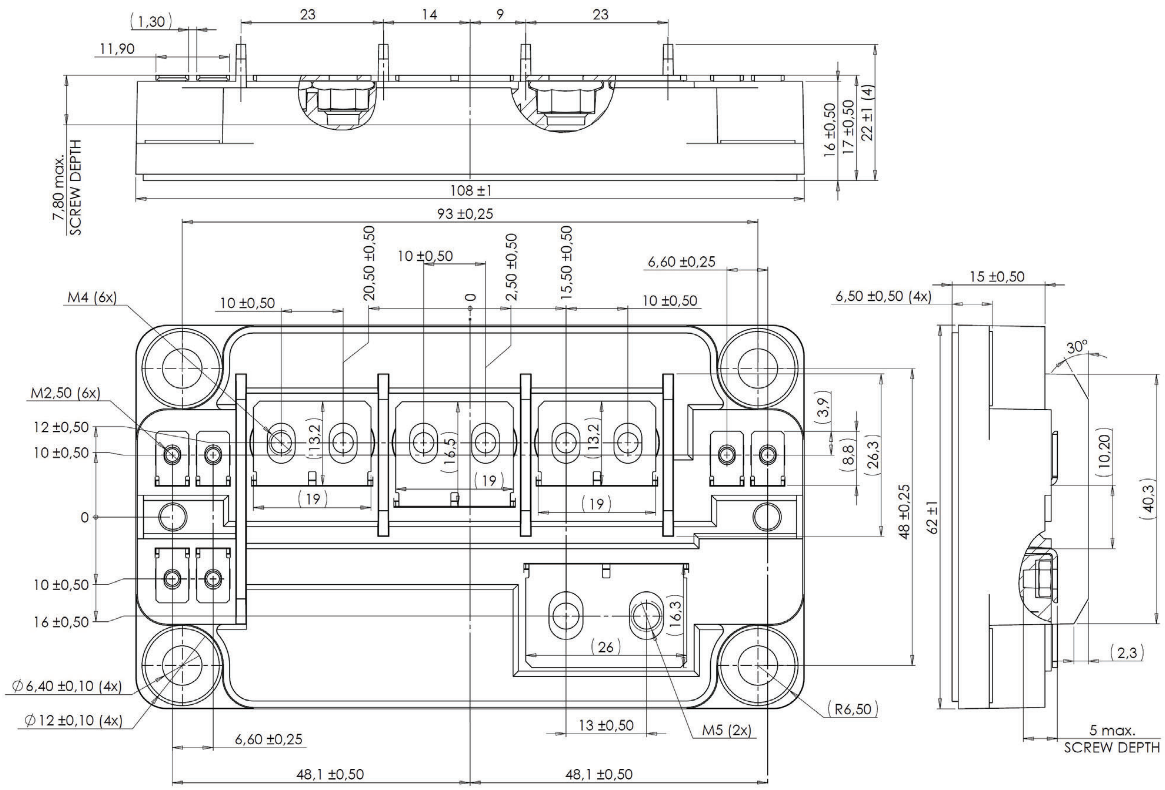
2. Package Specifications

The following section shows the package specification of the APTGX300A120T6LIAG device.

2.1 Package Outline

The following figure shows the package outline drawing of the APTGX300A120T6LIAG device. The dimensions in the following figure are in millimeters.

Figure 2-1. Package Outline Drawing



Note: For more information, see AN1911 - Mounting instructions for SP6 Low inductance Power Module.

3. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
A	09/2024	Initial revision

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