REGULATORY COMPLIANCE

Lead Free COMPLIANT | EU RoHS 2011/65 + 2015/863 COMPLIANT | China RoHS COMPLIANT | REACH SVHC COMPLIANT | DRC CONFLICT FREE

ITEM DESCRIPTION

Quartz Crystal Clock Oscillators XO (SPXO) LVCMOS (CMOS) 1.62Vdc to 3.63 4 Pad 2.5mm x 3.2mm Ceramic Surface Mount

ELECTRICAL SPECIFICATIONS

Nominal Frequency	1MHz to 50MHz
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration ±100ppm Maximum over -20°C to +70°C ±50ppm Maximum over -20°C to +70°C ±25ppm Maximum over -20°C to +70°C ±20ppm Maximum over -20°C to +70°C ±100ppm Maximum over -40°C to +85°C ±50ppm Maximum over -40°C to +85°C ±25ppm Maximum over -40°C to +85°C
Supply Voltage	1.62Vdc to 3.63Vdc
Input Current	3.0mA Maximum over Nominal Frequency of 1MHz to 9.999999MHz 4.0mA Maximum over Nominal Frequency of 10MHz to 39.999999MHz 5.0mA Maximum over Nominal Frequency of 40MHz to 50MHz
Output Voltage Logic High (V_{OH})	IOH= -4mA 90% of Vdd Minimum
Output Voltage Logic Low (V_{OL})	IOL= +4mA 10% of Vdd Maximum
Rise/Fall Time	Measured at 20% to 80% of waveform 5nSec Maximum over Nominal Frequency of 1MHz to 24MHz 4nSec Maximum over Nominal Frequency of 24.000001MHz to 50MHz
Duty Cycle	Measured at 50% of waveform 50 ±5(%)
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Pin 1 Connection	Tri-State (High Impedance)
Tri-State Input Voltage (Vih and Vil)	80% of Vdd Minimum or No Connect to Enable Output, 20% of Vdd Maximum to Disable Output (High Impedance)
Standby Current	10µA Maximum
RMS Phase Jitter	Fj = 12kHz to 20MHz 1pSec Maximum
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

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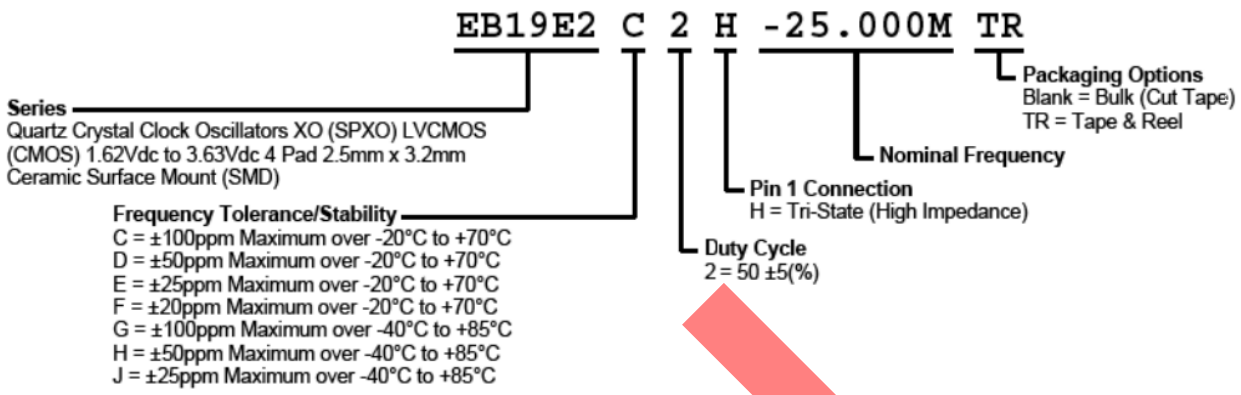
PART NUMBERING GUIDE



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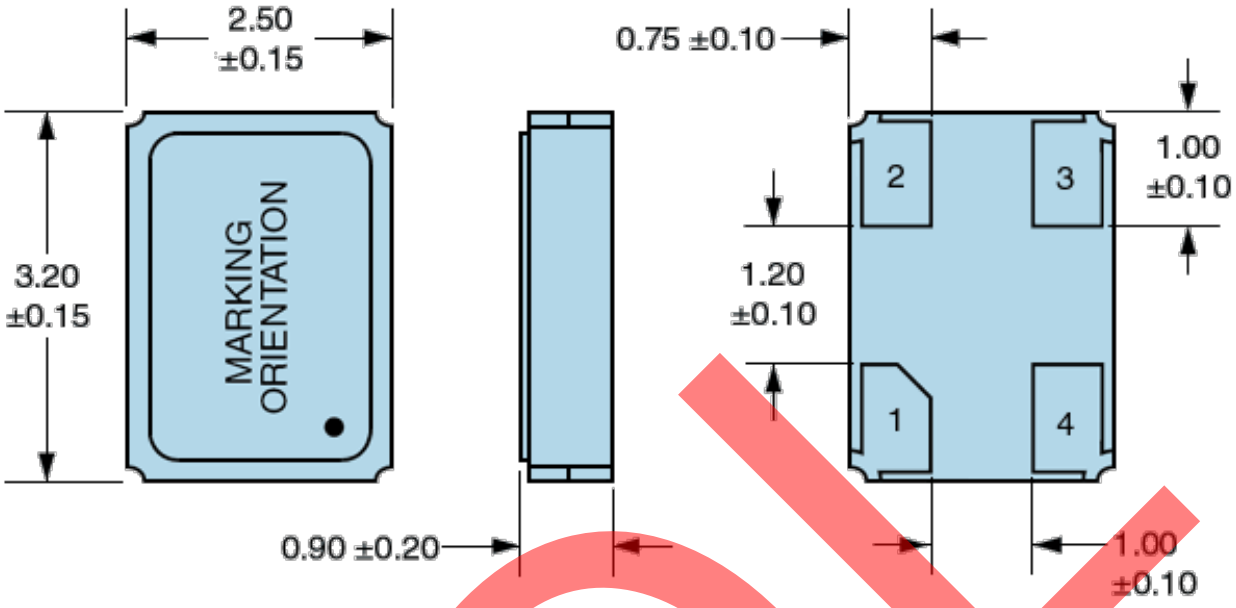
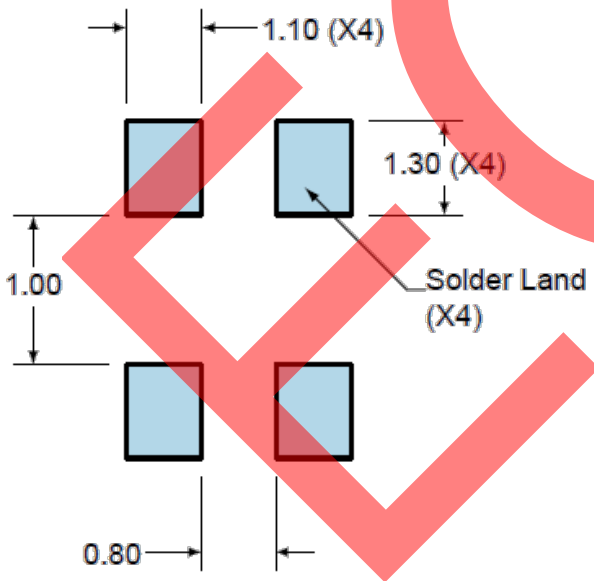
MECHANICAL DIMENSIONS



SUGGESTED SOLDER PAD LAYOUT



PIN	CONNECTION
1	Tri-State
2	Case/Ground
3	Output
4	Supply Voltage

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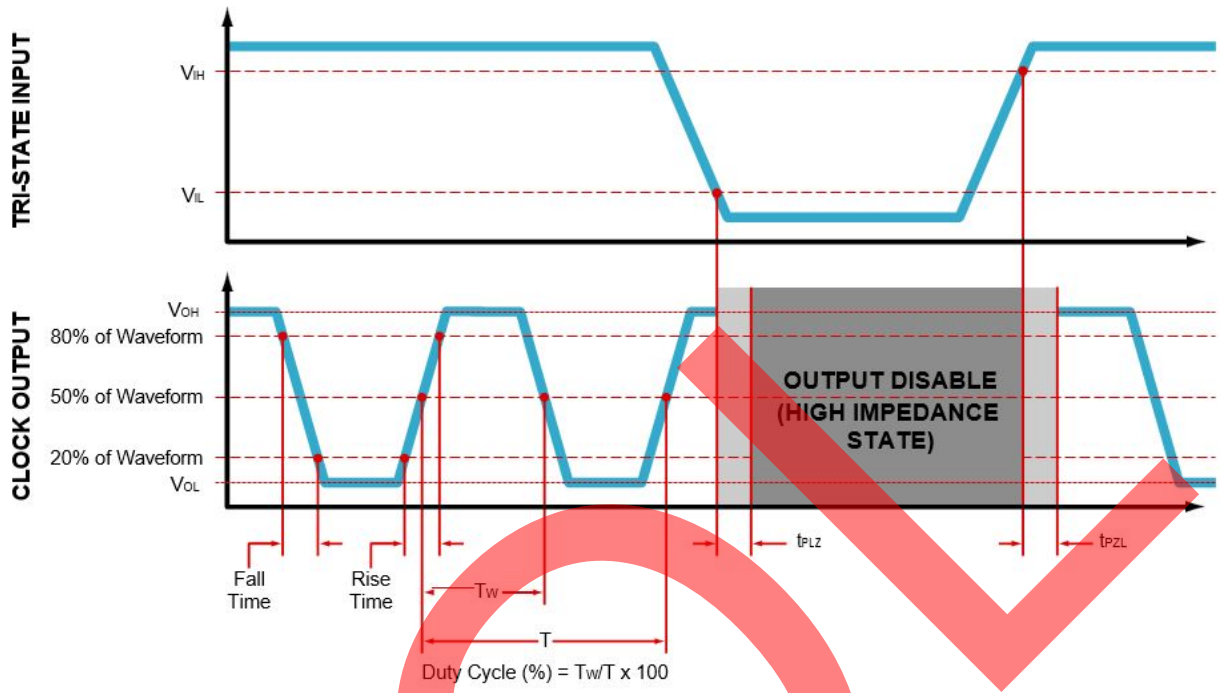
All Tolerances are ±0.1

All Dimensions in Millimeters

OUTPUT WAVEFORM & TIMING DIAGRAM



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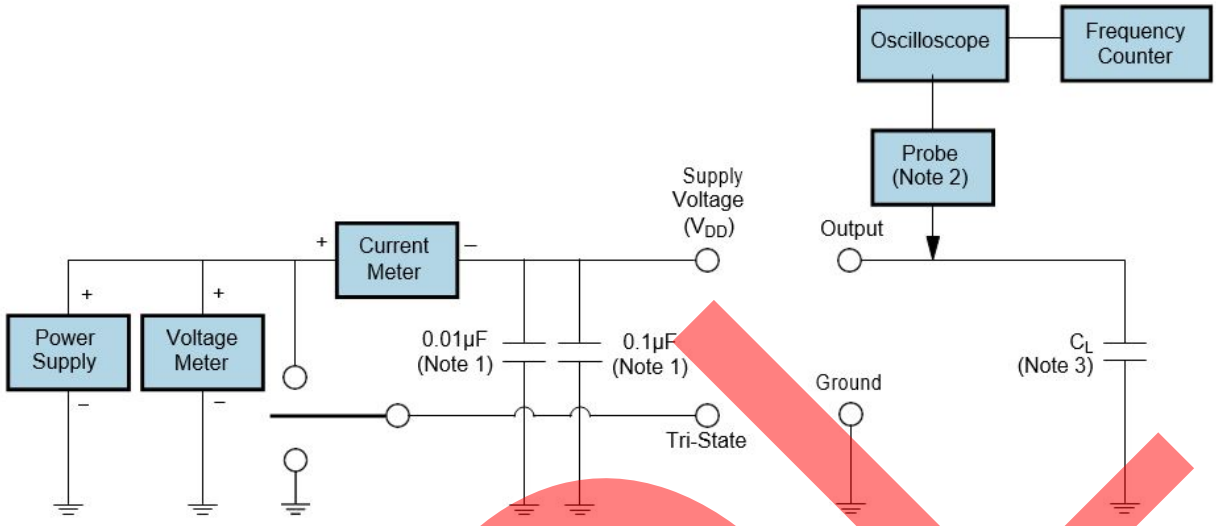
TEST CIRCUIT FOR CMOS OUTPUT



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less Than 2mm) to the package ground and supply voltage pin is required.
Note 2: A low capacitance (<12pF), 10X Attenuation Factor, High Impedance (>10Mohms), and High bandwidth (>300MHz) Passive probe is recommended.
Note 3: Capacitance value CL includes sum of all probe and fixture capacitance.

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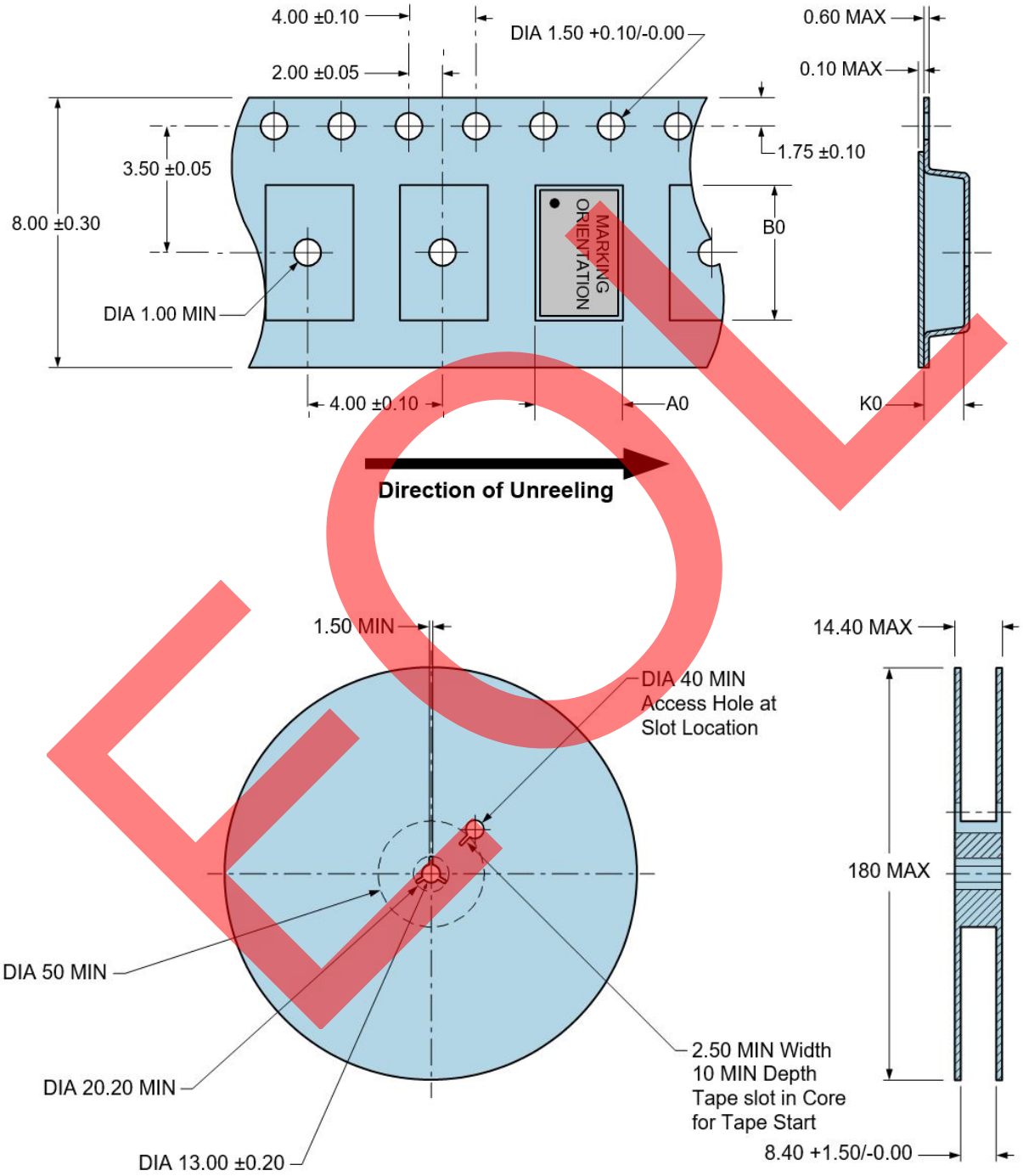
TAPE & REEL DIMENSIONS

Quantity per Reel: 1,000 Units

All Dimensions in Millimeters

Compliant to EIA-481



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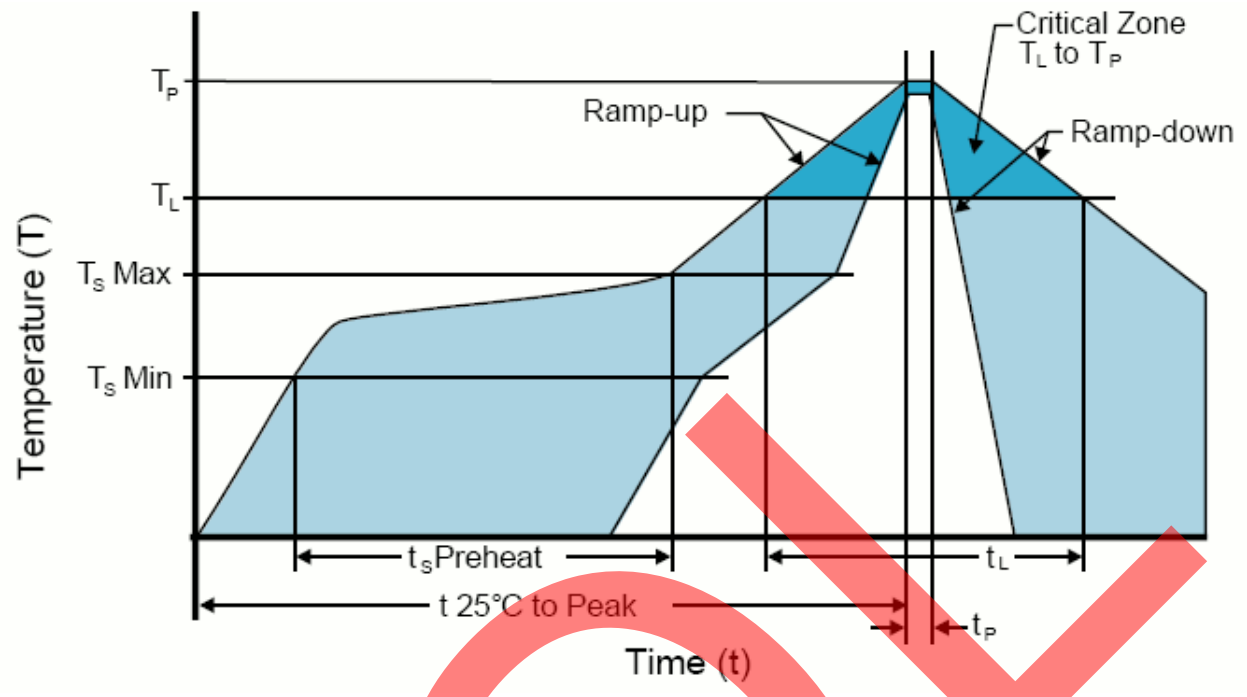
RECOMMENDED SOLDER REFLOW METHOD



HIGH TEMPERATURE INFRARED/CONVECTION	
T_S MAX to T_L (Ramp-up Rate)	3°C/Second Maximum
Preheat **- Temperature Minimum (T_S MIN)** **- Temperature Typical (T_S TYP)** **- Temperature Maximum(T_S MAX)** **- Time (t_s MIN)**	 150°C 175°C 200°C 60 - 180 Seconds
Ramp-up Rate (T_L to T_P)	3°C/Second Maximum
Time Maintained Above: **- Temperature (T_L)** **- Time (t_L)**	 217°C 60 - 150 Seconds
Peak Temperature (T_P)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature(T_P Target)	250°C +0/-5°C
Time within 5°C of actual peak (t_p)	20 - 40 Seconds
Ramp-down Rate	6°C/Second Maximum
Time 25°C to Peak Temperature (t)	8 Minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

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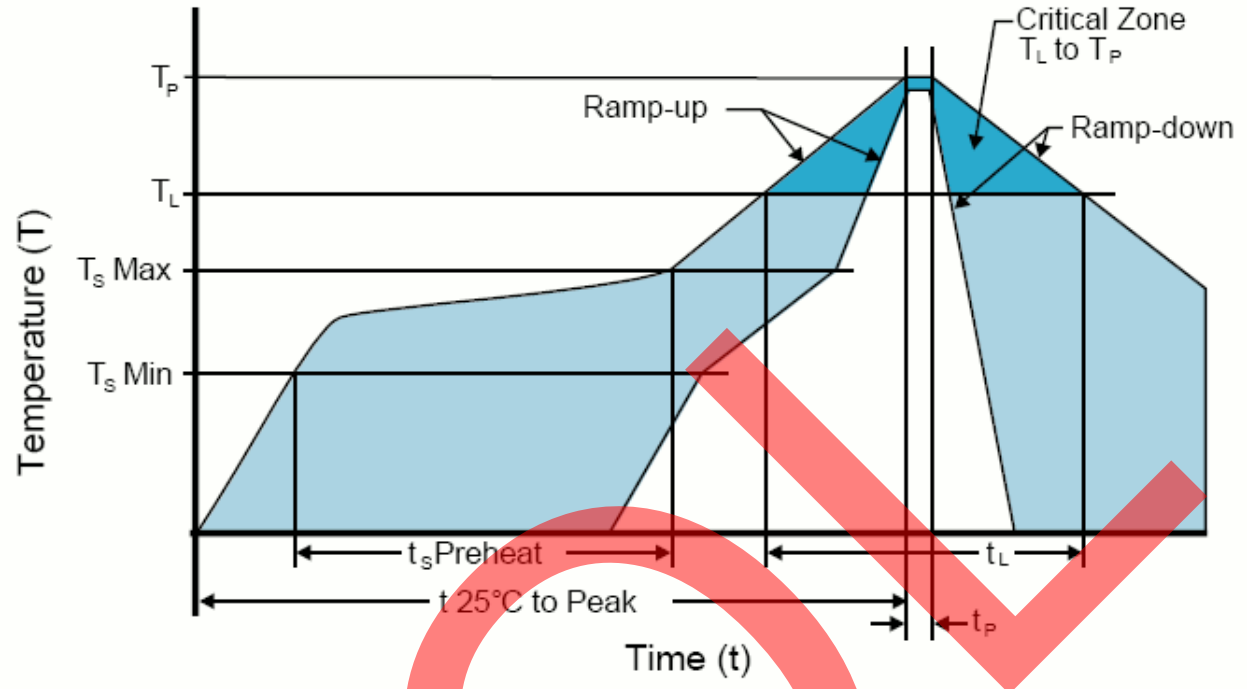
RECOMMENDED SOLDER REFLOW METHOD



LOW TEMPERATURE INFRARED/CONVECTION	
T_S MAX to T_L (Ramp-up Rate)	5°C/Second Maximum
Preheat **- Temperature Minimum (T_S MIN)** **- Temperature Typical (T_S TYP)** **- Temperature Maximum(T_S MAX)** **- Time (t_S MIN)**	 N/A 150°C N/A 60 - 120 Seconds
Ramp-up Rate (T_L to T_P)	5°C/Second Maximum
Time Maintained Above: **- Temperature (T_L)** **- Time (t_L)**	 150°C 200 Seconds Maximum
Peak Temperature (T_P)	240°C Maximum
Target Peak Temperature(T_P Target)	240°C Maximum 2 Times/230˚C Maximum 1Time
Time within 5°C of actual peak (t_P)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

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Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)