

RJ45 Jack with Dual USB 3.2 Type C ports



ARJG11E1 Series

Request Samples



Check Inventory



27.50x19.05x30.40mm

RoHS/RoHS II Compliant

MSL Level = N/A

Features

- RJ45 Jack with dual USB Type-C ports
- Supports 10 G Base-T Ethernet

Applications

- Ethernet networking
- Switches and access points
- IP cameras
- Industrial controllers and embedded systems

Electrical Specifications

Parameters	Typical	Units	Notes
Insulation Resistance	1000	MΩ	
Contact resistance	40	mΩ	
Dielectric Withstanding Voltage	500	VAC	
Voltage and Current Rating contact	1.5 A 30VAC		

Operating Temperature: -20°C to +85°C

Storage Temperature: -20°C to +85°C

Operating life: 750 Cycles min

Contact Abracon for more custom configurations. See options to build a custom configuration.



12117 Bee Caves Rd, Bldg-3, Suite 200

Bee Cave, TX 78738

Phone: 512-371-6159 Fax: 512-351-8858

For terms and conditions of sales, please visit:

www.abracon.com

REVISION Initial Release :
4/6/2026

ABRACON IS
ISO9001-2015
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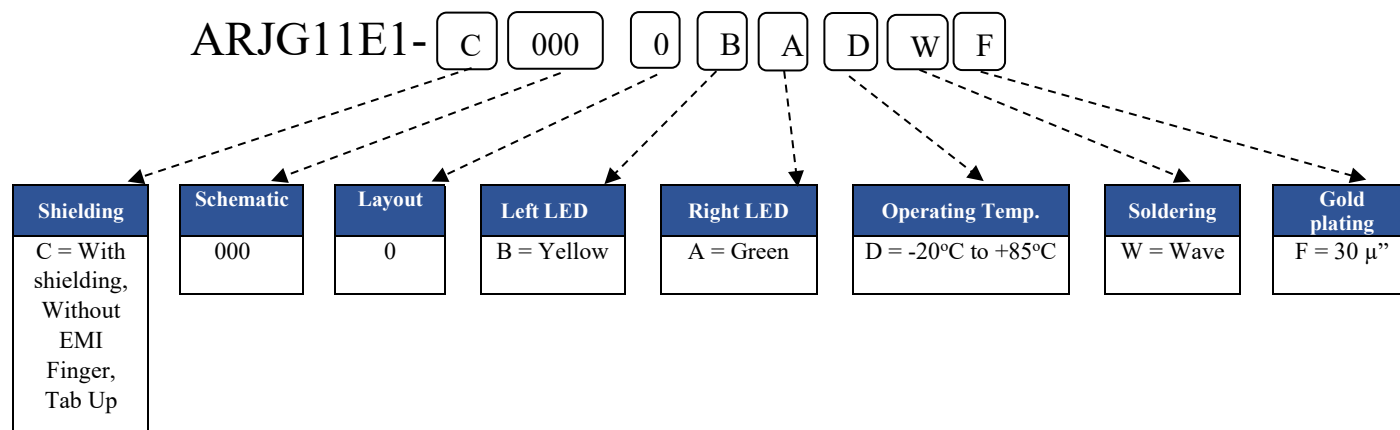


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Part Identification



*LED configurations and gold-plating options can be customized based on customer requirements.

Materials

- Housing Material:**
Thermoplastics PA9T+30%GF, UL94V-0.
- Insert Material:**
Phosphor Bronze C5191. Thickness=0.30mm.
gf" Gold plating on RJ45 contact area.
Tin Plating 100u" on solder tail area
45u" min Ni underplating overall.
- Contact pin Material:**
WELDABLE NICKEL
SELECTIVE GOLD, 30 MICRO-INCHS MIN IN CONTACT AREA
- Shielding Material:**
Stainless steel 304, Thickness=0.25 mm. Immersion tin.

Type-C

- HOUSING: LCP, UL 94V-0, COLOR- BLUE (PANTONE 300C) OR BLACK
- BOTTOM COVER: LCP, UL 94V-0, COLOR- BLUE (PANTONE 300C) OR BLACK
- CONTACT: USB3 C2680-H T=0.25mm; USB2 C5191-H T=0.25mm 30μ" MIN. GOLDPLATED ON CONTACT AREA, 120μ" MIN. MATTE TIN PLATED ON SOLDERING TAIL, 80μ" MIN. NICKEL UNDER-PLATED OVERALL
- FRONT SHELL: SUS304-1/2H, T=0.25mm, 30μ" MIN. NICKEL PLATED OVERALL, 100μ" MIN. MATTE TIN PLATED ON SOLDERING TAIL
- MIDDLE SHELL: SUS304-1/2H, T=0.25mm,
- REAR SHELL: SUS304-1/2H, T=0.20mm

Note: No material substitutions allowed without prior approval by customers in writing



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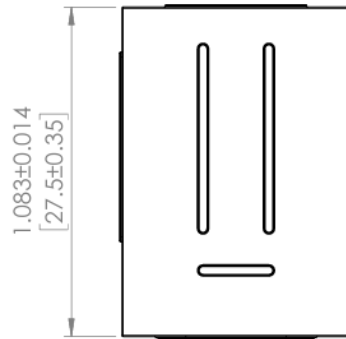
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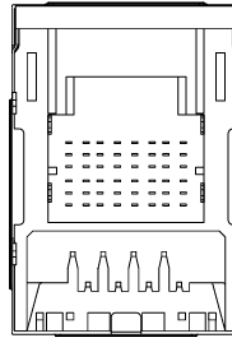
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Mechanical Dimensions

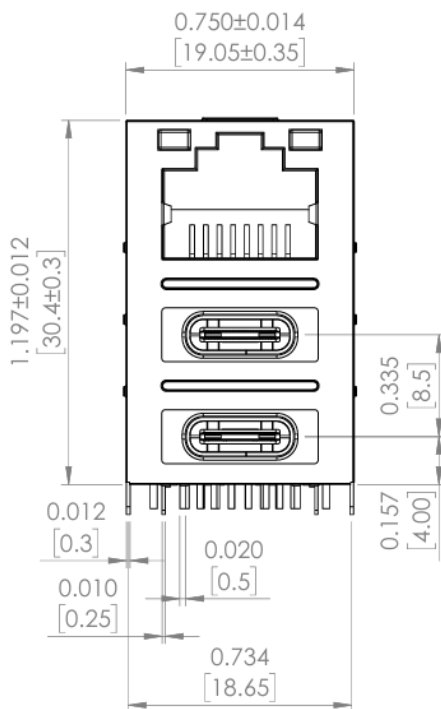
Layout 1:



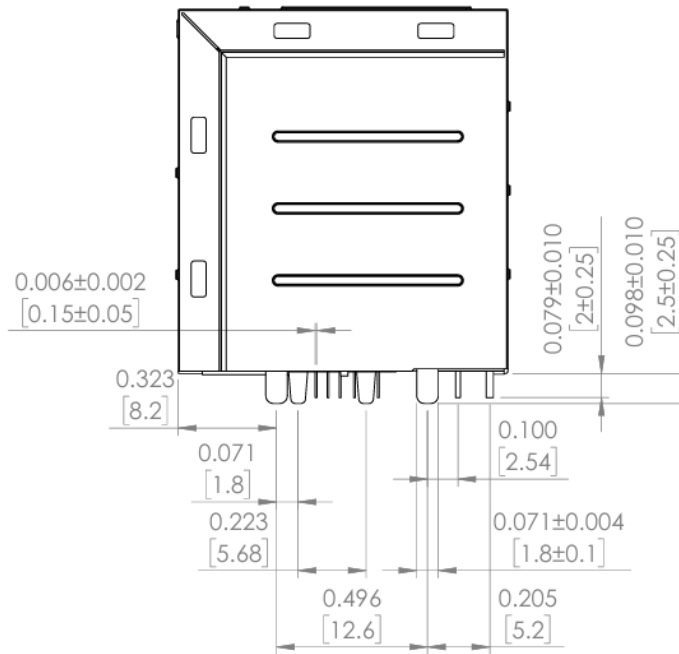
TOP VIEW



BOTTOM VIEW



FRONT VIEW



SIDE VIEW

DIMENSIONS: INCHES [mm]

X.X	±0.30
X.XX	±0.25
X.XXX	±0.15
ANGLES	±1°



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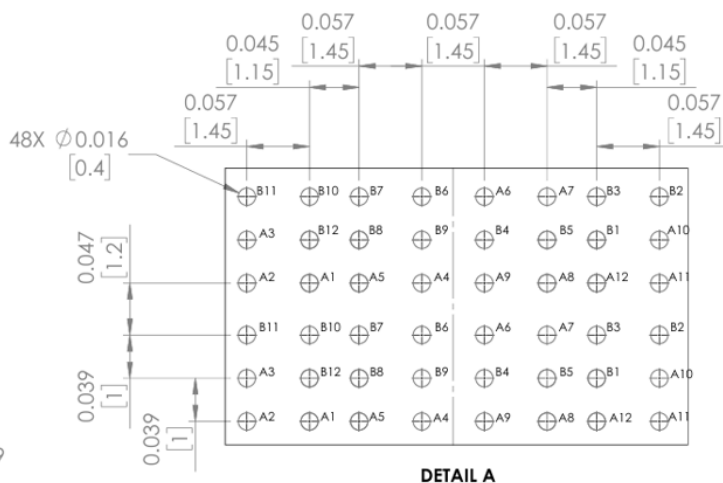
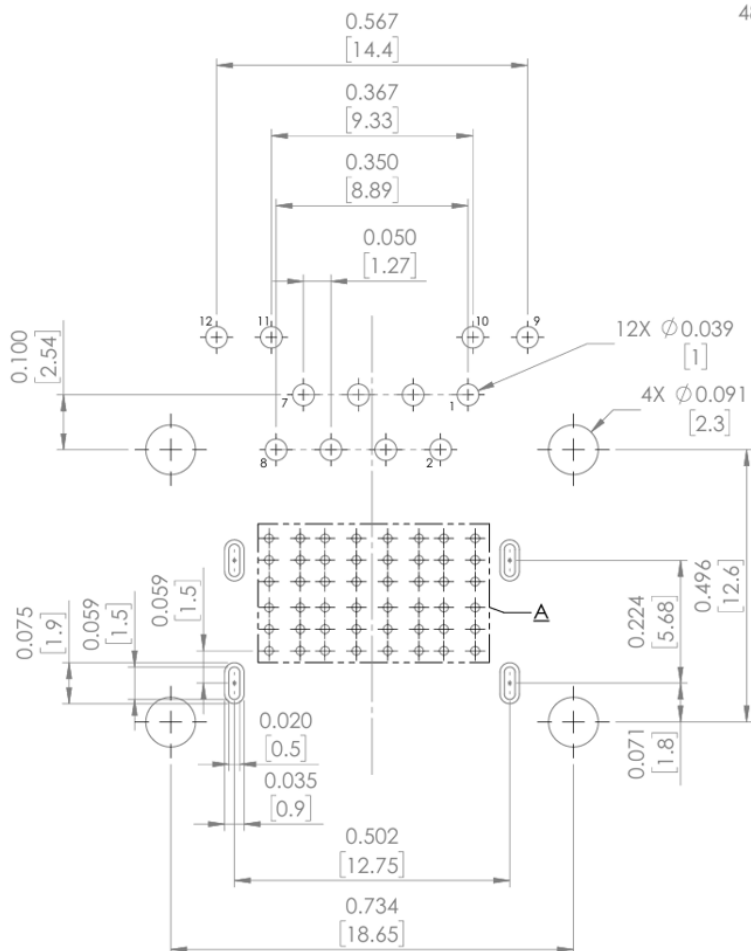


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RECOMMENDED LAND PATTERN



DIMENSIONS: INCHES [mm]

USB Pin Description:

PIN	Signal Name	Description	PIN	Signal Name
A1	GND	Ground return	B12	GND
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1
A4	VBUS	Bus Power	B9	VBUS
A5	CC1	Configuration Channel	B8	SBU2
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2
A8	SBU1	Sideband Use (SBU)	B5	CC2
A9	VBUS	Bus Power	B4	VBUS
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2
A12	GND	Ground return	B1	GND



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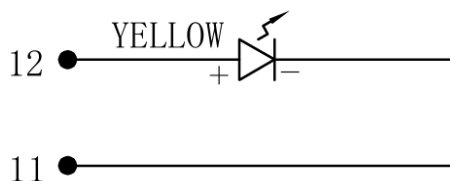


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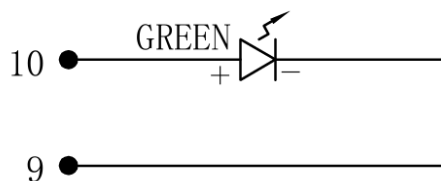
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LED polarity



LEFT LED



RIGHT LED

EMITTING COLOR	λ_p (nm)	VF @IF=20mA	IR @VR=5V
GREEN	565	1.8~2.1V	10 μ A max
YELLOW	585	1.8~2.1V	10 μ A max

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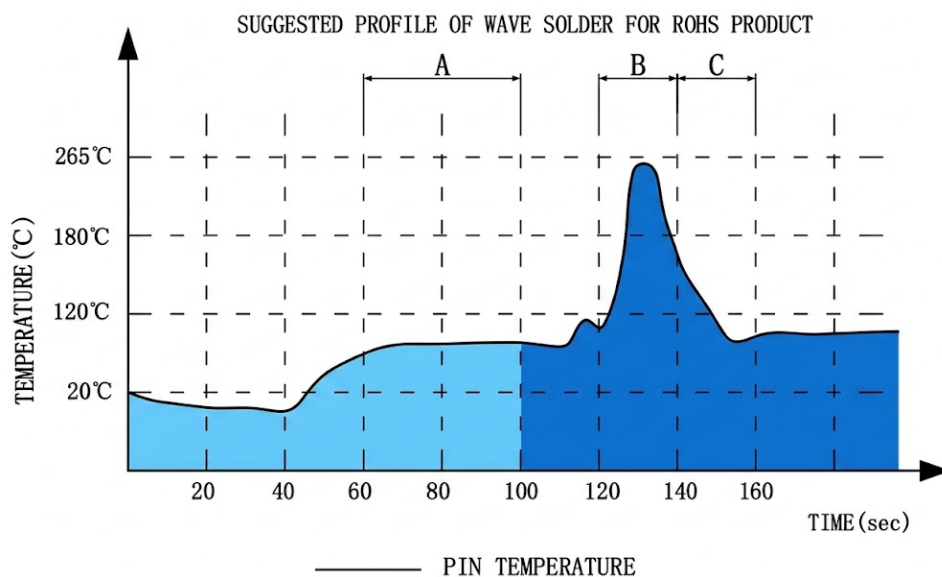


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MSL Level = N/A

Wave soldering Profile



Zone	Description	Temperature	Times
A	Preheat	$T_{SMIN} \sim T_{SMAX}$ 25°C ~ 120°C	100 sec. MAX
B	Wave Peak Temperature	T_P 250°C	5 sec.
C	Cooling Rate	$T_{COOLING}$	-5°C/Sec

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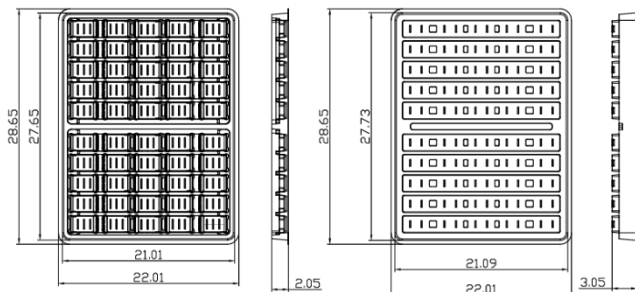
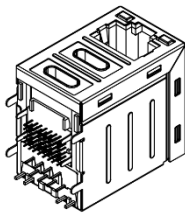
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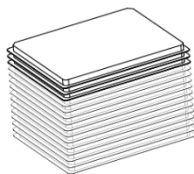
MSL Level = N/A

Packing

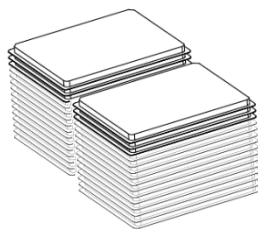
Packing: 50pcs/tray
Carton Packing: 500Pcs/10tray/Carton
Carton Size: 455x310x175mm
Gross Weight: Approx. 11.5KG/Carton



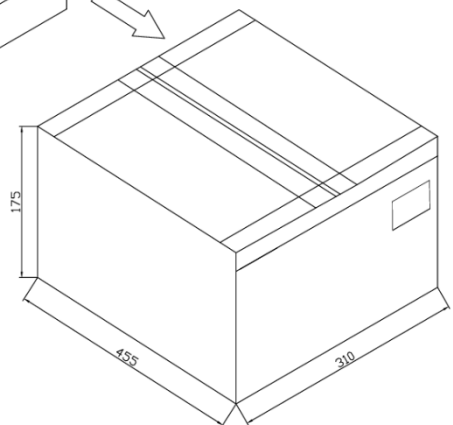
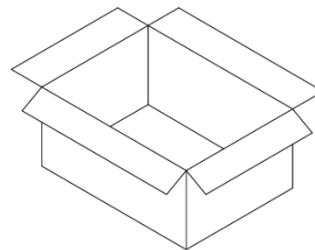
50 PCS/TRAY



5 TRAY/STACK



10 TRAY/2 STACK/CARTON



ATTENTION: Abracon LLC's products are Commercial-Off-The-Shelf (COTS, which are designed, intended and validated for use in commercial, industrial and automotive applications. The customer is responsible for testing and verifying the performance of an Abracon solution to meet their system-level requirements.



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