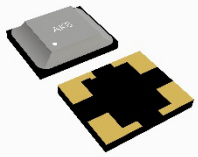
AK8 series

ClearClock™ Oscillator Family | Clock Oscillator| 1.0x0.8mm XO

Description

Abracon's AK8 series oscillator offers the latest high frequency crystal technology, in a compact SMD package size of 1.0 x 0.8 x 0.24mm. The Abracon AK8 series oscillator is available with an all-inclusive frequency stability of ±100ppm over an operating temperature range of -40℃ to 85℃. This series offers a supply voltage of 3.3V with LVPECL or LVDS (Standard and High Swing) output logic type.



Features

- Compact package 1.0 x 0.8 x 0.24mm
- Output - LVDS (Standard and High Swing) and LVPECL
- Frequency stability of ±100 ppm
- Au Diffusion Sealed SMD package
- REACH/RoHS II Compliant | MSL Level 2

Typical Applications

- Optical Transceivers and Modules
- Data Centers, Storage, and Servers
- Networking switches and gateways
- 100G/200G/400G/800G Ethernet
- Fibre Channel/SONET/SDH/PCIe
- Industrial and FPGA applications
- Test & measurement

Electrical Specifications

Parameters		Min.	Typ.	Max.	Unit	Notes
Frequency Range			156.25		MHz	
Supply Voltage (V_{DD})		3.135	3.3	3.465	V	Option "A"
		2.375	2.5	2.625		Option "B"
Supply Current (I_{DD})	LVPECL			57	mA	Option "P"
	LVDS			26		Option "D"
				35		Option "E"
Operating Temperature Range		-40		85	°C	See Options
Storage Temperature		-40		85	°C	
First Year Aging [Note 2]		-5		+5	ppm	At 25°C
Frequency Stability [Note 1]		-100		100	ppm	See Options

Note 1: Includes post reflow frequency tolerance, temperature stability, load pulling, power supply variation and first year aging.
Note 2: Relative to initial measured frequency @ +25°C.

Electrical Specifications *continued*

Parameters			Min.	Typ.	Max.	Unit	Notes	
Rise (Tr) / Fall (Tf) Time[Note 3]	LVPECL				0.50	ns	R_L=50Ω to V_{dd}–2.0V on each output, Option P	
	LVDS				0.40		R_L= 100Ω (Between Outputs), Option D & E	
Duty Cycle			45		55	%		
Start-up Time					2	ms		
Output High Voltage (VOH) Output Low Voltage (VOL)	LVPECL	V_{OH}	V_{DD}-1.025		V_{DD}-0.88	V	R_L=50Ω into V_{dd}–2.0V on each output	
		V_{OL}	V_{DD}-1.81		V_{DD}-1.62			
Output Voltage	LVDS	V_{OD}	247		454	mV	R_L= 100Ω (Between Outputs)	Option “D”
			500		1000	mV		Option “E”
		ΔV_{OD}			50	mV		Option “D & E”
Offset Voltage		V_{OS}	1.125		1.375	V		
		ΔV_{OS}			50	mV		
Differential Output Voltage Swing (Vopp)		LVPECL	400			mV	OUT-OUTN	Option “P”
		LVDS	254					Option “D”
			500					Option “E”
RMS Phase Jitter (12kHz to 20MHz from Carrier)			See Table 1 below				Vdd, output logic type and Carrier frequency dependent	

Note 3: Measured over 20% to 80% of waveform.

Phase Noise Specifications

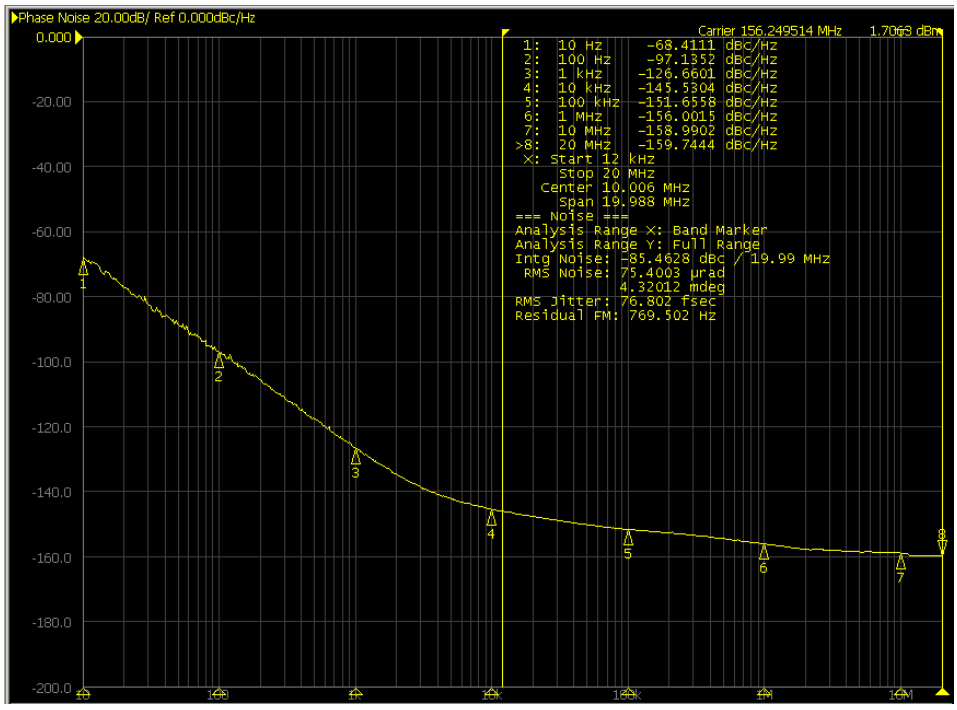
Table 1
RMS Phase Jitter 12kHz – 20MHz BW[Note 4,5]

Carrier Frequency	Output Logic	Supply Voltage (2.5V)		Supply Voltage (3.3V)	
		RMS Jitter		RMS Jitter	
		Typ. (fs)	Max (fs)	Typ. (fs)	Max (fs)
156.25	LVDS (V_{OD} = 247mV-454mV)	76	100	76	100
	LVDS (V_{OD} = 500mV-1000mV)	N/A	N/A	80	100
	LVPECL	N/A	N/A	77	100

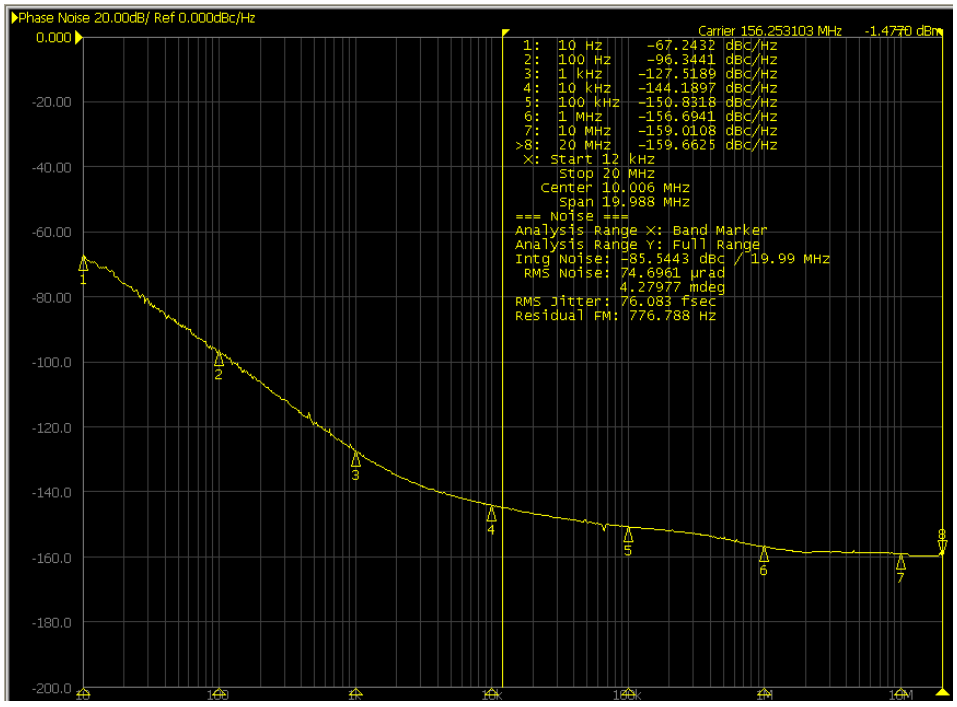
Note 4: Guaranteed by characterization; RMS Phase Jitter specifications are inclusive of any spurs
Note 5: Phase jitter measured with Keysight E5052B Signal Source Analyzer

Phase Noise Plots [Note 5]

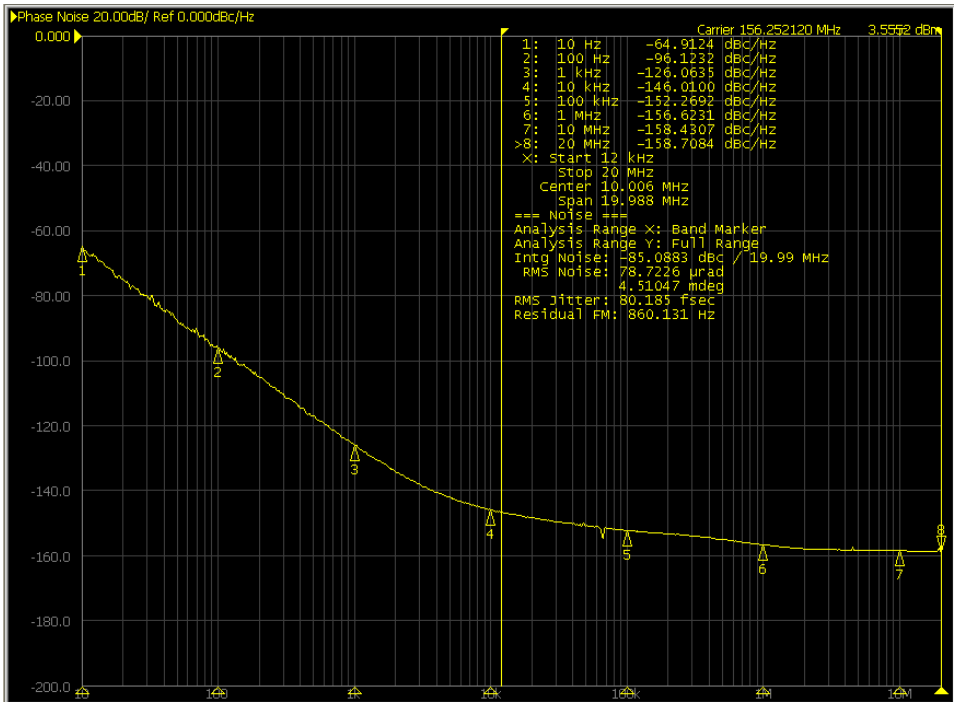
Plot #1: LVPECL, 3.3V, 156.25 MHz



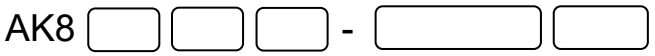
Plot #2: LVDS (V_{OD} = 247mV-454mV), 3.3V, 156.25 MHz



Plot #3: LVDS (V_{OD} = 500mV-1000mV), 3.3V, 156.25 MHz



Part Identification (Left Blank If Standard)



(1): Output
P: LVPECL
D: LVDS (V_{OD}=247mV-454mV)
E: LVDS (V_{OD}=500mV-1000mV)

(3): Frequency Stability
A: ±100 ppm over -20°C to +70°C
B: ±100 ppm over -40°C to +85°C

(5): Packaging
Blank: Bulk
T3: 3k pcs / reel

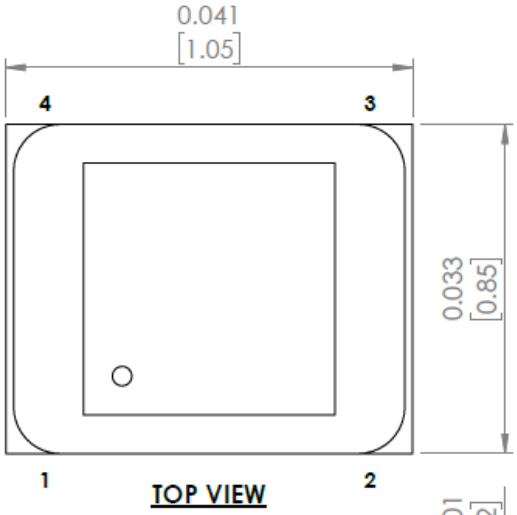
(2): Vdd
A: 3.3V
B: 2.5V*

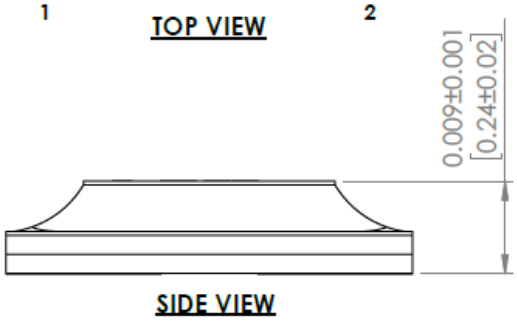
*Only Available for Option D

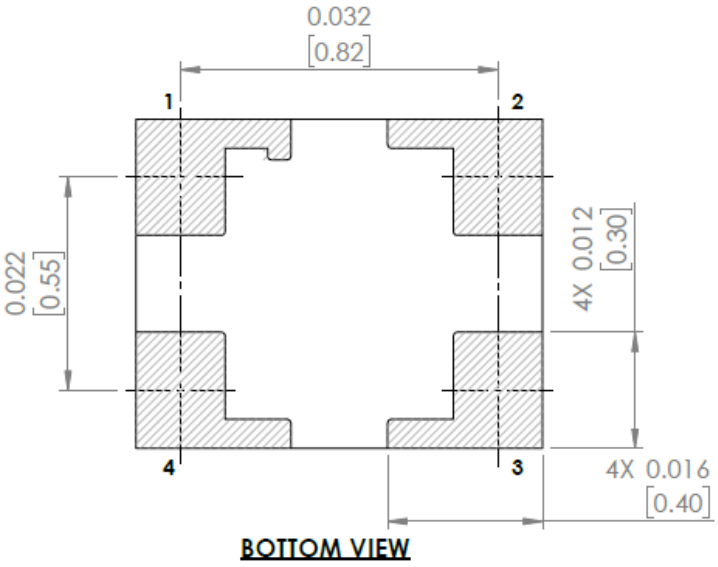
(4): Output Frequency

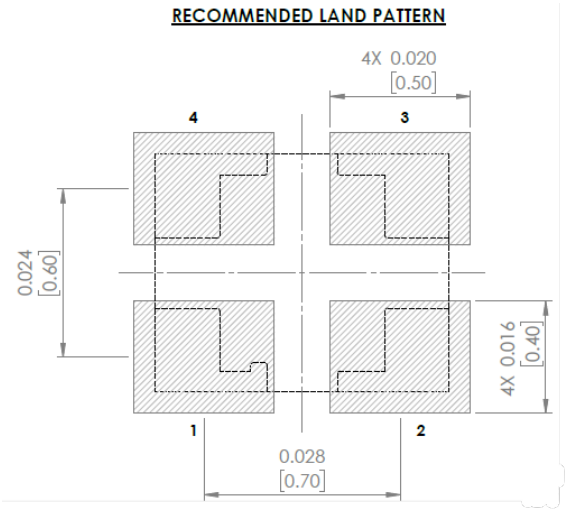
Please specify the Frequency in units of MHz out to 4**-digit** accuracy after the decimal.
Example: Output frequency of 156.25MHz = '156.2500'

Mechanical Dimensions









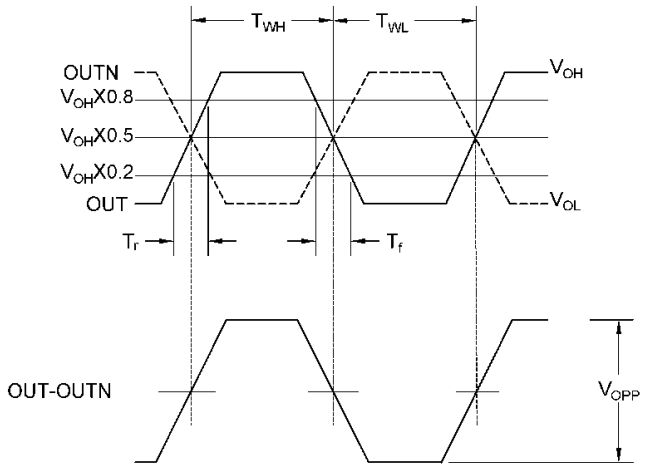
Pin #	Function
1	GND
2	Output
3	Complimentary Output
4	V_{DD}

Note: Recommended to use approximately 0.01µF bypass capacitor between PIN 1 and PIN 4

Dimensions: inches (mm)

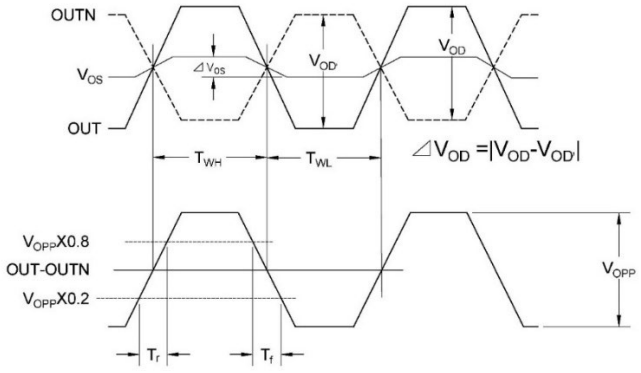
Output Waveform Diagram

LVPECL: Output Wave Form (Duty, Tr, Tf, V_{OH}, V_{OL}, Vopp)



$$\text{Duty Cycle} = \frac{T_{WH}}{T_{WH}+T_{WL}} \text{ X } 100\%$$

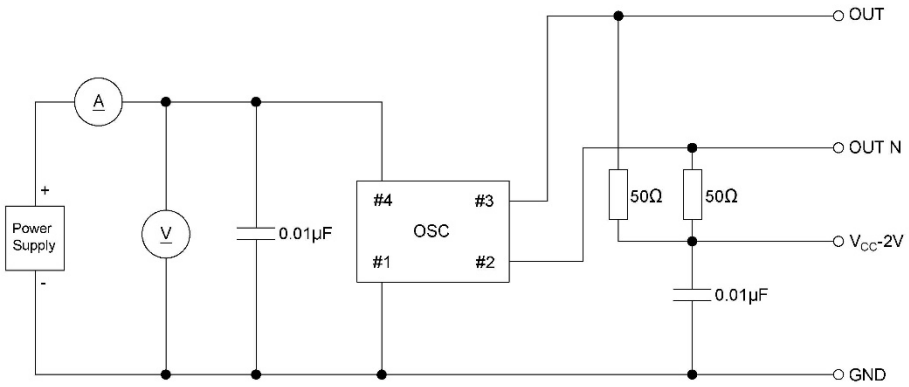
LVDS: Output Wave Form (Duty, Tr, Tf, V_{OD}, ΔV_{OD}, V_{OS}, ΔV_{OS}, Vopp)



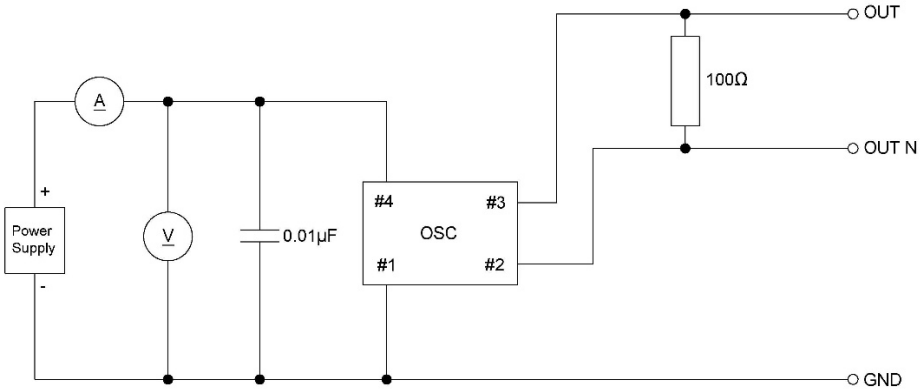
$$\text{Duty} = \frac{T_{WH}}{T_{WH}+T_{WL}} \text{ X } 100\%$$

Recommended Test Circuit

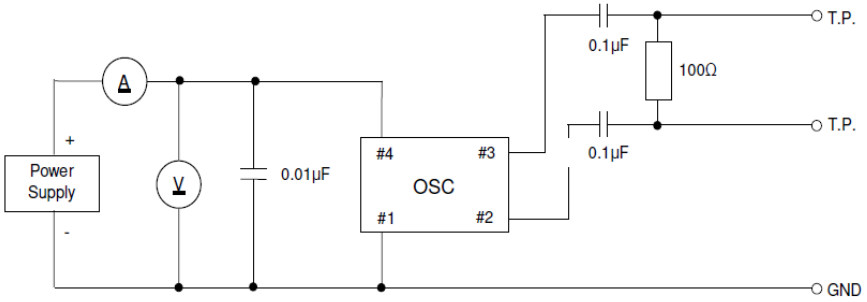
LVPECL



LVDS (VOD=247mV-454mV)



LVDS (VOD=500mV-1000mV)



Absolute Maximum Ratings [Note 6]

Parameters	Min.	Typ.	Max.	Unit	Notes
Supply Voltage	-0.5		5.0	V	
Input Voltage	-0.5		V_{DD}+0.5	V	
Output Voltage	-0.5		V_{DD}+0.5	V	
Maximum Junction Operating Temperature		-55	150	ºC	
Ambient Operating Temperature Range	-40		85	ºC	Industrial
Reflow Temperature			260	°C	See Reflow Profile
ESD Protection	2kV HBM, 0.75kV CDM				
MSL Rating	2				

Note 6: Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability. The data sheet limits are not guaranteed if the device is operated beyond the recommended operating conditions.

Reflow Profile [JEDEC J-STD-020]

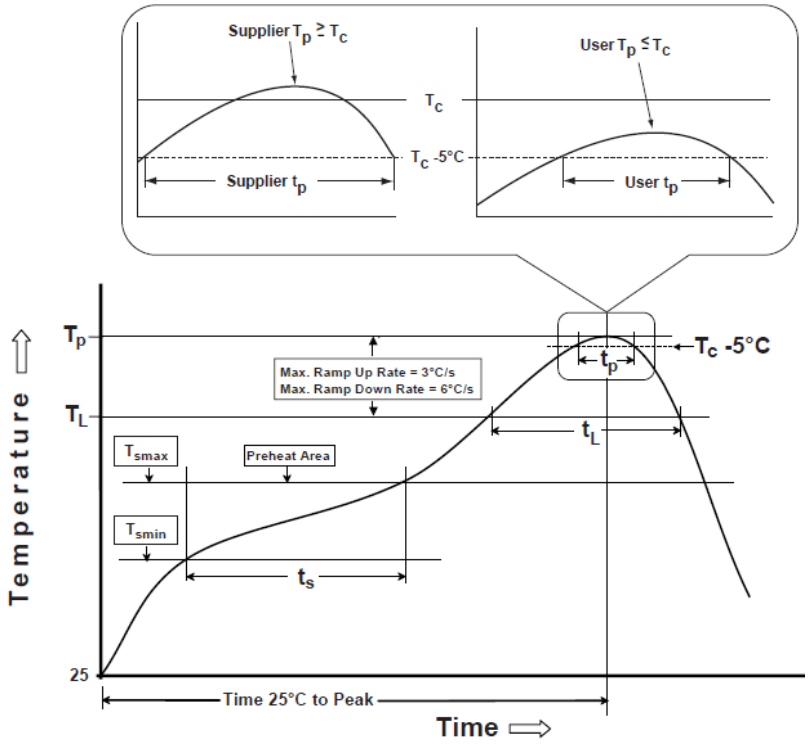


Table 1

SnPb Eutectic Process Classification Temperatures (T_c)		
Package Thickness	**Volume mm³ <350**	**Volume mm³ ≥350**
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2

Pb-Free Process Classification Temperatures (T_c)			
Package Thickness	**Volume mm³ <350**	**Volume mm³ 350-2000**	**Volume mm³ >2000**
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm - 2.5 mm	260 °C	250 °C	245 °C
>2.5 mm	250 °C	245 °C	245 °C

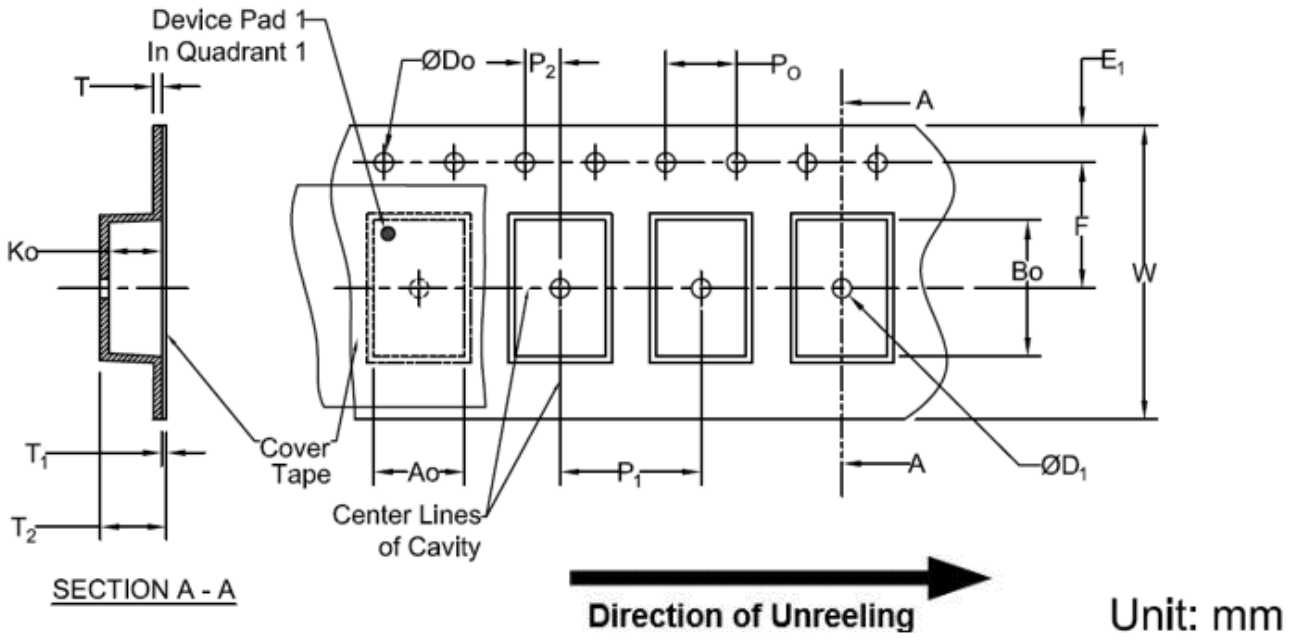
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T_{smin})	100°C	150°C
Temperature maximum (T_{smax})	150°C	200°C
Time (T_{smin} to T_{smax}) (t_s)	60 - 120 sec.	60 - 120 sec.
Average ramp-up rate (T_{smax} to T_P)	3°C/sec. max	3°C/sec. max
Liquidous temperature (T_L)	183°C	217°C
Time at liquidous (t_L)	60 - 150 sec.	60 - 150 sec.
Peak package body temperature (T_P)*	**see Table 1**	**see Table 2**
Time (t_p)** within 5°C of the specified classification temperature (T_C)	20 sec.	30 sec.
Ramp-down rate (T_p to T_{smax})	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

*Tolerance for peak profile temperature (T_P) is defined as a supplier minimum and a user maximum.

**Tolerance for time at peak profile temperature (t_p) is defined as supplier minimum and a user maximum.

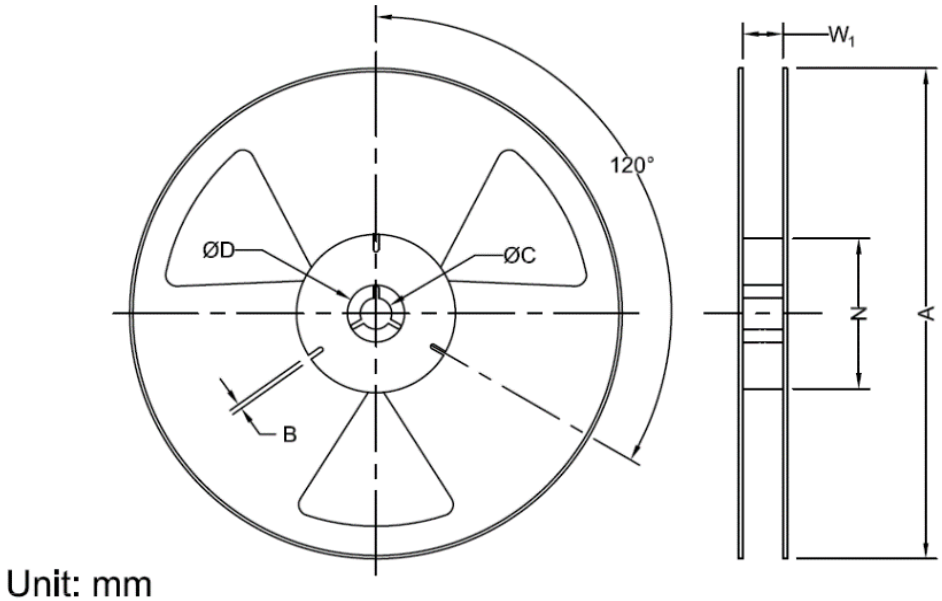
Packaging

T3: 3,000pcs/reel



Tape Specifications (mm)							
Width	Ao	Bo	Do	D_1 (Min)	E_1	F	Ko
8mm	1.0±0.05	1.2±0.05	1.5+0.1/-0.0	1.0	1.75±0.1	3.5±0.05	0.45±0.05
Width	P_1	P_2	P_0	T	T_1 (Max)	T_2 (Max)	W (Max)
8mm	4.0±0.1	2.0±0.05	4.0±0.1	0.20±0.05	0.1	0.45	8.0±0.2

***Note: Compliant to EIA-481**



Unit: mm

Reel Specifications (mm)							
Width	Qty/Reel	A	B	C	D (Min)	N	*W_1
8mm	3000	180+0/-3	4.0±0.2	13.0±0.5	20.2	60+1/-0	9.0±0.3

***Note: Measured at Hub**