

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
60V	8.0mΩ @ V <sub>GS</sub> = 10V	16.5A

## Description and Applications

This new generation N-Channel enhancement mode MOSFET is designed to minimize R<sub>DS(ON)</sub> yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switches.

- Motor control
- DC-DC converters
- Power management

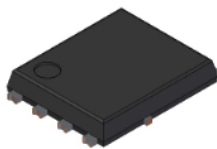
## Features and Benefits

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching – Ensures More Reliable and Robust End Application
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile – Ideal for Thin Applications
- Wettable Flank for Improved Optical Inspections
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**  
<https://www.diodes.com/quality/product-definitions/>
- **An automotive-compliant part is available under separate datasheet (DMNH6008SPSWQ)**

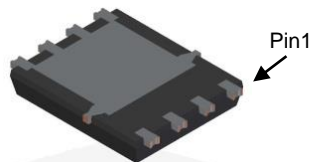
## Mechanical Data

- Package: PowerDI<sup>®</sup>5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 Ⓜ3
- Weight: 0.097 grams (Approximate)

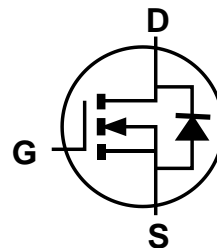
PowerDI5060-8/SWP (Type UX)



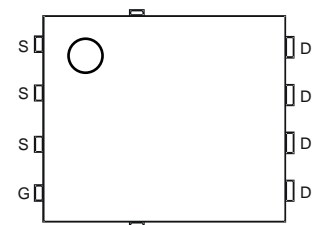
Top View



Bottom View



Internal Schematic



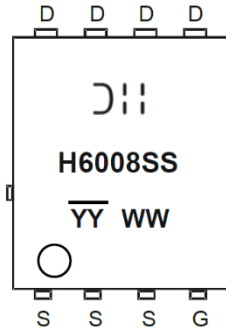
Top View  
Pin Configuration

## Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DMNH6008SPSW-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



**DII** = Manufacturer's Marking  
**H6008SS** = Product Type Marking Code  
**YYWW** = Date Code Marking  
**YY** = Year (ex: 25 = 2025)  
**WW** = Week Code (01 to 53)

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	60	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +100°C	I <sub>D</sub>	16.5 11.7	A
	Steady State	T <sub>C</sub> = +25°C T <sub>C</sub> = +100°C	I <sub>D</sub>	88 63	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	352	A
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	90	A
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)			I <sub>SM</sub>	352	A
Avalanche Current (Note 7) L=0.1mH			I <sub>AS</sub>	62	A
Avalanche Energy (Note 7) L=0.1mH			E <sub>AS</sub>	194	mJ

## Thermal Characteristics

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)			P <sub>D</sub>	1.6	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R <sub>θJA</sub>	95	°C/W
Total Power Dissipation (Note 6)			P <sub>D</sub>	3.3	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R <sub>θJA</sub>	46	°C/W
Thermal Resistance, Junction to Case (Note 6)			R <sub>θJC</sub>	1.6	
Operating and Storage Temperature Range			T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  - I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±16V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	—	4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	6.0	8.0	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>iss</sub>	—	2597	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	437	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	118	—		
Gate Resistance	R <sub>G</sub>	—	2.0	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	40.1	—	nC	V <sub>DD</sub> = 30V, I <sub>D</sub> = 20A
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	21.2	—		
Gate-Source Charge	Q <sub>gs</sub>	—	8.3	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	11.8	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	5.7	—	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V, R <sub>G</sub> = 1Ω, I <sub>D</sub> = 20A
Turn-On Rise Time	t <sub>R</sub>	—	5.0	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	15.6	—		
Turn-Off Fall Time	t <sub>F</sub>	—	3.3	—		
Reverse-Recovery Time	t <sub>RR</sub>	—	33	—	ns	I <sub>F</sub> = 20A, di/dt = 100A/μs
Reverse-Recovery Charge	Q <sub>RR</sub>	—	33	—	nC	

Notes: 8. Short duration pulse test used to minimize self-heating effect.  
9. Guaranteed by design. Not subject to product testing.

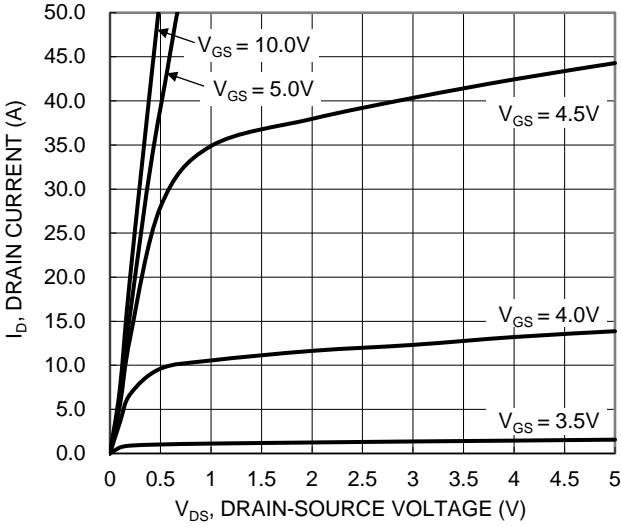


Figure 1. Typical Output Characteristic

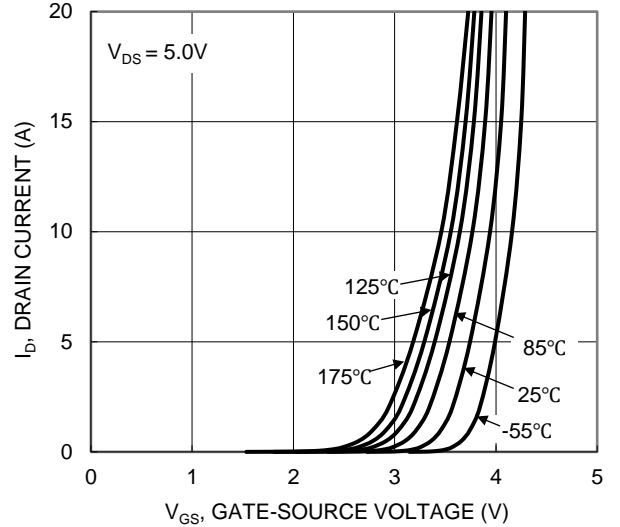


Figure 2. Typical Transfer Characteristic

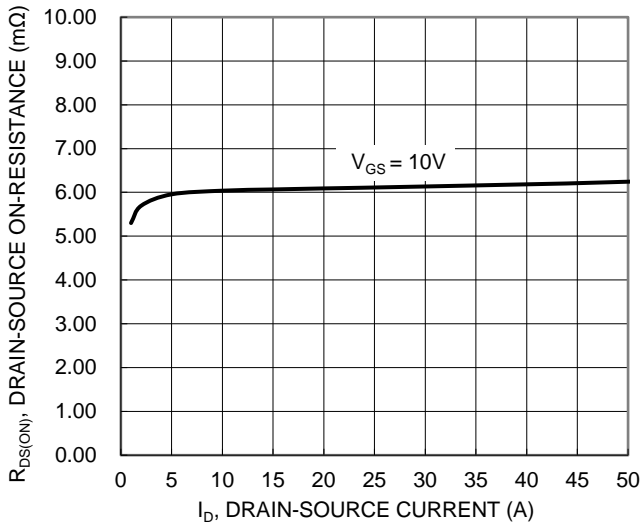


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

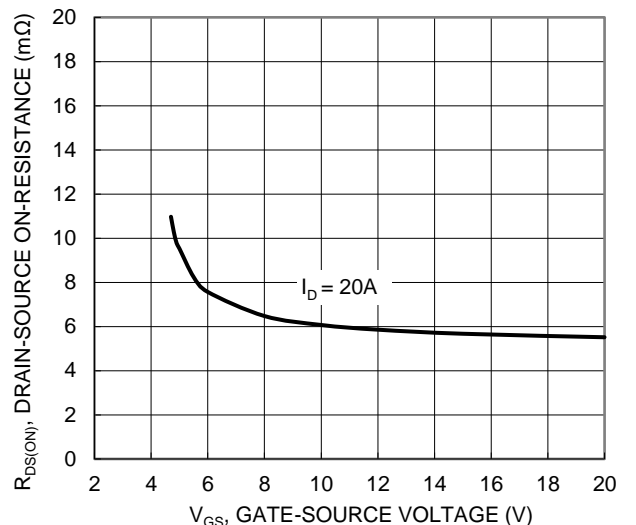


Figure 4. Typical Transfer Characteristic

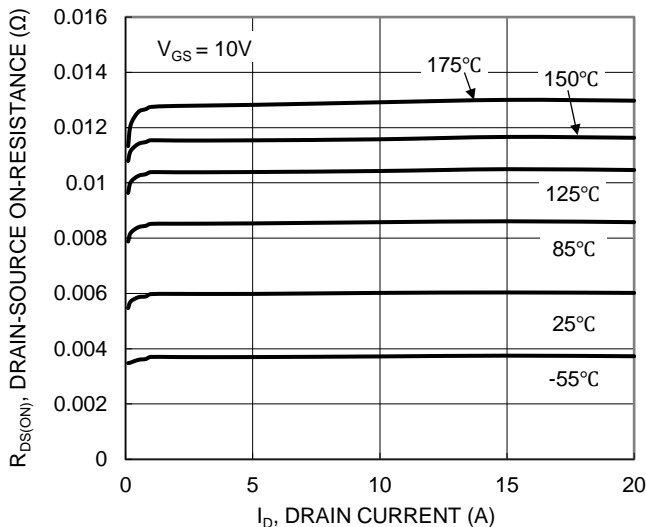


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

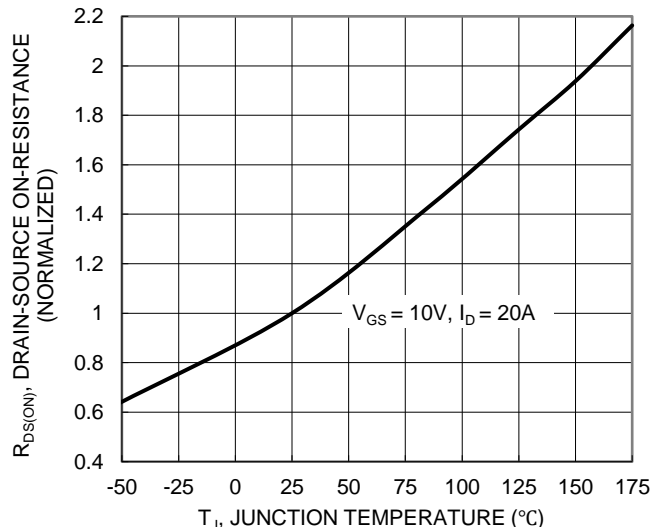


Figure 6. On-Resistance Variation with Temperature

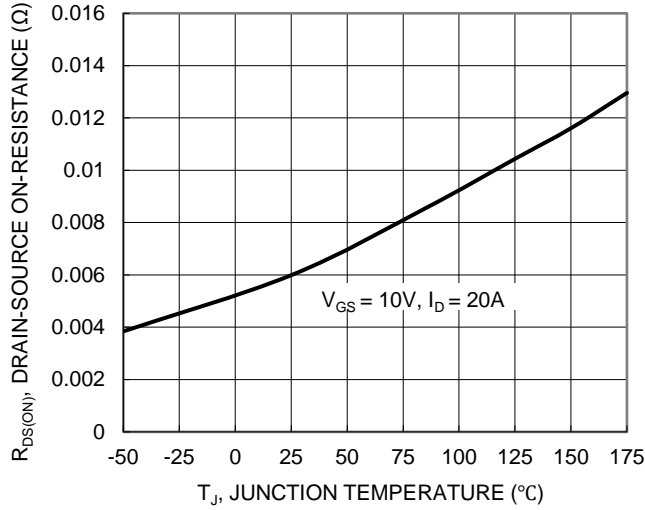


Figure 7. On-Resistance Variation with Temperature

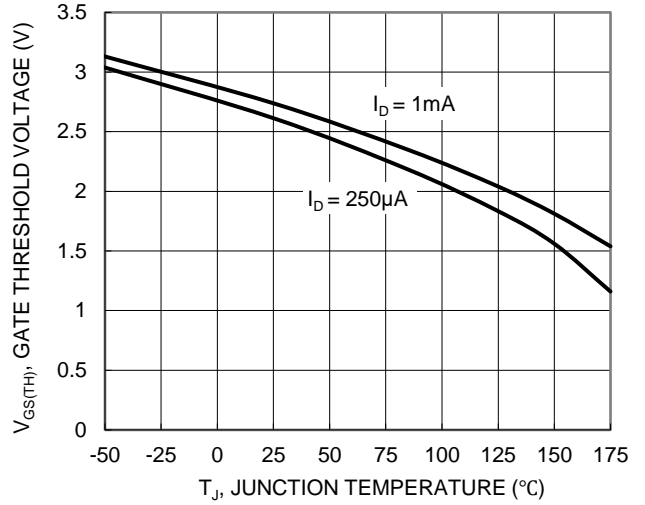


Figure 8. Gate Threshold Variation vs. Temperature

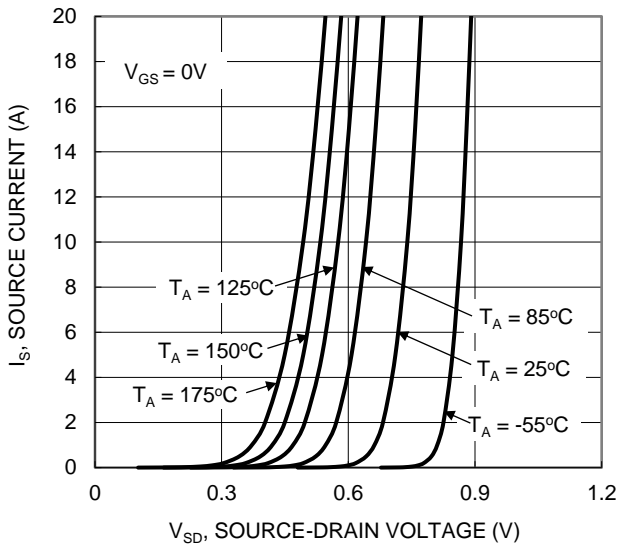


Figure 9. Diode Forward Voltage vs. Current

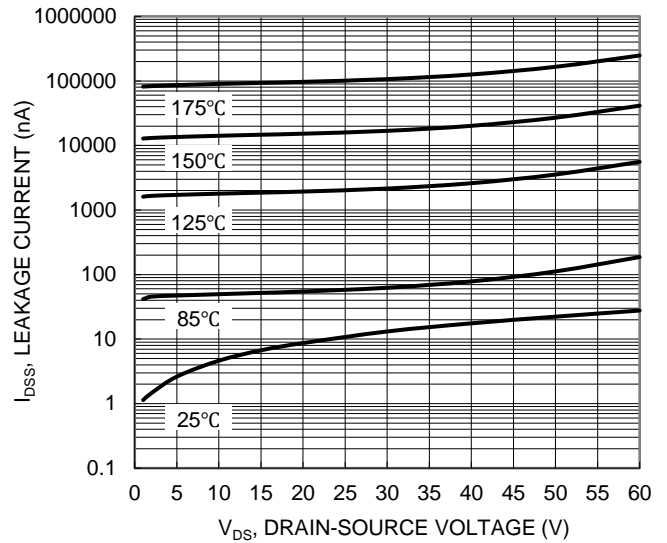


Figure 10. Typical Drain-Source Leakage Current vs. Voltage

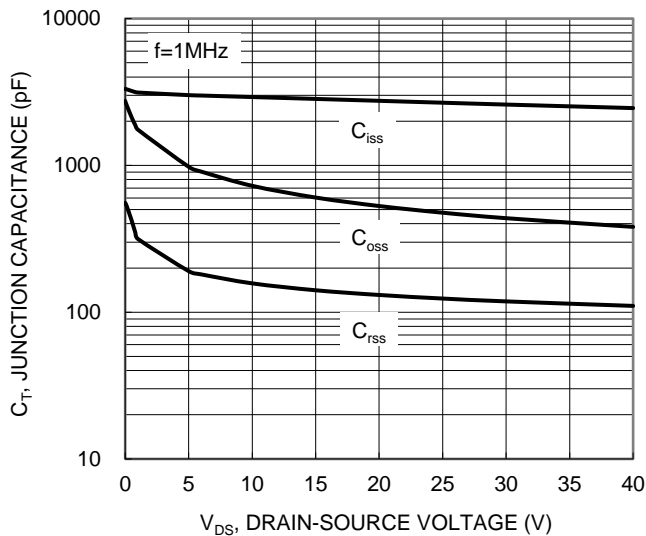


Figure 11. Typical Junction Capacitance

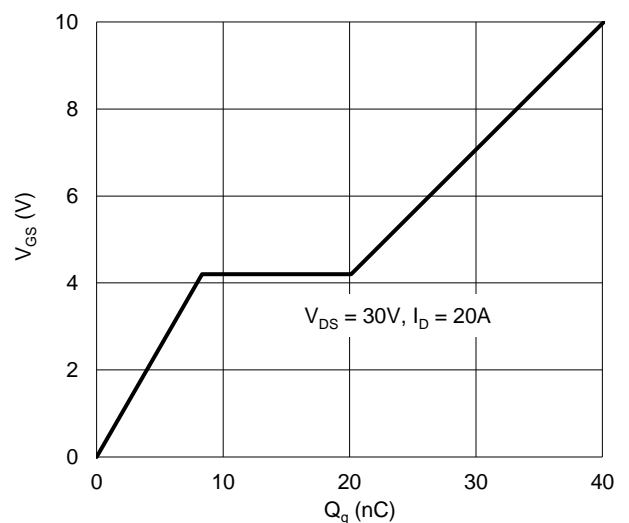


Figure 12. Gate Charge

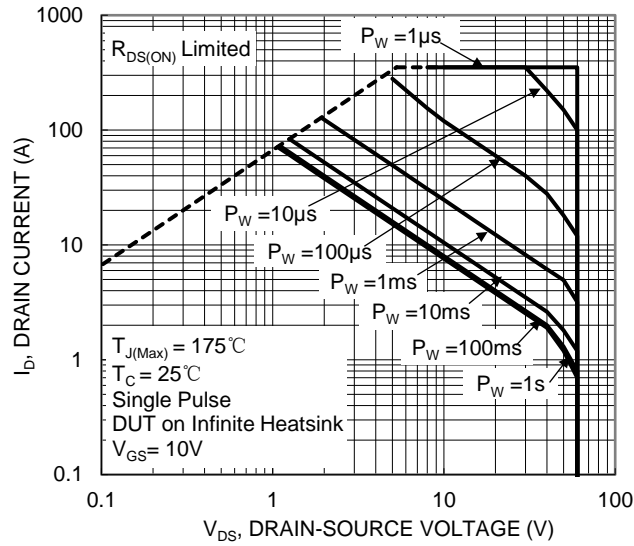


Figure 13. SOA, Safe Operation Area

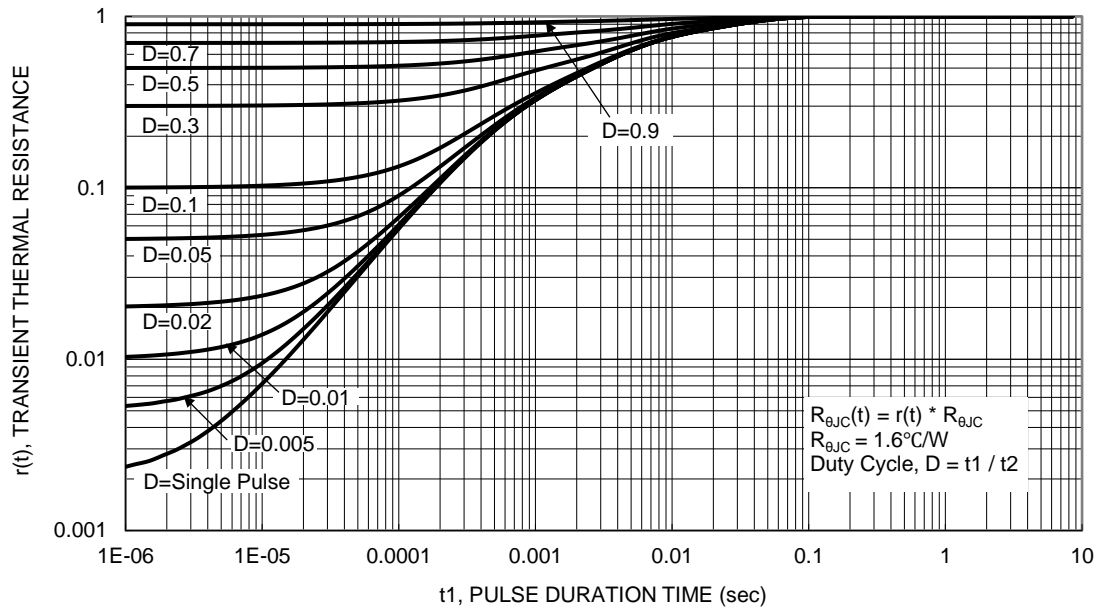


Figure 14. Transient Thermal Resistance



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