

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
30V	4.2Ω @ V <sub>GS</sub> = 5V	200mA
	2.8Ω @ V <sub>GS</sub> = 10V	260mA

## Description

This new generation MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

## Applications

- DC-DC converters
- Power-management functions
- Battery-operated systems and solid-state relays
- Drivers: relays, solenoids, lamps, hammers, displays, memories, transistors, etc.

## Features

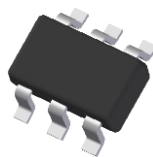
- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Small Surface-Mount Package
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

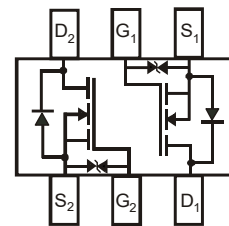
- Package: SOT563
- Package Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (B)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



SOT563



Top View



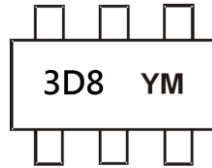
Top View  
Internal Schematic

## Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DMN63D8LV-7	SOT563	3,000	Tape & Reel
DMN63D8LV-7B	SOT563	8,000	Tape & Reel (Note 5)
DMN63D8LV-13	SOT563	10,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.
  5. Change the pitch from 4mm to 2mm in tape & reel.

## Marking Information



3D8 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: M = 2025)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2012	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	Z	-	M	N	P	R	S	T	U	V	W	X

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6), V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	260 200	mA
Continuous Drain Current (Note 6), V <sub>GS</sub> = 5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	220 160	mA
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	800	mA

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation	(Note 6)	P <sub>D</sub>	450	mW
Thermal Resistance, Junction to Ambient	(Note 6)	R <sub>θJA</sub>	281	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Note: 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—	—	V	V <sub>GS</sub> = 0, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1.0	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0
Gate-Body Leakage	I <sub>GSS</sub>	—	—	±10.0	μA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.8	—	1.5	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	—	2.8	Ω	V <sub>GS</sub> = 10.0V, I <sub>D</sub> = 250mA
		—	—	3.8		V <sub>GS</sub> = 5.0V, I <sub>D</sub> = 250mA
		—	—	4.2		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 250mA
		—	—	4.5		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 250mA
		—	—	13		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 10mA
Forward Transconductance	g <sub>FS</sub>	80	—	—	mS	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.115A
Diode Forward Voltage	V <sub>SD</sub>	—	0.8	1.2	V	V <sub>GS</sub> = 0, I <sub>S</sub> = 115mA
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	22.0	—	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	3.2	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	2.0	—		
Gate Resistance	R <sub>G</sub>	—	79.9	—	Ω	V <sub>DS</sub> = 0, V <sub>GS</sub> = 0, f = 1.0MHz
Total Gate Charge V <sub>GS</sub> = 10V	Q <sub>g</sub>	—	0.87	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 150mA
Total Gate Charge V <sub>GS</sub> = 4.5V	Q <sub>g</sub>	—	0.43	—		
Gate-Source Charge	Q <sub>gs</sub>	—	0.11	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	0.11	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	3.3	—	ns	V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.115A, V <sub>GEN</sub> = 10V, R <sub>GEN</sub> = 25Ω
Turn-On Rise Time	t <sub>r</sub>	—	3.2	—		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	12.0	—		
Turn-Off Fall Time	t <sub>f</sub>	—	6.3	—		

Notes: 7. Short duration pulse test used to minimize self-heating effect.  
8. Guaranteed by design. Not subject to production testing.

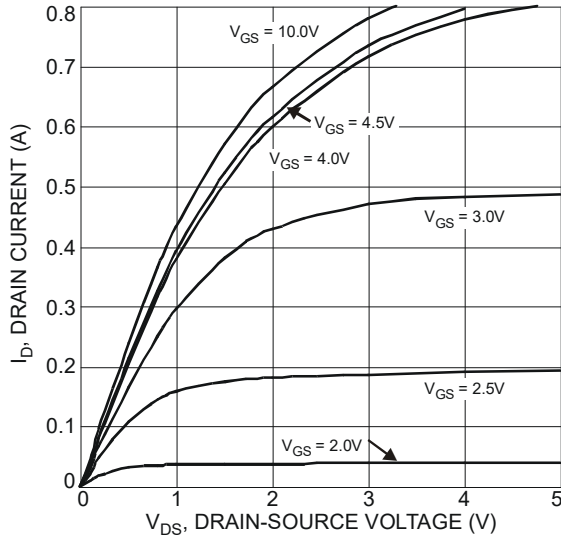


Figure 1 Typical Output Characteristic

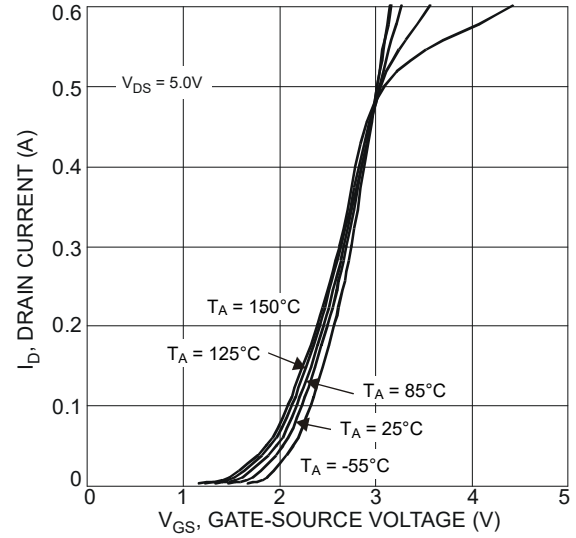


Figure 2 Typical Transfer Characteristics

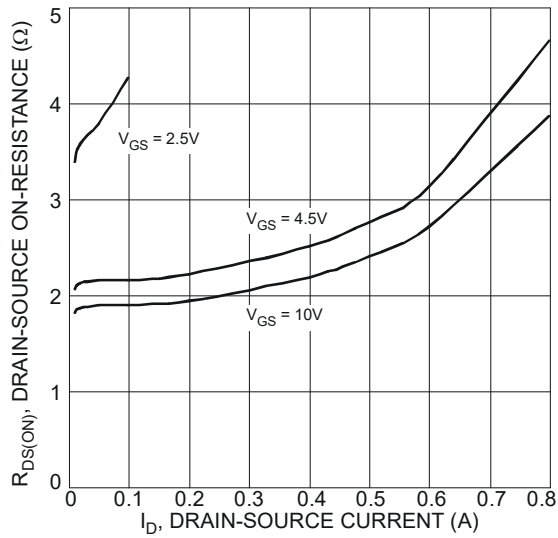


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

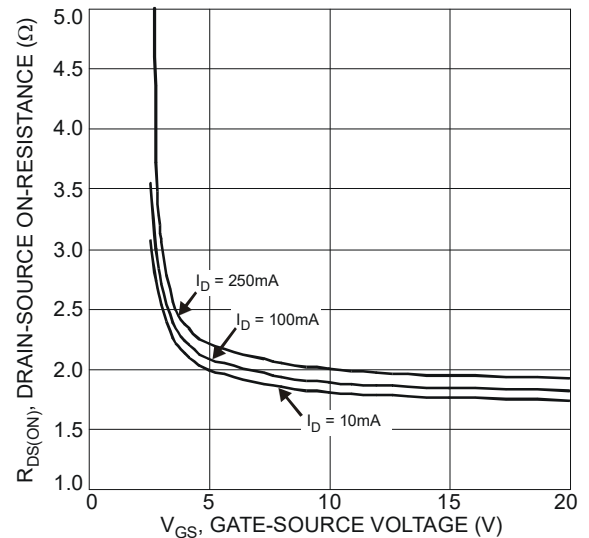


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

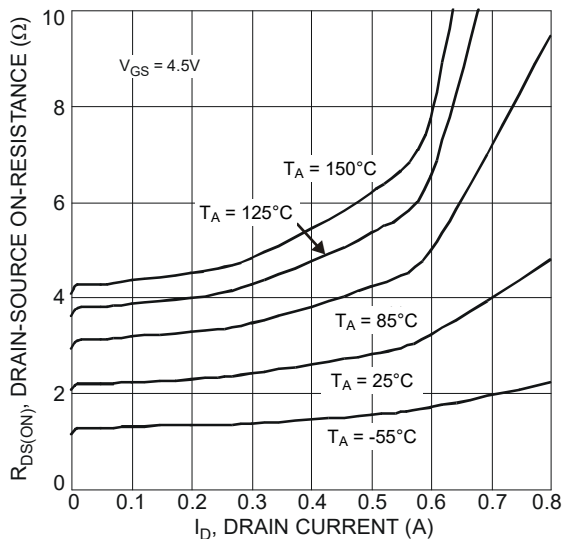


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

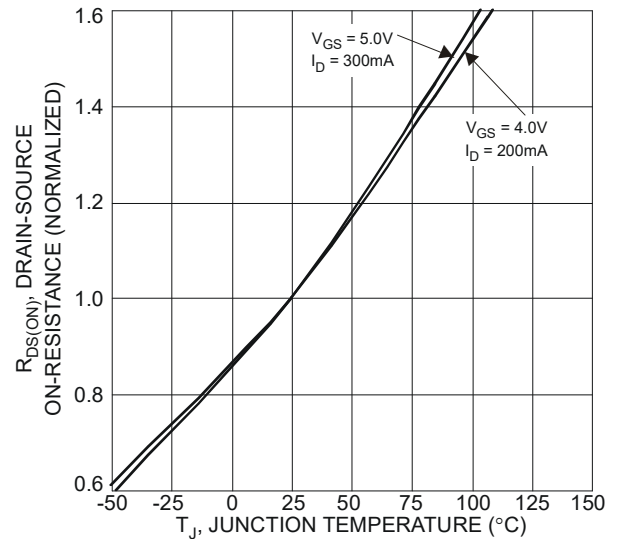


Figure 6 On-Resistance Variation with Temperature

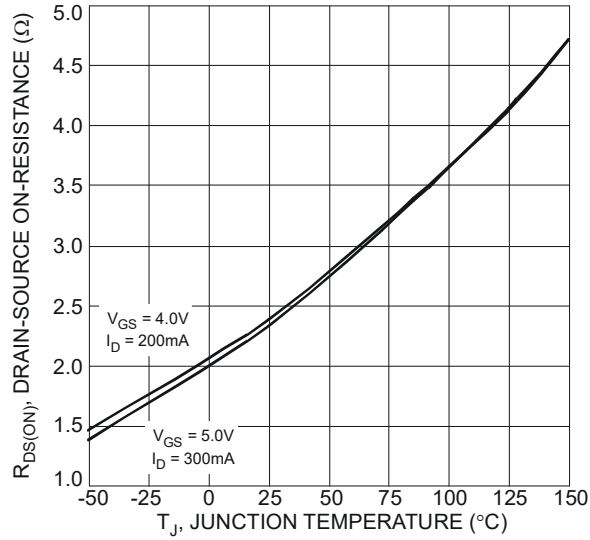


Figure 7 On-Resistance Variation with Temperature

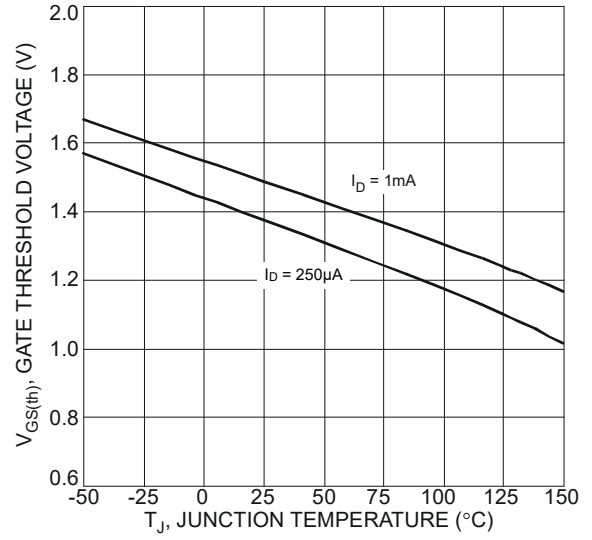


Figure 8 Gate Threshold Variation vs. Junction Temperature

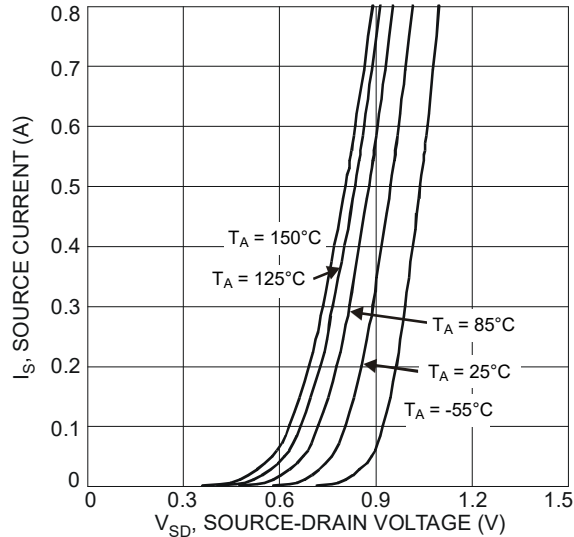
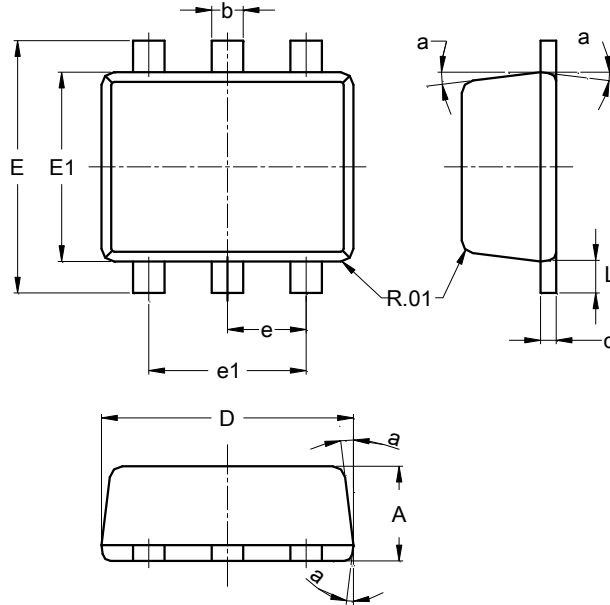


Figure 9 Diode Forward Voltage vs. Current

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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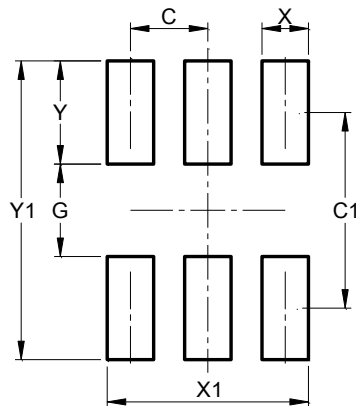


SOT563			
Dim	Min	Max	Typ
A	0.55	0.60	--
b	0.15	0.30	0.20
c	0.10	0.18	0.11
D	1.50	1.70	1.60
E	1.55	1.70	1.60
E1	1.10	1.25	1.20
e	--	--	0.50
e1	0.90	1.10	1.00
L	0.10	0.30	0.20
a	8°	9°	7°
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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Dimensions	Value (in mm)
C	0.500
C1	1.270
G	0.600
X	0.300
X1	1.300
Y	0.670
Y1	1.940

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