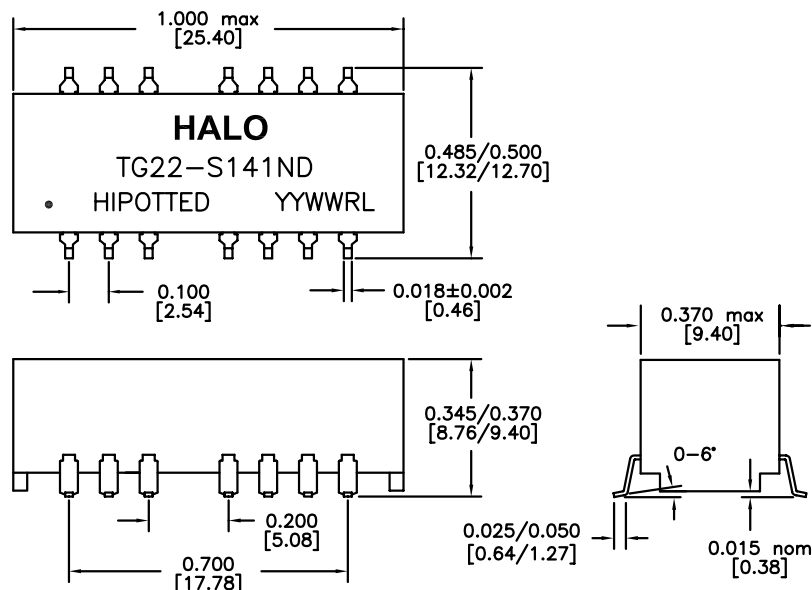
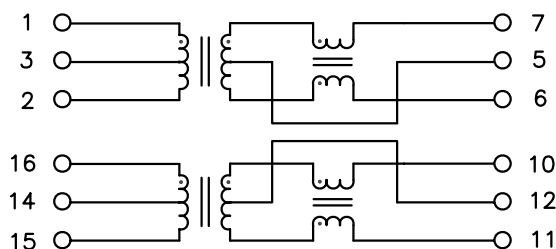




DIMENSIONS: Inch[mm]
CO-PLANARITY: 0.004[0.10]
TOLERANCES: ±0.005 INCH IF NOT SPECIFIED



PART NO. : TG22-S141NDRL

16PIN SMT 10/100BASE-TX ISOLATION MODULE

OPTIMIZED FOR AMD 79C973/5 PHY CHIPS

RoHS COMPLIANT

COMPATIBLE TO LEAD-FREE SOLDERING PROCESS
CONDITION PER IPC/JEDEC J-STD-020C

UL/EN60950 AND DEMKO RECOGNIZED

OPERATING TEMPERATURE 0/+70°C

ELECTRICAL SPECIFICATIONS @25° C

TURN RATIO

TX P16-15:P10-11

1CT:1.41CT±3%

RX P1-2:P7-6

1CT:1CT±3%

OCL (100KHz,0.1Vrms,8mA)

P7-6,P10-11

350µH min

DCR P1-2,P16-15

0.9Ω max

LL P1-2,P16-15

0.4µH max

Cw/w

20pF typ

CROSSTALK 0.1-100MHz

-40dB typ

CMR 0.1-100MHz

-40dB typ

ISOLATION

1,500Vrms



HALO/PBL

CALIFORNIA, USA
KOWLOON, HONG KONG
SINGAPORE

TITLE ISOLATION MODULE

FOR 10/100BASE-TX

PART NO. TG22-S141NDRL

SCALE NONE

PAGE 1 OF 1

SIGNATURES

DRAWN PETER LU

CHECKED LEI KEONG

APPROVED PETER LU

FILE S141NDRL.DWG

DATE

5/2/07

5/2/07

5/2/07

REV.

A

DESC.

FIRST ISSUE

DATE

5/2/07