



QPC6742

75Ω SP4T Switch 5MHz to 2000MHz

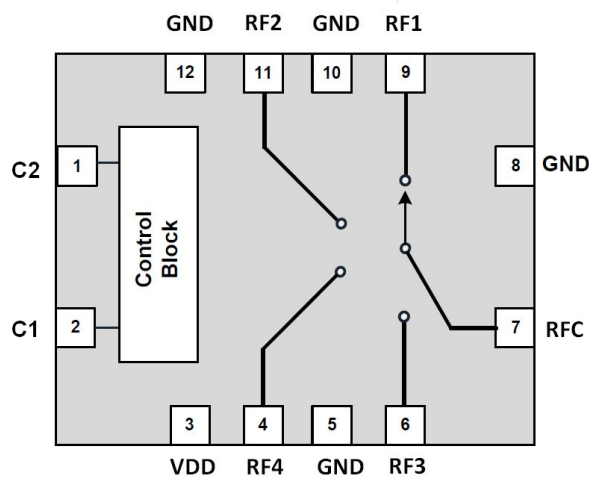
Product Overview

The QPC6742 is a 75Ω Silicon on Insulator (SOI) single-pole, four throw (SP4T) switch designed for use in CATV, satellite set top, and other high-performance communications systems. It offers a high isolation symmetric topology with excellent linearity and power handling capability. No blocking caps are necessary on the RF ports. QPC6742 is packaged in a convenient 1.8mm x 1.8mm QFN package



12 Pin 1.8 x 1.8mm QFN Package

Functional Block Diagram



Top View

Ordering Information

Part No.	Description
QPC6742SQ	Sample bag with 25 pieces
QPC6742SR	7" Reel with 100 pieces
QPC6742TR7	7" Reel with 2500 pieces
QPC6742PCK	5–2000MHz PCBA with 5 pc. sample bag

Key Features

- 5 MHz to 2000 MHz Operation
- 5 MHz to 3300 MHz Operation with Additional Matching
- Low Insertion Loss: 0.35 dB at 800 MHz
- No Blocking Caps Required Unless Voltage on RF Line
- High Isolation: 42 dB at 800 MHz
- High Input IP3: 82 dBm at 850 MHz
- 2kV ESD
- +1.8V Logic Compatible
- 3V to 5V Operation

Applications

- MDU Amplifiers
- Point To Point
- Optical Nodes
- Set Top Box
- PCTV
- Multi-tuner DVR



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Absolute Maximum Ratings

Parameter	Rating
Control Voltage ($V_{C1, C2}$)	+3.0V
Supply Voltage (V_{DD})	+6.0V
Maximum CW Input Power at 25°C	+35dBm
Max Input Power During Active Switching	+27dBm
Storage Temperature Range	-40 to +150°C

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Supply Voltage, V_{DD}	+2.7	+3	+5.5	V
Temperature Range	-40		+85	°C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

Electrical Specifications

Parameter	Conditions ⁽¹⁾	Min	Typ	Max	Units
Frequency Range		5		2000	MHz
Insertion Loss (RFC to RF1/RF2/RF3/RF4)	5MHz		0.13		dB
	50MHz		0.15		
	800MHz		0.25		
	1.2GHz		0.3		
	2GHz		0.4		
Return Loss ⁽²⁾ (RFC, RFx)	5MHz		43		dB
	50MHz		43		
	800MHz		30		
	1.2GHz		37		
	2GHz		22		
Isolation ⁽³⁾ (RFC to RF1/RF2/RF3/RF4)	5MHz		68		dB
	50MHz		62		
	800MHz		42		
	1.2GHz		37		
	2GHz		31		
Isolation ⁽³⁾ (RF1/RF2/RF3/RF4)	5MHz		72		dB
	50MHz		65		
	800MHz		43		
	1.2GHz		39		
	2GHz		34		

Notes:

1. Test Conditions Unless Otherwise Specified: $T_A = +25^\circ\text{C}$, $V_{DD} = +3.0\text{V}$, $V_{C1, C2} = 0/+2.5\text{V}$, 75Ω system.
2. Includes series matching. Refer to EVB Schematic on page 5.
3. Average Isolation. Refer to Performance Plots on pg.9 for more detail.

Electrical Specifications (cont'd.)

Parameter	Conditions ⁽¹⁾	Min	Typ	Max	Units
Input IP3	850MHz +12dBm input power per tone, 30MHz tone spacing		82		dBm
Input IP2	850MHz +12dBm input power per tone, 30MHz tone spacing		130.6		
Input 1dB Compression Point	850MHz		40.2		
Input 0.1dB Compression Point	850MHz		34.0		
Switching Speed	10/90% RF		0.6		μs
Switching Speed	50% control to 10/90% RF		1.3		
Turn On Time	Time for VDD = 0V to part ON and RF = 90%		20		
NVG Spurs	F = 1.6MHz		-152		dBm/Hz
	F > 5MHz		-157		
Harmonics-2nd	5 MHz		-76		dBc
	50MHz		-88		
	850MHz		-129		
	1800MHz		-114		
Harmonics-3rd	5MHz		-97		dBc
	50MHz		-110		
	850MHz		-129		
	1800MHz		-110		

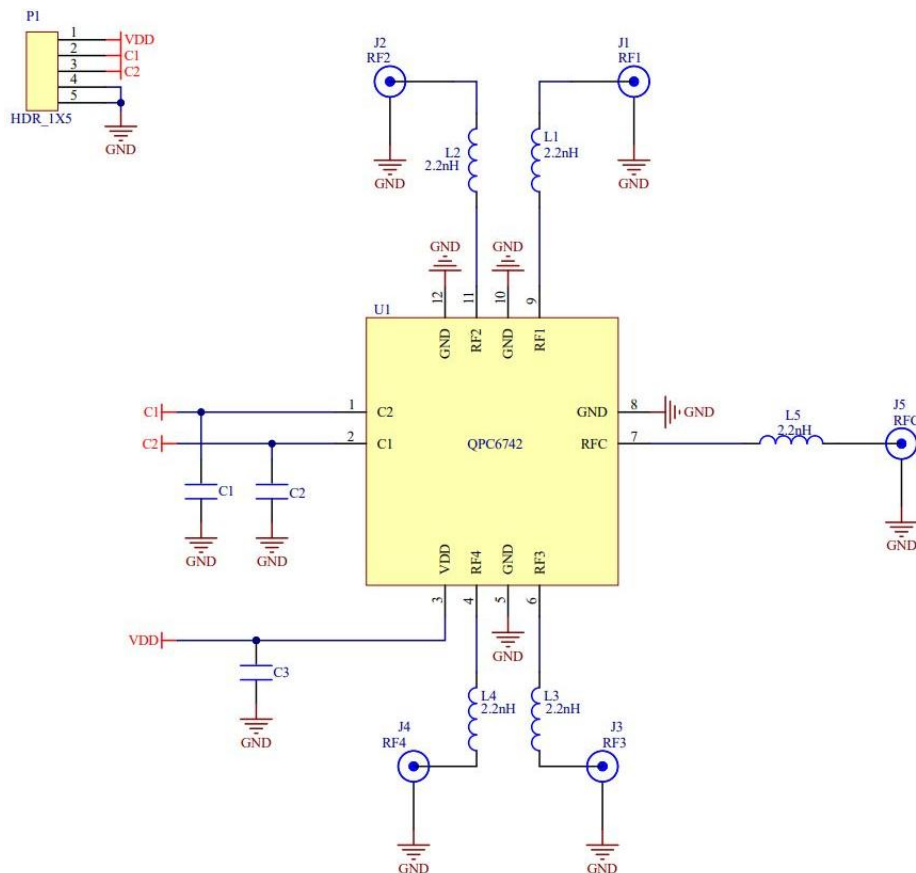
Notes:

1. Test Conditions Unless Otherwise Specified: T_A = +25°C, V_{DD} = +3.0V, V_{CTL} = 0/+2.5V, 75Ω system. Drive RFC, RFx output.
2. V_{DD} = +3.0V, V_{CTL} = 0/+2.5V, 75Ω system.

Electrical Specifications - Power Supply

Parameter	Conditions ⁽¹⁾	Min	Typ	Max	Units
Supply Current (I _{DD})	V _{DD} = +3.0V		65	130	μA
Control Current				5	μA
Control Voltage High		1.3		2.7	V
Control Voltage Low		0		0.45	V

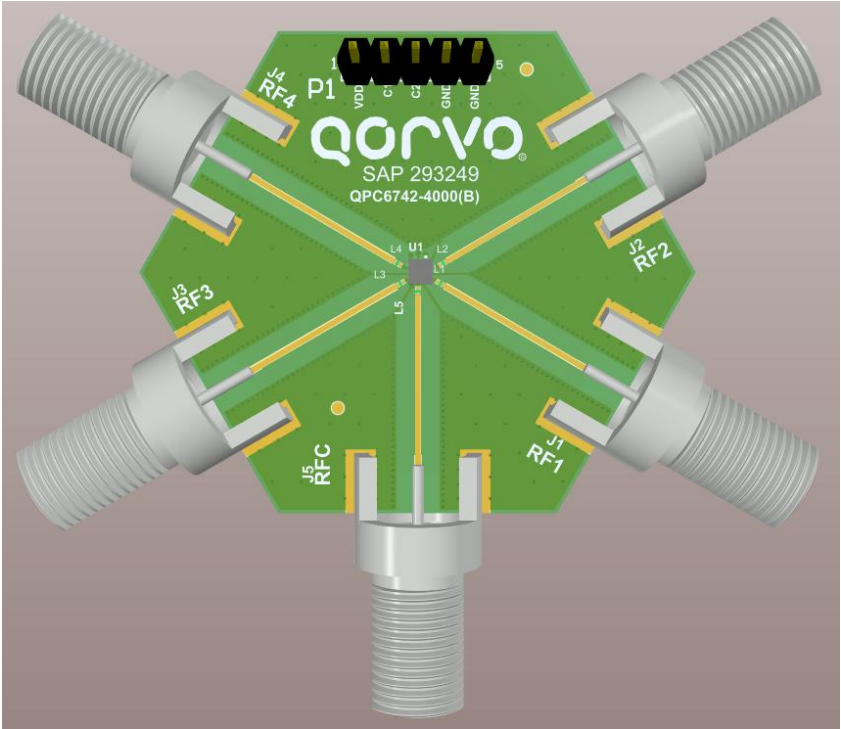
Evaluation Board Schematic



Evaluation Board Bill of Materials

Ref Designator	Qty	Description	Manufacturer	Manufacturer Part #
PCB	1	Evaluation Board PCB	TTM Technologies, Inc.	QPC6742-4000B
U1	1	75ohm SP4T Switch	Qorvo	QPC6742
J1, J2, J3, J4, J5	5	CONN, F FEM EDGE MOUNT, 75 OHMS, 0.068"	Millimeter Wave Technologies	MW-846-C-DD-75
P1	1	CONN, HDR, ST, PLRZD, 5-PIN, 0.100"	ITW Pancon	MPSS100-5-C
C3	1	CAP, 0.1uF, 10%, 16V, X7R, 0402	Kemet	C0402C104K4RACTU
L1, L2, L3, L4, L5	5	IND, 2.2nH, +/-0.2nH, M/L, MID-Q, 0201	TDK	MLG0603PPA2N2CT000
C1, C2		Not Populated	N/A	N/A

Evaluation Board Assembly



Evaluation Board Stackup

Layer Stack Legend				
	Material	Layer	Thickness	Dielectric Material
		Top Overlay		
	Surface Material	Top Solder	0.40mil	Solder Resist
	Copper	Top Layer	0.70mil	
	Core		20.00mil	RO4003C
	Copper	MidLayer1	1.40mil	
	Prepreg		4.22mil	370HR
	Copper	MidLayer2	1.40mil	
	Core		21.00mil	370HR
	Copper	Bottom Layer	0.70mil	
	Surface Material	Bottom Solder	0.40mil	Solder Resist
		Bottom Overlay		
Total thickness:			50.22mil	



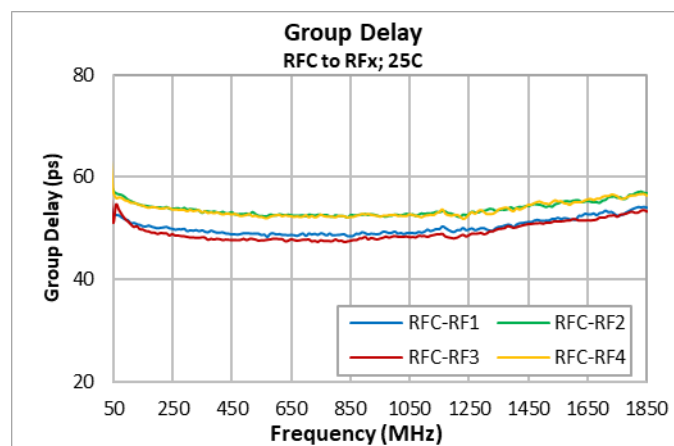
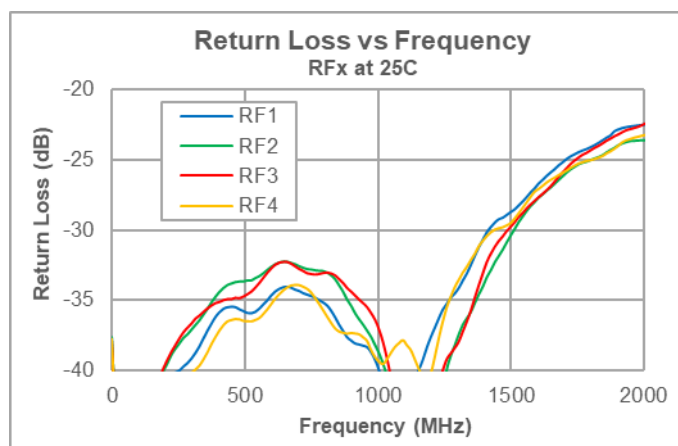
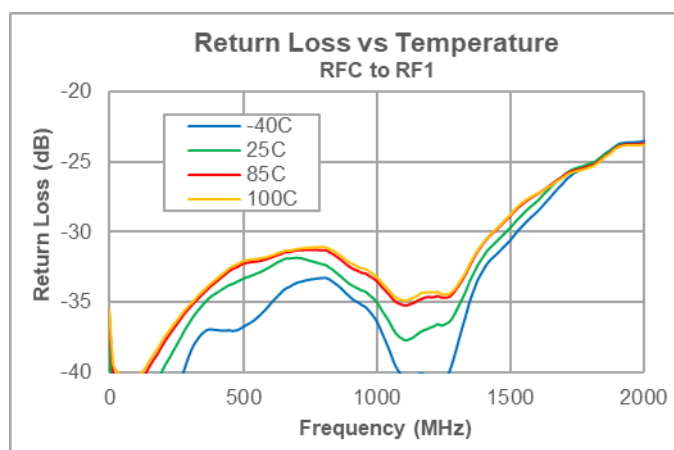
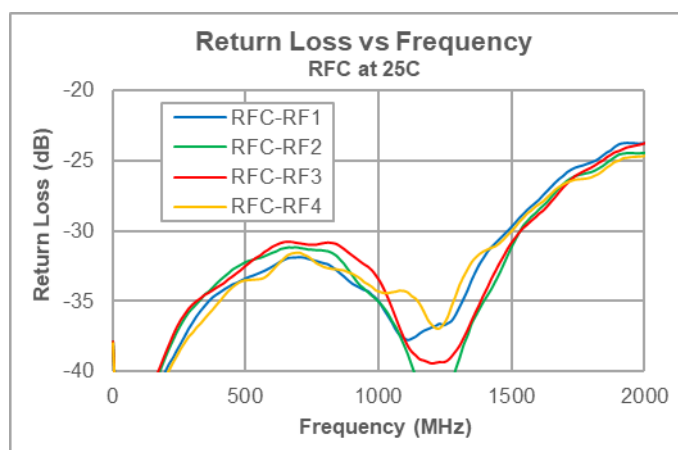
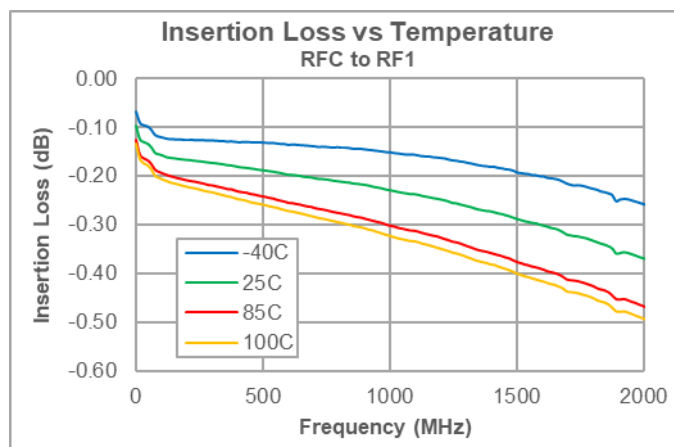
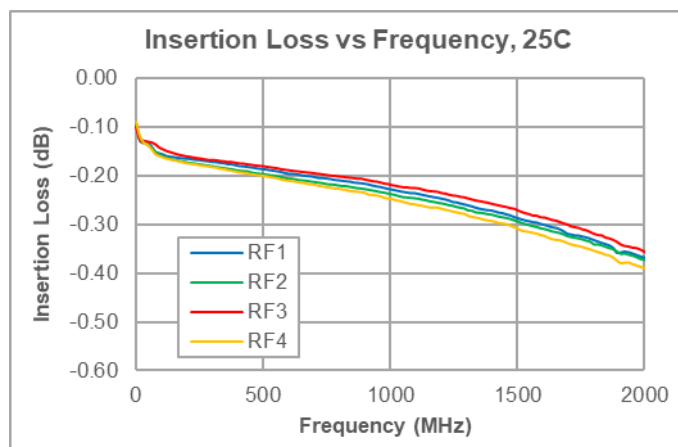
Power Supply Sequencing Requirements

Condition	Sequence
Power Up	Turn on VDD, then C1 and C2, then (20μs or greater), apply RF signal
Power Down	Turn off RF signal, then C1 and C2, turn off VDD

Truth Table

Mode	Control Signals	
	C1	C2
RFC to RF1	High	Low
RFC to RF2	Low	High
RFC to RF3	High	High
RFC to RF4	Low	Low

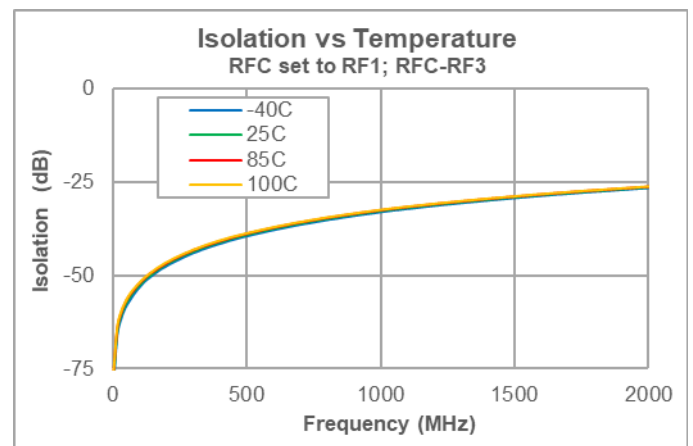
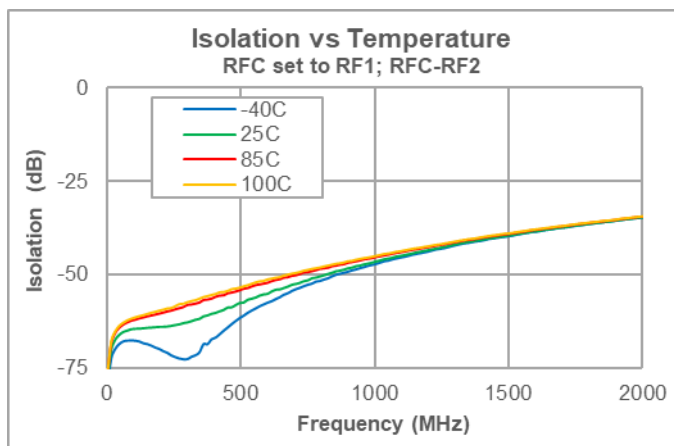
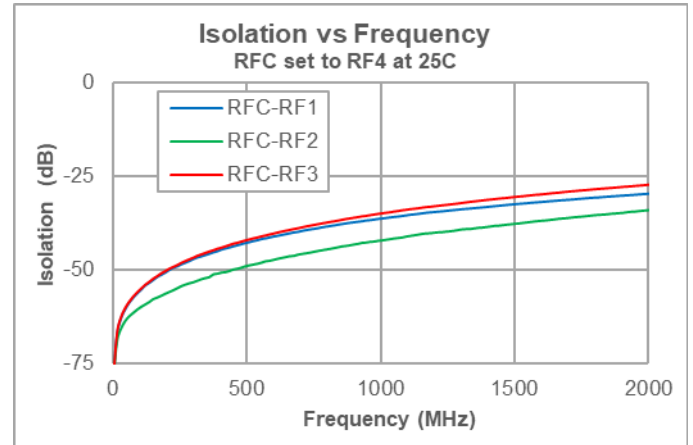
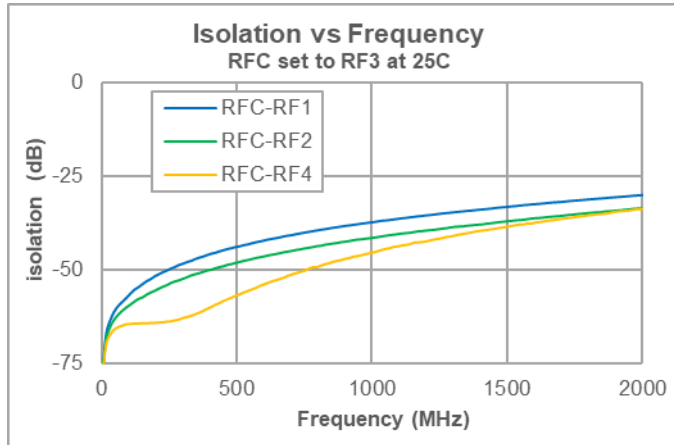
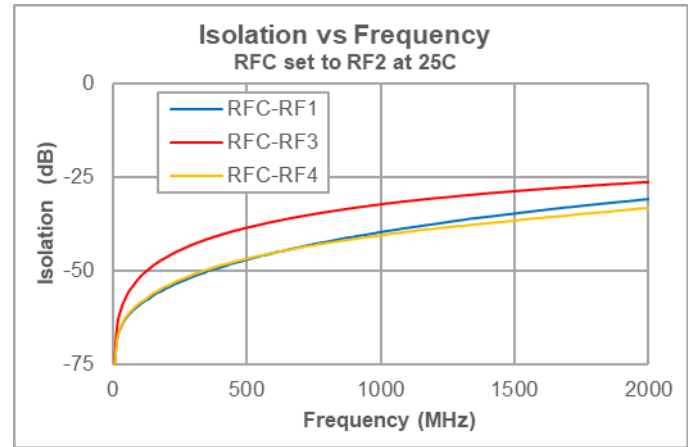
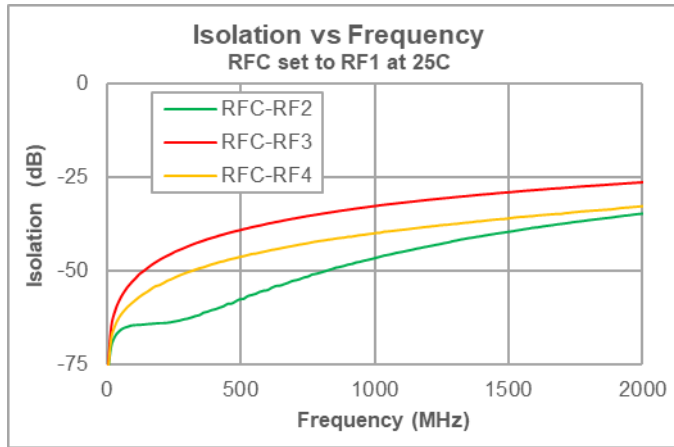
Performance Plots



Notes:

1. VDD = +3.0V, VC2, C1 = 0 / 2.5V, Temp = +25°C, Zo = 75Ω
2. Insertion Loss plots are loss compensated to remove effects of EVB.
3. Group Delay is deembedded to remove effects of EVB and matching elements.

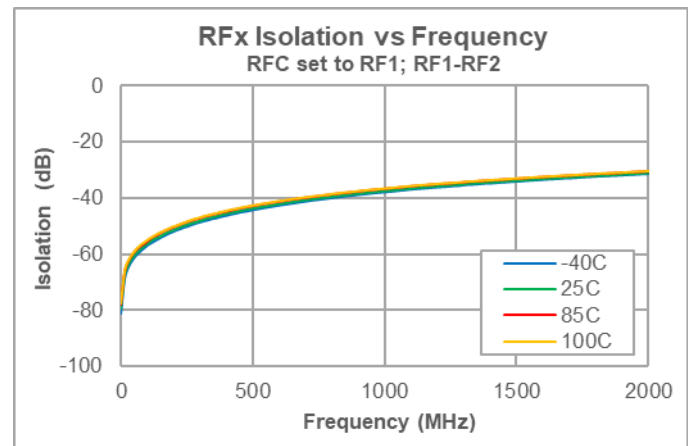
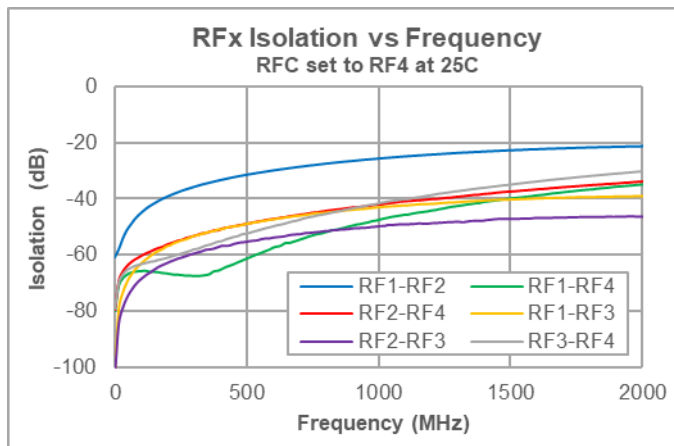
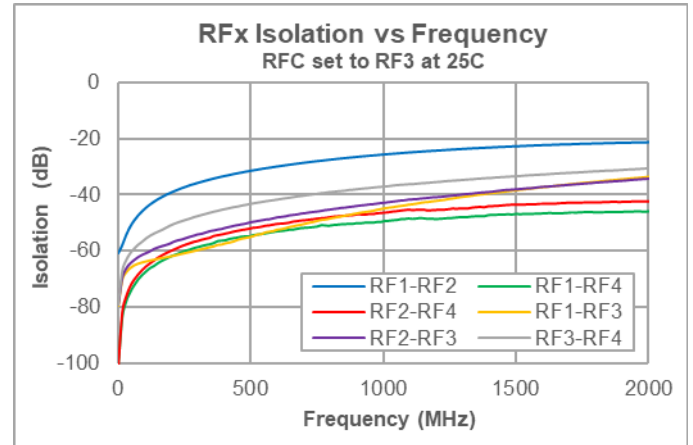
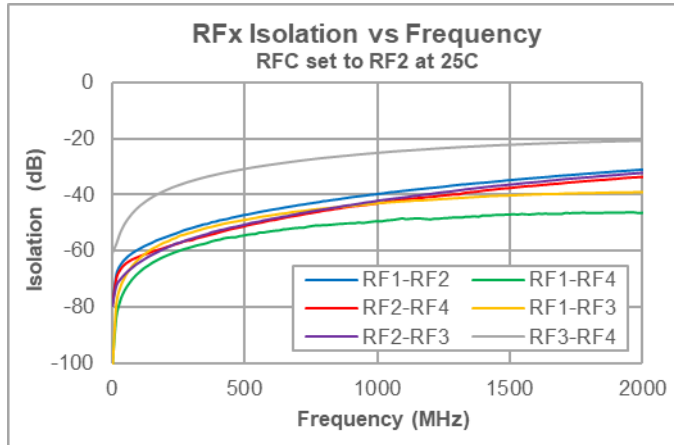
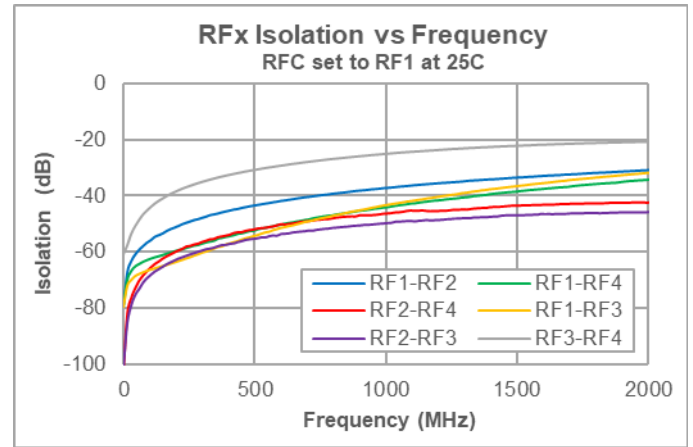
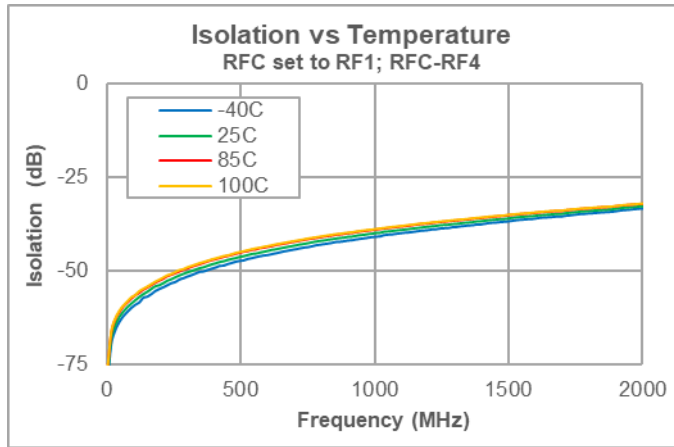
Performance Plots (cont'd.)



Notes:

1. VDD = +3.0V, VC2, C1 = 0 / 2.5V, Temp = +25°C, Zo = 75Ω

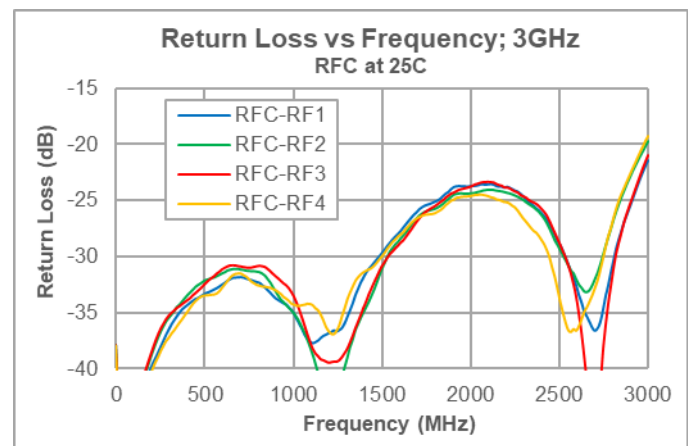
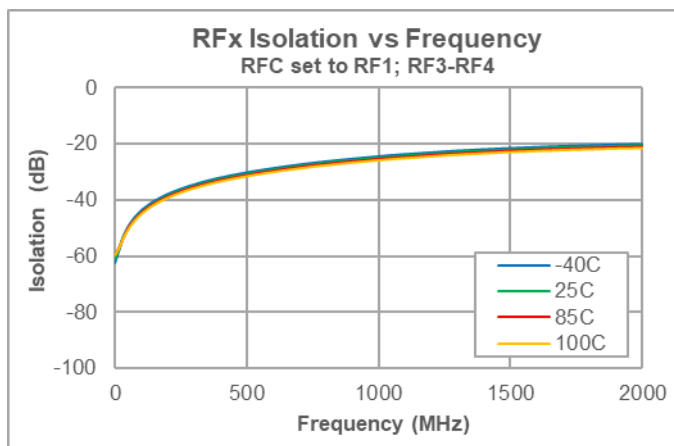
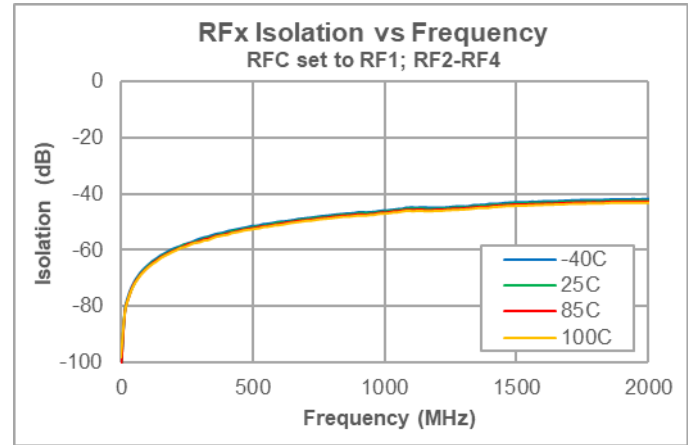
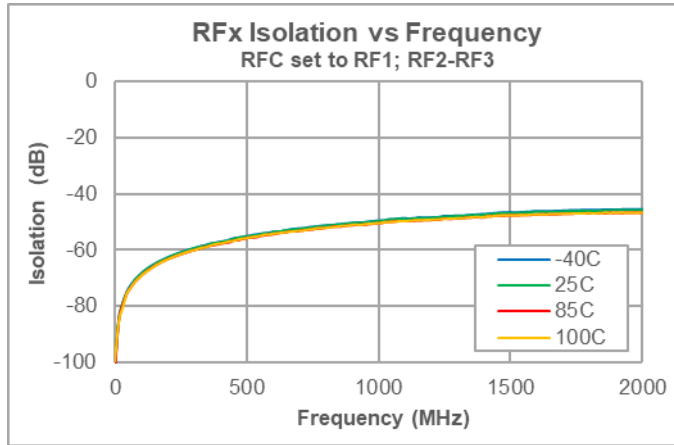
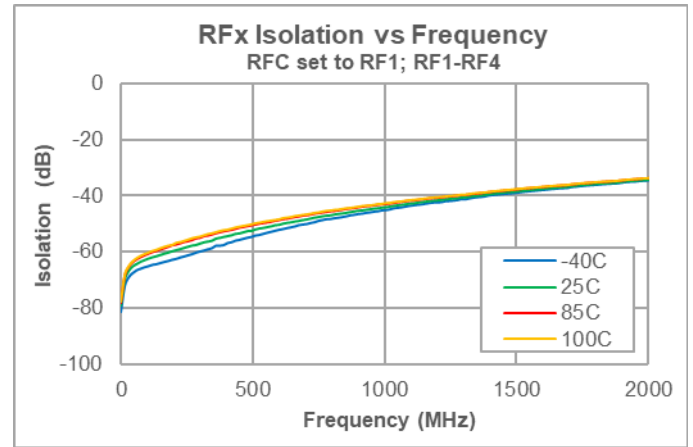
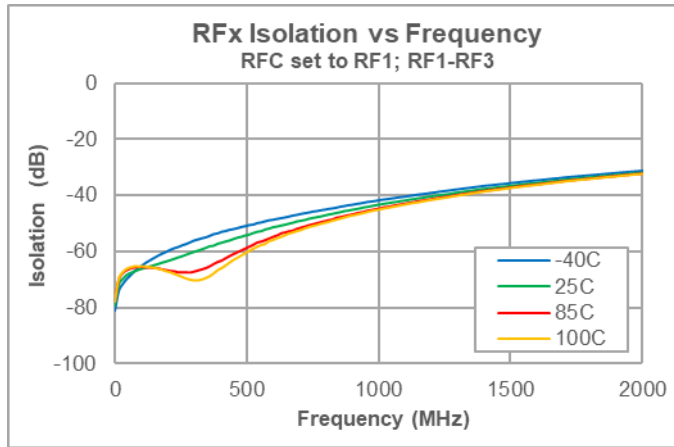
Performance Plots (cont'd.)



Notes:

1. VDD = +3.0V, VC2, C1 = 0 / 2.5V, Temp = +25°C, Zo = 75Ω

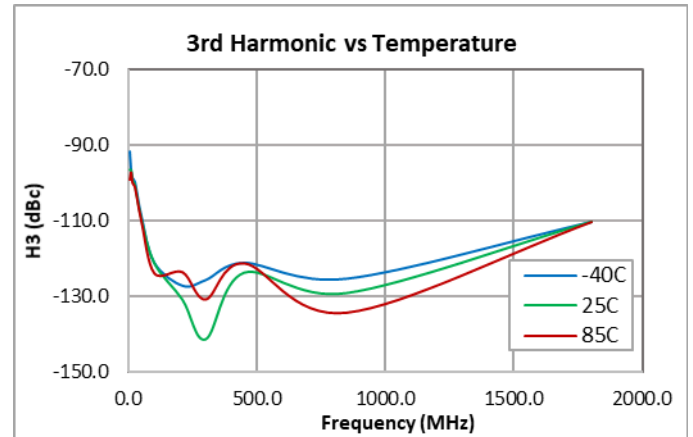
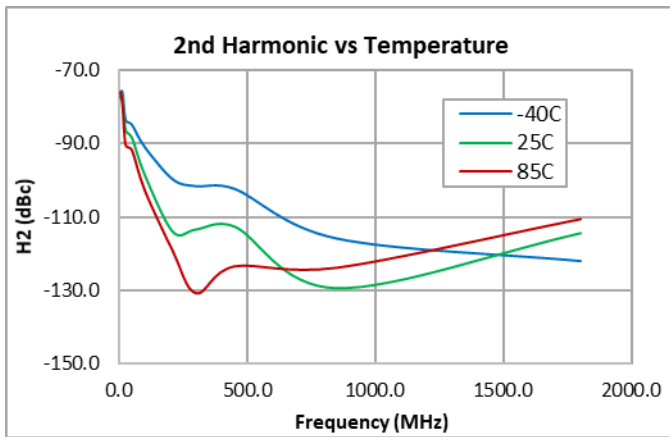
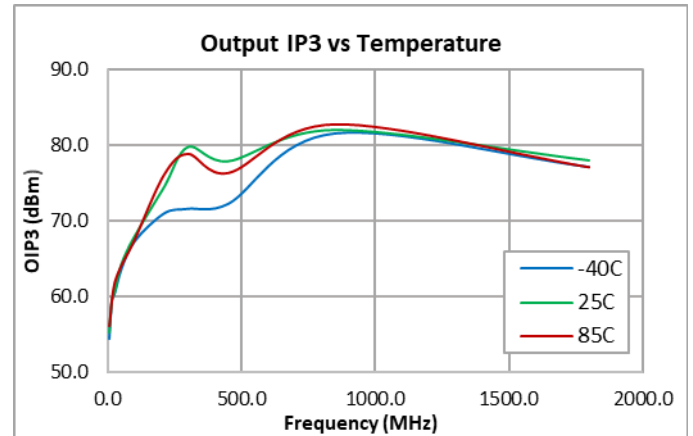
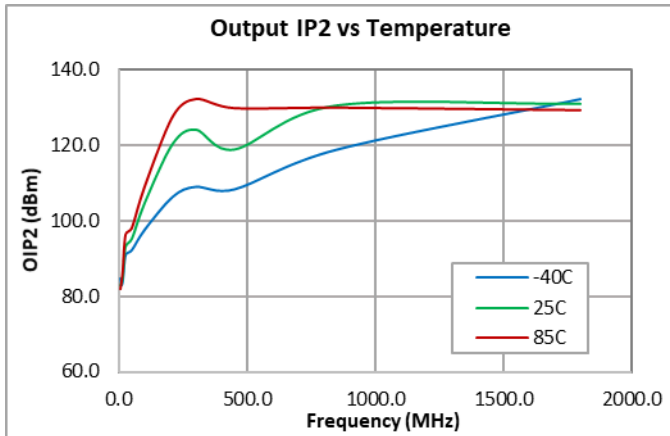
Performance Plots (cont'd.)



Notes:

1. VDD = +3.0V, VC2, C1 = 0 / 2.5V, Temp = +25°C, Zo = 75Ω
2. 3GHz return loss can be recentered by adjusting matching components, L1-L5.

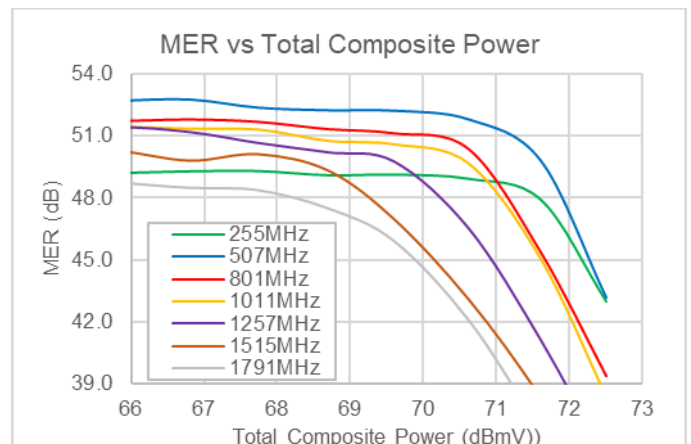
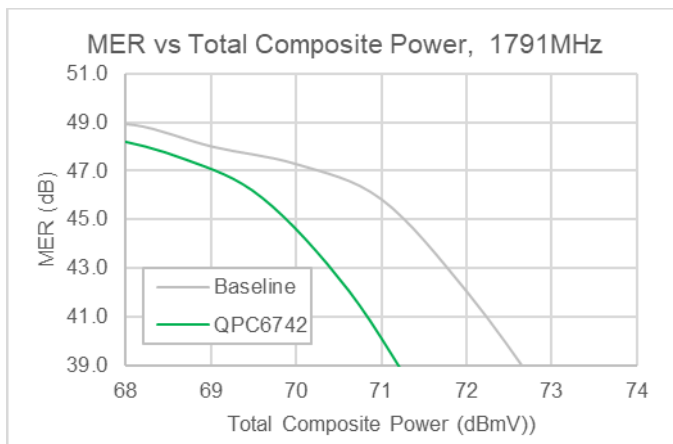
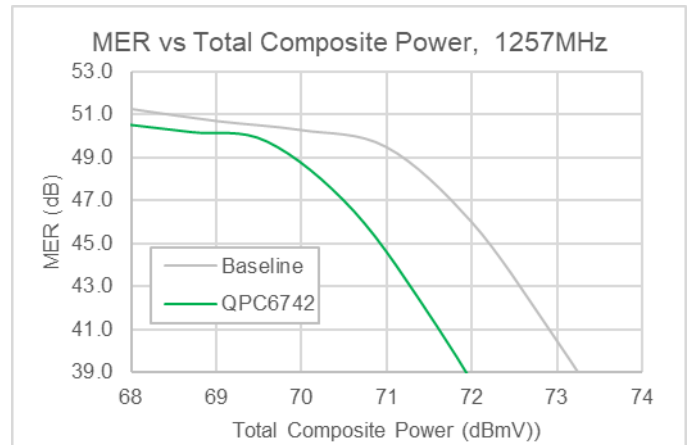
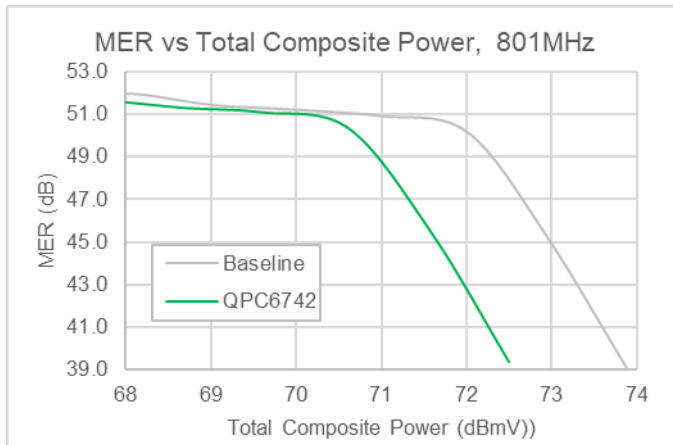
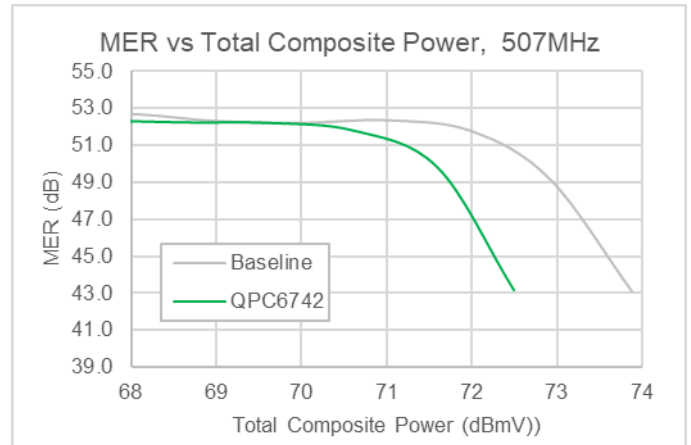
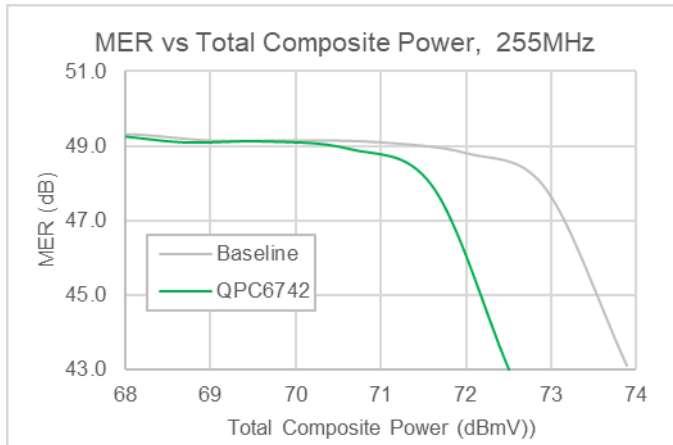
Performance Plots (cont'd.)



Notes:

1. VDD = +3.0V, VC2, C1 = 0 / 2.5V, Temp = +25°C, Zo = 75Ω
2. +12dBm per tone.

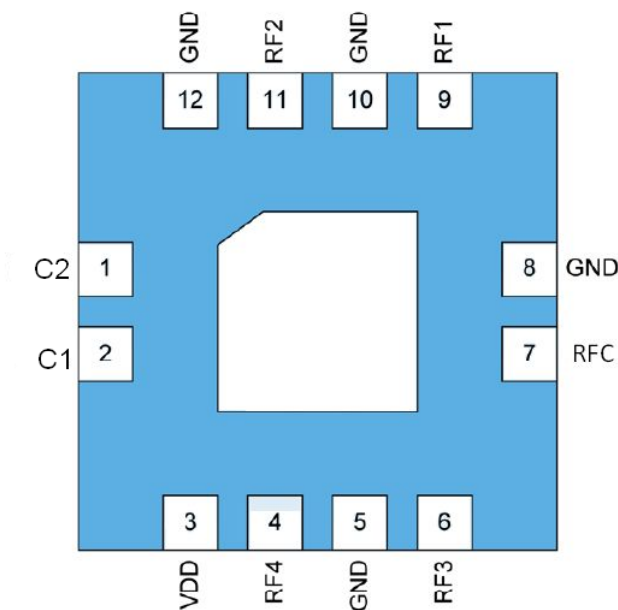
Performance Plots (cont'd.)



MER/CCN Test Conditions:

1. VDD = +5.0V, VC2 = 0 / 2.5V, Temp = +25°C, Zo = 75Ω
2. MER: 280Ch 256QAM ITU-T J.83 Annex B; 108-1791MHz; 0dB Tilt, 0dB Offset. Source Corrected (4.3dB Maximum correction). Baseline represents QPL1823 8V output.

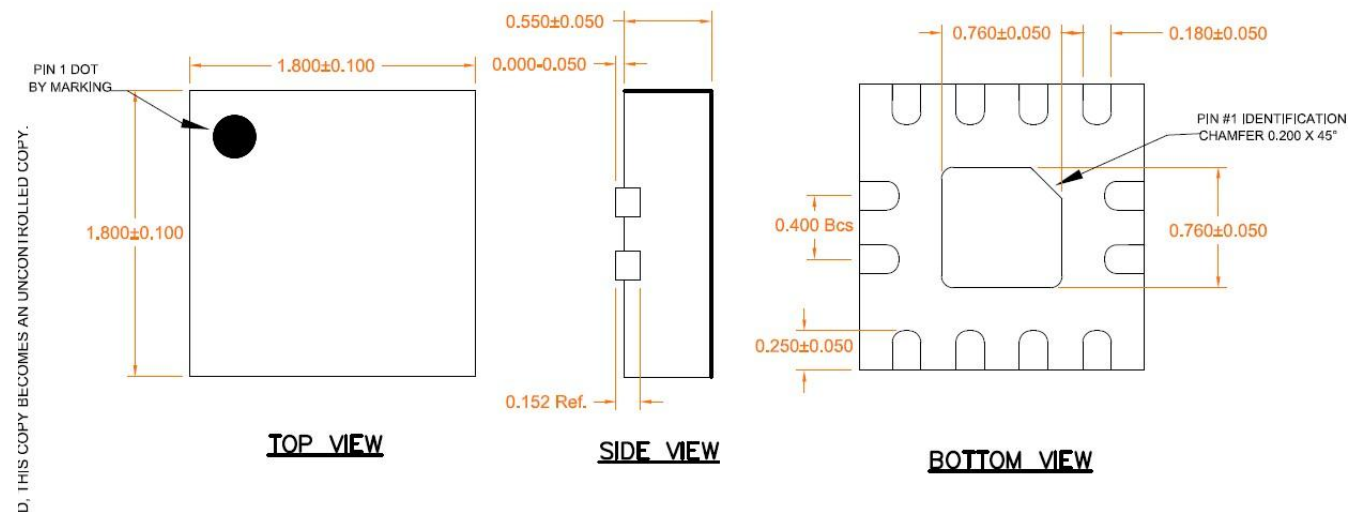
Pin Configuration and Description



Top View

Pad No.	Label	Description
1	C2	Switch Logic Control 2
2	C1	Switch Logic Control 1
3	VDD	Supply Voltage
4	RF4	RF Output Port
5	GND	Ground
6	RF3	RF Output Port
7	RFC	RF Input Port
8	GND	Ground
9	RF1	RF Output Port
10	GND	Ground
11	RF2	RF Output Port
12	GND	Ground

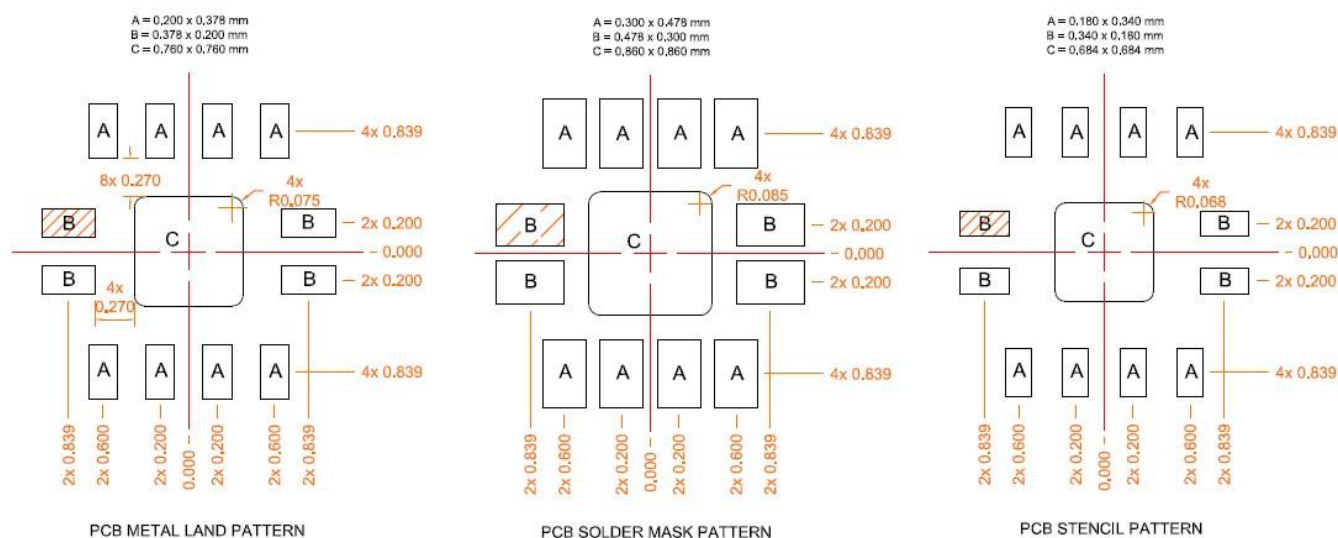
Package Dimensions



Notes:

1. All dimensions are in millimeters. Angles are in degrees.
2. The terminal #1 identifier and terminal numbering conform to JESD 95-1 SPP-012.
3. Contact plating: Matte Sn

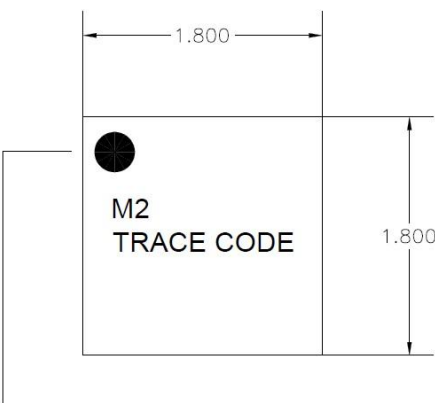
Recommended Mounting Pattern



Notes:

1. All dimensions are in millimeters. Angles are in degrees.

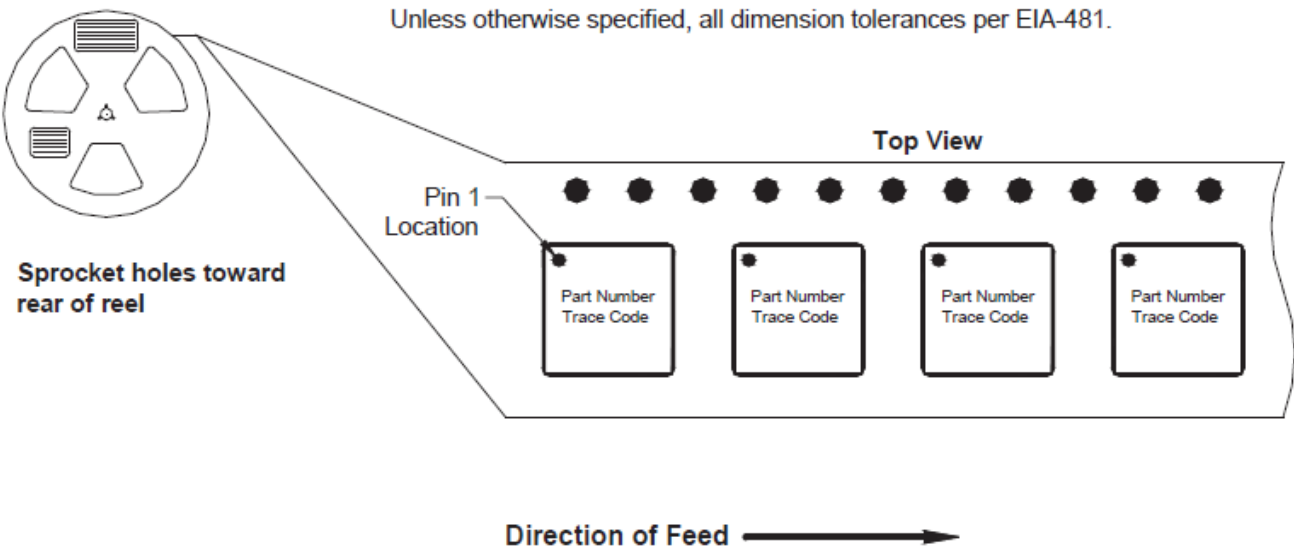
Package Marking



Pin 1 Indicator
Trace Code to be assigned by SubCon

Tape and Reel Information

Qorvo Part Number	Reel Diameter Inch (mm)	Hub Diameter Inch (mm)	Width (mm)	Pocket Pitch (mm)	Feed	Units Per Reel
QPC6742TR7	7 (178)	2.4 (61)	8	4	Single	2500



1.8mm x 1.8mm Carrier Tape Drawing with Part Orientation

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 2 (2000V)	ANSI/ESD/JEDEC JS-001-2010
ESD – Charged Device Model (CDM)	Class C3 (1000V)	JESD22-C101
MSL – Moisture Sensitivity Level	MSL2	JEDEC J-STD-020



Caution!
ESD-Sensitive Device

Solderability

Compatible with both lead-free (260°C max. reflow temp.) and tin/lead (245°C max. reflow temp.) soldering processes.
Solder profiles available upon request.

Contact plating: Matte Sn

RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- SVHC Free



Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

Web: www.qorvo.com

Tel: +1 844-890-8163

Email: customer.support@qorvo.com



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