

NB3W800LMNGEVB

NB3W800LMNGEVB GUI Evaluation Board User's Manual



ON Semiconductor®

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EVAL BOARD USER'S MANUAL

Devices Supported:

NB3W800L (QFN48)

Introduction

The NB3W800L is a low-power 8-output differential buffer that meets all the performance requirements of the DB800ZL specification. The NB3W800L is capable of distributing the reference clocks for Intel® QuickPath Interconnect (Intel QPI), PCIe Gen1/Gen2/Gen3, SAS, SATA, and Intel Scalable Memory Interconnect (Intel SMI) applications. A fixed, internal feedback path maintains low drift for critical QPI applications.

ON Semiconductor has developed a GUI that can be used with the device Eval Board NB3W800LMNGEVB to control NB3W800L device register parameters. Its operation is covered in this manual.

Software Installation

- Unzip the distribution archive “DB800_GUI_revC.zip”
 - ♦ All files are contained in the parent folder DB800_GUI_revC which you can un-zip anywhere on your PC

- Look in the parent folder
 - ♦ You will see a file “NB3W800L_Programming_GUI.exe”
- Make a short cut to that file and place it on your desktop, start menu etc.
- That's it
 - ♦ There is no manipulation of the registry or path variables
 - ♦ To un-install just delete the files

Software Use and Initialization

- Connect the Eval Board NB3W800LMNGEVB to a USB port of a PC
- Allow Windows® to install the necessary drivers for the Evaluation board USB interface hardware .. it will go out to the web to find them
- Start the program using the short cut you made earlier

NB3W800LMNGEVB

SMBus Activities

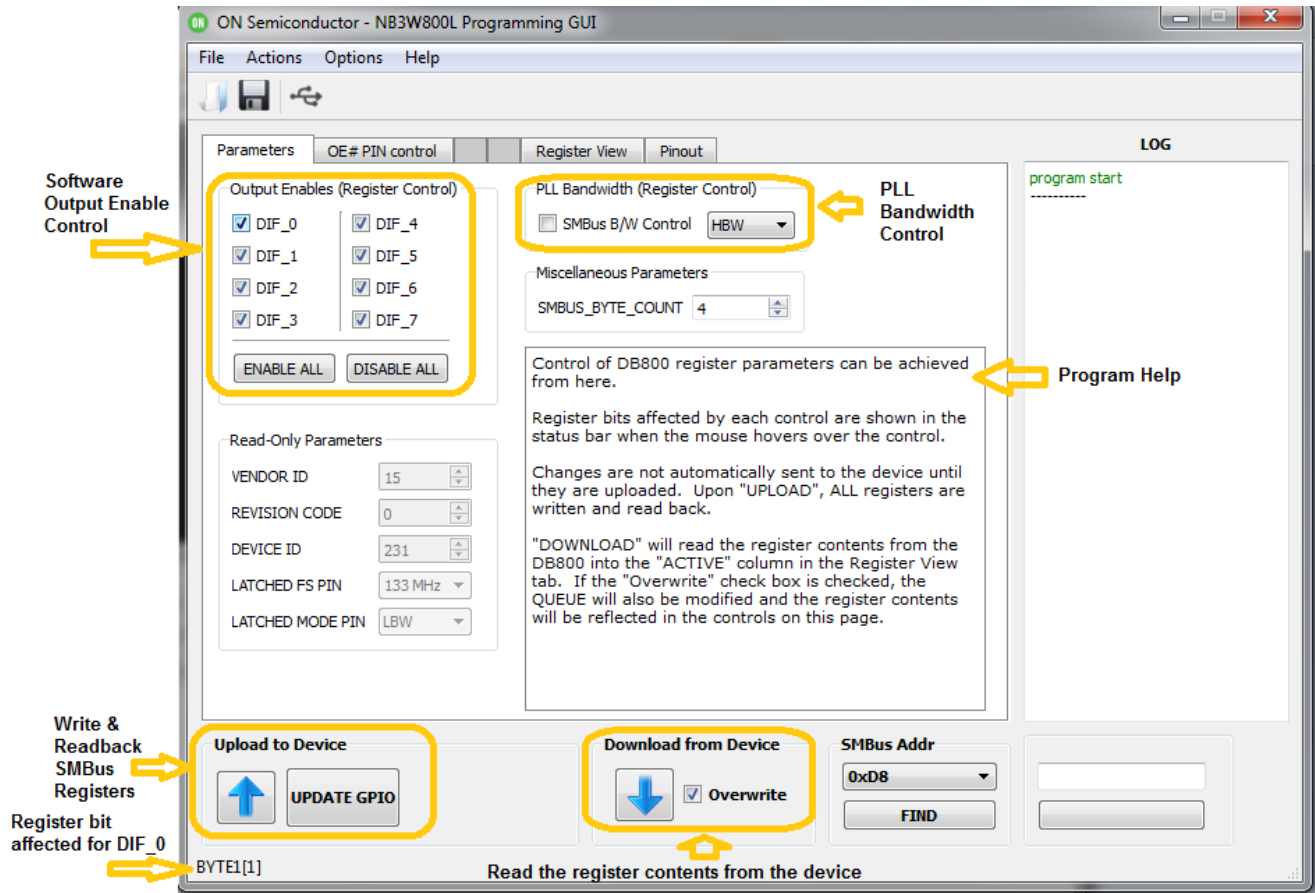


Figure 1.

NB3W800LMNGEVB

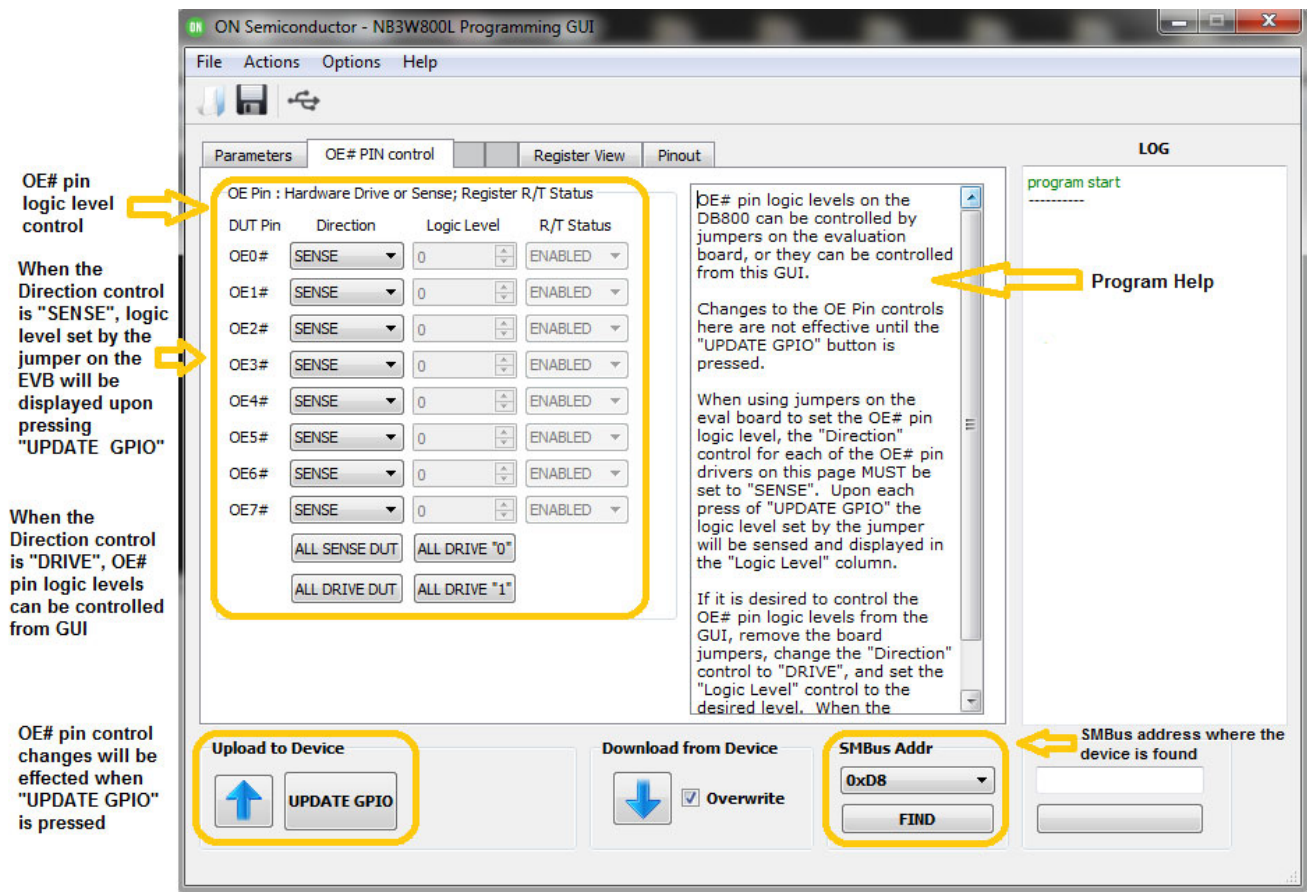


Figure 2.

Menu Options

- File Menu

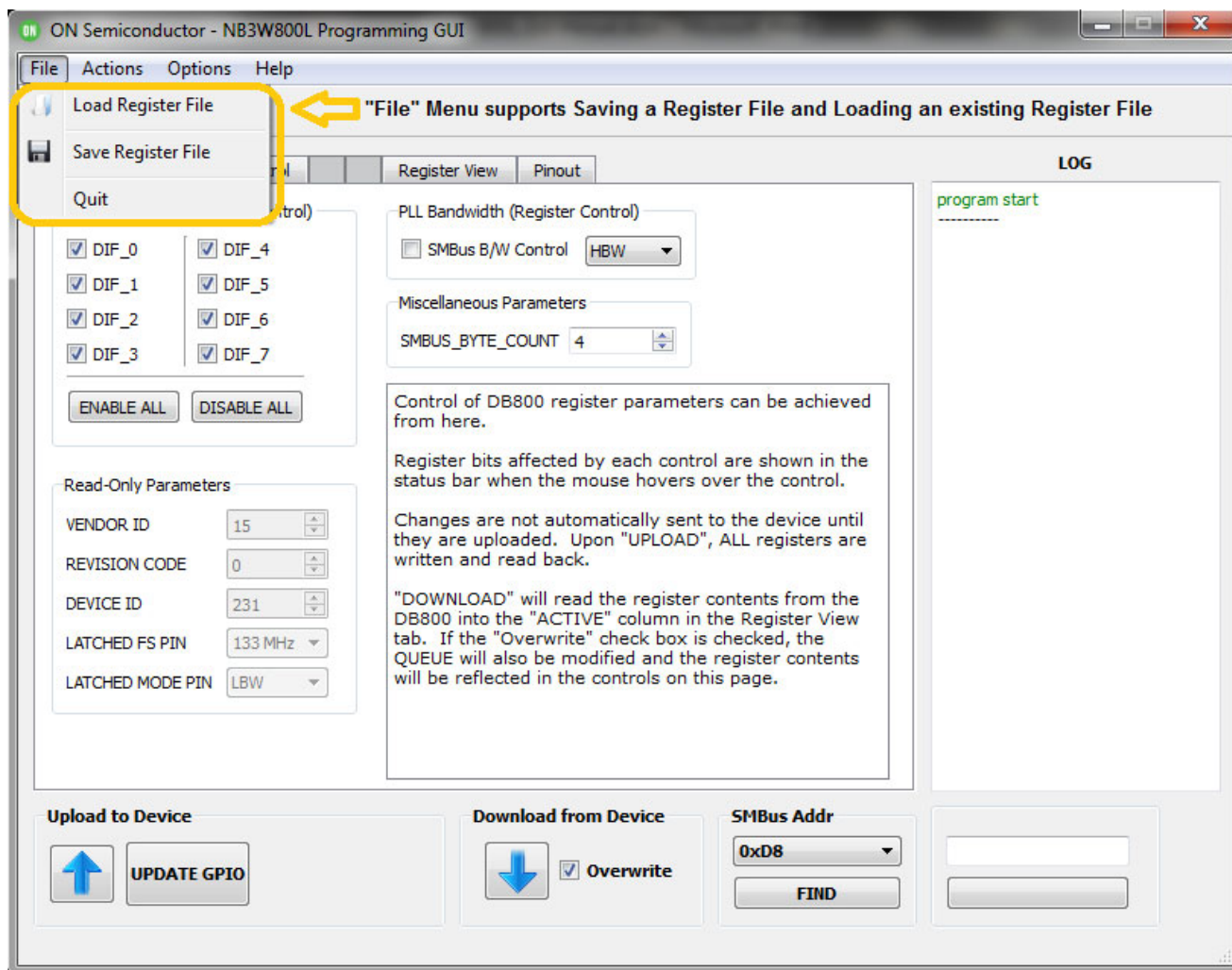


Figure 3.File Menu

NB3W800LMNGEVB

- Actions Menu

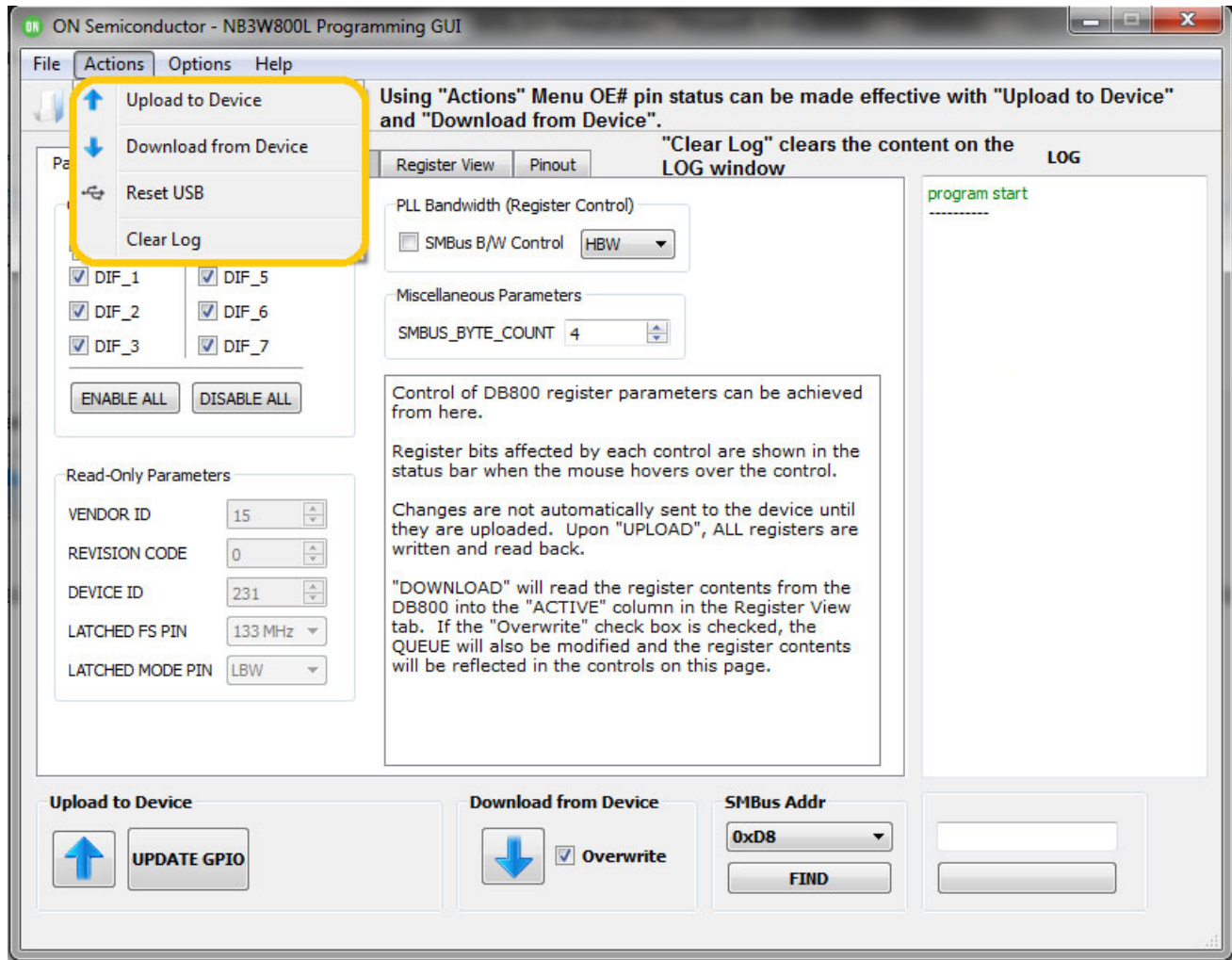


Figure 4.Actions Menu

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